



PRODUCT BULLETIN

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ISSUE DATE: 03-Jun-2013
NOTIFICATION: 15657
TITLE: MPC5534_53_65_66_67 ERRATA REPORT UPDATES
EFFECTIVE DATE: 04-Jun-2013

DEVICE(S)

MPN
MPC5534MVM80
MPC5534MZQ80
MPC5553MVR132
MPC5553MVZ132
MPC5553MZP132
MPC5553MZP80
MPC5553MZQ132
MPC5565MVZ132
MPC5566MVR132
MPC5566MZP132
MPC5566MZP144
MPC5567MVR132
MPC5567MVR80
SC5567MVR132
SCW5566
SPC5533MVF80R
SPC5533MVM80R
SPC5533MVZ80R
SPC5534MVF80R
SPC5534MVM80
SPC5534MVM80R
SPC5534MVZ66
SPC5534MVZ80
SPC5534MVZ80R
SPC5534MZQ80
SPC5534MZQ80R
SPC5553MVF132

SPC5553MVZ132
SPC5553MVZ80
SPC5553MZP132R
SPC5553MZQ112
SPC5553MZQ132
SPC5553MZQ132R
SPC5553MZQ80
SPC5553MZQ80R
SPC5565MVZ132
SPC5565MVZ132R
SPC5565MZQ132R
SPC5566MVR132
SPC5566MVR132R
SPC5566MVR144
SPC5566MZP112R
SPC5566MZP132
SPC5566MZP132R
SPC5566MZP144
SPC5566MZP144R
SPC5566MZP80
SPC5566MZP80R
SPC5567MVR132
SPC5567MVR80R
SPC5567MVZ132
SPC5567MZP132
SPC5567MZQ132
SPC5567MZQ132R
SPC5567MZQ80R

AFFECTED CHANGE CATEGORIES

- DEVICE SPECIFICATION / ERRATA

DESCRIPTION OF CHANGE

The errata documentation for MPC5534, MPC5553, MPC5565, MPC5566 and MPC5567 has been updated to remove erroneous errata and to add additional errata that have been identified.

For MPC5534, the following errata updates have been made:

Errata removed:

- 1419 SIU: Reverting ENGCLK source to the system clock has a very small chance of causing the ENGCLK generator to enter an indeterminate state

Errata added:

- 6531 FMPLL: Selecting GPIO mode on RSTCFG/PLLCFG[0:1] may cause PLL failure after a reset
- 6726 NPC: MCKO clock may be gated one clock period early when MCKO frequency is programmed as SYS_CLK/8.and gating is enabled

The updated errata documentation is attached to this notice and can be found at:

http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=MPC5534&nodeId=01624606C10325&fpSP=1&tab=Documentation_Tab

For MPC5553, the following errata updates have been made:

Errata removed:

- 456 Pad Ring: Additional DSPI CS Pin Multiplexing options are not available
- 1419 SIU: Reverting ENGCLK source to the system clock has a very small chance of causing the ENGCLK generator to enter an indeterminate state

Errata added:

- 2043 eDMA: Unexpected start of channel 15 during a spurious preemption
- 2455 FEC: Fast Ethernet Controller (FEC) start-up issue
- 3421 e200z: Cache returns value, even when disabled
- 6531 FMPLL: Selecting GPIO mode on RSTCFG/PLLCFG[0:1] may cause PLL failure after a reset
- 6726 NPC: MCKO clock may be gated one clock period early when MCKO frequency is programmed as SYS_CLK/8.and gating is enabled

The updated errata documentation is attached to this notice and can be found at:

http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=MPC5553&nodeId=01624606C10325&fpSP=1&tab=Documentation_Tab

For MPC5565, the following errata updates have been made:

Errata removed:

- 1419 SIU: Reverting ENGCLK source to the system clock has a very small chance of causing the ENGCLK generator to enter an indeterminate state

Errata added:

- 3421 e200z: Cache returns value, even when disabled

- 6531 FMPLL: Selecting GPIO mode on RSTCFG/PLLCFG[0:1] may cause PLL failure after a reset
- 6726 NPC: MCKO clock may be gated one clock period early when MCKO frequency is programmed as SYS_CLK/8.and gating is enabled

The updated errata documentation is attached to this notice and can be found at:

http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=MPC5565&nodeId=01624606C10325&fosp=1&tab=Documentation_Tab

For MPC5566, the following errata updates have been made:

Errata removed:

- 1419 SIU: Reverting ENGCLK source to the system clock has a very small chance of causing the ENGCLK generator to enter an indeterminate state

Errata added:

- 2455 FEC: Fast Ethernet Controller (FEC) start-up issue
- 3421 e200z: Cache returns value, even when disabled
- 6531 FMPLL: Selecting GPIO mode on RSTCFG/PLLCFG[0:1] may cause PLL failure after a reset
- 6726 NPC: MCKO clock may be gated one clock period early when MCKO frequency is programmed as SYS_CLK/8.and gating is enabled

The updated errata documentation is attached to this notice and can be found at:

http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=MPC5566&nodeId=01624606C10325&fosp=1&tab=Documentation_Tab

For MPC5567, the following errata updates have been made:

Errata removed:

- 1419 SIU: Reverting ENGCLK source to the system clock has a very small chance of causing the ENGCLK generator to enter an indeterminate state

Errata added:

- 1429 Flexray : No reception in slot in dynamic segment
- 2344 FlexRay: Repeated wakeup after leading coldstart has too short WAKEUP_LISTEN phase
- 2455 FEC: Fast Ethernet Controller (FEC) start-up issue
- 3421 e200z: Cache returns value, even when disabled
- 3553 NXFR: Flexray databus translates into unexpected data format on the Nexus interface
- 6531 FMPLL: Selecting GPIO mode on RSTCFG/PLLCFG[0:1] may cause PLL failure after a reset
- 6726 NPC: MCKO clock may be gated one clock period early when MCKO frequency is

programmed as SYS_CLK/8.and gating is enabled

The updated errata documentation is attached to this notice and can be found at:

http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=MPC5567&nodeId=01624606C10325&fsp=1&tab=Documentation_Tab

REASON FOR CHANGE

The errata documentation for MPC5534, MPC5553, MPC5565, MPC5566 and MPC5567 has been updated.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No changes have been made to the current production devices. The errata describe existing conditions identified on current production devices. There are potential hardware and/or software implications to customers.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 01-May-2013

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15657 MPC5534 53 65 66 67 errata reports.pdf](#)
