

**PRODUCT BULLETIN**

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**ISSUE DATE:** 25-Jul-2013  
**NOTIFICATION:** 15786  
**TITLE:** MPC5554 & MPC5561 ERRATA REPORT UPDATE  
**EFFECTIVE DATE:** 26-Jul-2013

**DEVICE(S)**

MPN
MPC5554AZP132
MPC5554MVR132
MPC5554MVR132R2
MPC5554MVR80
MPC5554MVRX1
MPC5554MVRX2
MPC5554MVRXXX
MPC5554MZP112
MPC5554MZP112R2
MPC5554MZP132
MPC5554MZP132R2
MPC5554MZP80
MPC5554MZP80R2
MPC5554MZPJ1
MPC5554MZPJ2
MPC5554MZPJ3
MPC5554MZPT1
MPC5554MZPT2
MPC5554MZPT3
MPC5554MZPX1
MPC5554MZPX2
MPC5554MZPX3
MPC5561MZQX1
MPC5561MZQX2
MPC5561MZQX3
SC5554MVRX1
SC5554MVRX2

SC5554MVRX3
SC5554MZP112
SC5554MZP112R2
SC5554MZP132
SC5554MZPX1
SC5554MZPX2
SC5554MZPX3
SPC5554MVR132
SPC5561F1MVZ132
SPC5561F1MVZ132R
SPC5561F1MVZX1
SPC5561F1MVZX2
SPC5561F1MVZX3
SPC5561F1MZQ132
SPC5561F1MZQ132R
SPC5561F1MZQX1
SPC5561F1MZQX2
SPC5561F1MZQX3
SPC5561F2MVZ132
SPC5561F2MVZ132R
SPC5561F2MZQ132
SPC5561F2MZQ132R

## **AFFECTED CHANGE CATEGORIES**

- ERRATA

## **DESCRIPTION OF CHANGE**

The MPC5554 errata documentation has been updated to include additional errata that have been identified and to remove errata that do not apply to the device.

### MPC5554 errata added:

2043 eDMA: Unexpected start of channel 15 during a spurious preemption

3421 e200z: Cache returns value, even when disabled

6531 FMPLL: Selecting GPIO mode on RSTCFG/PLLCFG[0:1] may cause PLL failure after a reset

6726 NPC: MCKO clock may be gated one clock period early when MCKO frequency is programmed as SYS\_CLK/8.and gating is enabled

### MPC5554 errata removed:

1419 SIU: Reverting ENGCLK source to the system clock has a very small chance of causing the ENGCLK generator to enter an indeterminate state

The updated MPC5554 errata documentation can be found here:

[http://www.freescale.com/webapp/sps/site/prod\\_summary.jsp?code=MPC5554&nodeId=01624606C10325&fosp=1&tab=Documentation\\_Tab](http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=MPC5554&nodeId=01624606C10325&fosp=1&tab=Documentation_Tab)

The MPC5561 errata documentation has been updated to include additional errata that have been identified.

### MPC5561 errata added:

1429 Flexray : No reception in slot in dynamic segment

2344 FlexRay: Repeated wakeup after leading cold start has too short WAKEUP\_LISTEN phase

3421 e200z: Cache returns value, even when disabled

6531 FMPLL: Selecting GPIO mode on RSTCFG/PLLCFG[0:1] may cause PLL failure after a reset

6726 NPC: MCKO clock may be gated one clock period early when MCKO frequency is programmed as SYS\_CLK/8.and gating is enabled

6966 eDMA: Possible misbehavior of a preempted channel when using continuous link mode

The updated MPC5561 errata documentation can be found here:

[http://www.freescale.com/webapp/sps/site/prod\\_summary.jsp?code=MPC5561&nodeId=01624606C10325&fosp=1&tab=Documentation\\_Tab](http://www.freescale.com/webapp/sps/site/prod_summary.jsp?code=MPC5561&nodeId=01624606C10325&fosp=1&tab=Documentation_Tab)

## **REASON FOR CHANGE**

The errata documentation for MPC5554 and MPC5561 has been updated.

## **ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)**

No changes have been made to the current production devices. The errata describe existing conditions identified on current production devices. There are potential hardware and/or software implications to customers.

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**NOTE:**

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

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**QUAL DATA AVAILABILITY DATE:** 17-Jul-2013

**QUALIFICATION STATUS:** COMPLETED

**QUALIFICATION PLAN:**

N/A

**RELIABILITY DATA SUMMARY:**

N/A

**ELECTRICAL CHARACTERISTIC SUMMARY:**

N/A

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**CHANGED PART IDENTIFICATION:**

N/A

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**ATTACHMENT(S):**

External attachment(s) FOR this notification can be viewed AT:

[15786\\_MPC5554REVB\\_E.pdf](#)

[15786\\_MPC5561ACE.pdf](#)

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