



PRODUCT BULLETIN

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ISSUE DATE: 21-Nov-2013
NOTIFICATION: 15946
TITLE: Kinetis K-Series 120MHz Maskset 2N03G Errata Update
EFFECTIVE DATE: 22-Nov-2013

DEVICE(S)

MPN
MK21FN1M0VLQ12
MK21FN1M0VMC12
MK21FN1M0VMD12
MK21FX512VLQ12
MK21FX512VMC12
MK21FX512VMD12
MK22FN1M0VLH12
MK22FN1M0VLK12
MK22FN1M0VLL12
MK22FN1M0VLQ12
MK22FN1M0VMC12
MK22FN1M0VMD12
MK22FX512VLH12
MK22FX512VLK12
MK22FX512VLL12
MK22FX512VLQ12
MK22FX512VMC12
MK22FX512VMD12
PK21FN1M0VMC10
PK22FN1M0VDC12
PK22FN1M0VLH10
PK22FN1M0VLK10
PK22FN1M0VLL10
PK22FN1M0VLQ10
PK22FN1M0VMC10

AFFECTED CHANGE CATEGORIES

- ERRATA

DESCRIPTION OF CHANGE

The Kinetis 120 MHz devices (2N03G mask set) errata documentation has been updated to include additional errata that have been identified. The updated version has been added to the freescale.com website at:

http://cache.freescale.com/files/microcontrollers/doc/errata/KINETIS_2N03G.pdf

- 6804 CJTAG: Performing a mode change from Standard Protocol to Advanced Protocol may reset the CJTAG.
- 6939 Core: Interrupted loads to SP can cause erroneous behavior
- 6940 Core: VDIV or VSQRT instructions might not complete correctly when very short ISRs are used
- 4710 FTM: FTMx_PWMLoad register does not support 8-/16-bit accesses
- 6573 JTAG: JTAG TDO function on the PTA2 disables the pull resistor
- 7214 Low Leakage Stop (LLS) mode non-functional
- 7027 UART: During ISO-7816 T=0 initial character detection invalid initial characters are stored in the RxFIFO
- 7028 UART: During ISO-7816 initial character detection the parity, framing, and noise error flags can set
- 6472 UART: ETU compensation needed for ISO-7816 wait time (WT) and block wait time (BWT)
- 7029 UART: In ISO-7816 T=1 mode, CWT interrupts assert at both character and block boundaries
- 7090 UART: In ISO-7816 mode, timer interrupts flags do not clear
- 7031 UART: In single wire receive mode UART will attempt to transmit if data is written to UART_D
- 7091 UART: UART_S1[NF] and UART_S1[PE] can set erroneously while UART_S1[FE] is set
- 7092 UART: UART_S1[TC] is not cleared by queuing a preamble or break character
- 6933 eDMA: Possible misbehavior of a preempted channel when using continuous link mode

REASON FOR CHANGE

The errata documentation for the Kinetis 120MHz (2N03G mask set) has been updated.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

There have been no changes made to the current production device. The errata describe existing conditions identified on current production devices. There are potential software and/or hardware implications to customers.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification,

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For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 07-Nov-2013

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[15946_KINETIS_2N03G.pdf](#)
