PRODUCT BULLETIN
Generic Copy

ISSUE DATE: 11-Dec-2013

NOTIFICATION: 15969

TITLE: I.MX 6SOLOLITE DOCUMENTATION UPDATE

EFFECTIVE DATE: 12-Dec-2013

DEVICE(S)

MPN
MCIMX6L2DVN10AA
MCIMX6L2DVN10AB
MCIMX6L2EVN10AA
MCIMX6L2EVN10AB
MCIMX6L3DVN10AA
MCIMX6L3DVN10AB
MCIMX6L3EVN10AA
MCIMX6L3EVN10AB
MCIMX6L7DVN10AA
MCIMX6L7DVN10AB
MCIMX6L7DVN10SA
MCIMX6L8DVN10AA
MCIMX6L8DVN10AB

AFFECTED CHANGE CATEGORIES

- ERRATA

DESCRIPTION OF CHANGE

Silicon Errata:
The silicon errata document has been updated to revision 3 for the i.MX 6SoloLite product lines. The updated errata can be found at: IMX6SLCE

Substantive changes in the errata are as follows:
• Added the following errata:
– ERR007007 "ARM/MP: 794073 -- Speculative instruction fetches with MMU disabled might not comply with architectural requirements"
– ERR007008 "ARM/MP: 794074 --A write request to Uncacheable Shareable memory region might be executed twice"

– ERR007265 "CCM: When improper low-power sequence is used, the SoC enters low power mode before the ARM core executes WFI"

– ERR007266 "ROM: EIM NOR boot may fail if plug-in is used"

• Updated the following errata:

– ERR003778 "SSI: In AC97, 16-bit mode, received data is shifted by 4-bit locations"

– ERR005313 "EPDC: Incorrect data fetched when the buffer update width is 2048 pixels or greater"

Refer to the document revision history for detailed information.

REASON FOR CHANGE

Updated documentation

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

No changes to form, fit, function or reliability

NOTE:
THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a Service Request

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 20-Dec-2013

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

   N/A

RELIABILITY DATA SUMMARY:

   N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

   N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:
15969_IMX6SLCE_Rev3.pdf