



PRODUCT BULLETIN

Generic Copy

ISSUE DATE: 30-Jan-2014
NOTIFICATION: 16024
TITLE: i.MX6DL/S, REV1.2, Production Release
EFFECTIVE DATE: 31-Jan-2014

DEVICE(S)

MPN
MCIMX6S1AVM08AB
MCIMX6S1AVM08ABR
MCIMX6S1AVM08AC
MCIMX6S1AVM08ACR
MCIMX6S4AVM08AB
MCIMX6S4AVM08ABR
MCIMX6S4AVM08AC
MCIMX6S4AVM08ACR
MCIMX6S5DVM10AB
MCIMX6S5DVM10ABR
MCIMX6S5DVM10AC
MCIMX6S5DVM10ACR
MCIMX6S5EVM10AB
MCIMX6S5EVM10ABR
MCIMX6S5EVM10AC
MCIMX6S5EVM10ACR
MCIMX6S6AVM08AB
MCIMX6S6AVM08ABR
MCIMX6S6AVM08AC
MCIMX6S6AVM08ACR
MCIMX6S7CVM08AB
MCIMX6S7CVM08AC
MCIMX6S8DVM10AB
MCIMX6S8DVM10AC
MCIMX6U1AVM08AB
MCIMX6U1AVM08ABR
MCIMX6U1AVM08AC
MCIMX6U1AVM08ACR
MCIMX6U4AVM08AB

MCIMX6U4AVM08ABR
MCIMX6U4AVM08AC
MCIMX6U4AVM08ACR
MCIMX6U5DVM10AB
MCIMX6U5DVM10ABR
MCIMX6U5DVM10AC
MCIMX6U5DVM10ACR
MCIMX6U5EVM10AB
MCIMX6U5EVM10ABR
MCIMX6U5EVM10AC
MCIMX6U5EVM10ACR
MCIMX6U6AVM08AB
MCIMX6U6AVM08ABR
MCIMX6U6AVM08AC
MCIMX6U6AVM08ACR
MCIMX6U7CVM08AB
MCIMX6U7CVM08AC
MCIMX6U8DVM10AB
MCIMX6U8DVM10AC

AFFECTED CHANGE CATEGORIES

- DESIGN CHANGE

DESCRIPTION OF CHANGE

Freescale is releasing a new revision of i.MX6DL/S silicon to fix specific errata that are listed below

ERR007117 ROM: When booting from NAND flash, enfc_clk_root clock is not gated off when doing the clock source switch

ERR007220 ROM: NAND boot may fail due to incorrect Hamming checking implementation in the ROM code

ERR007122 ROM: TZASC_ENABLE fuse bit is coded in ROM as bit 24 at location 0x460 whereas the fuse map defines it as bit 28.

The detail errata information are listed at www.freescale.com website. URL:
<http://cache.freescale.com/files/32bit/doc/errata/IMX6SDLCE.pdf>

REASON FOR CHANGE

This new revision fixes critical errata that had limited or no workarounds available.

ANTICIPATED IMPACT OF PRODUCT CHANGE(FORM, FIT, FUNCTION, OR RELIABILITY)

The change allows for added functionality of the device. The new revision meets the same high Freescale reliability standards met by the previous revision and will be fully backwards compatible.

The revision has been fully validated. All of the updates are working as intended and no new issues have been introduced.

Both the previous revision and new revision are available for purchase. The previous revision is denoted by a "B" at the end of the ordering part number. The new revision is denoted by a "C" at the end of the ordering part number.

The marking on the new revision contains one major change, which is the addition of the maskset number. An additional line has been added that will have the "2N81E" maskset number.

NOTE:

THE CHANGE(S) SPECIFIED IN THIS NOTIFICATION WILL BE IMPLEMENTED ON THE EFFECTIVE DATE LISTED ABOVE. To request further data or inquire about the notification, please enter a [Service Request](#)

For sample inquiries - please go to www.freescale.com

QUAL DATA AVAILABILITY DATE: 30-Sep-2013

QUALIFICATION STATUS: COMPLETED

QUALIFICATION PLAN:

N/A

RELIABILITY DATA SUMMARY:

N/A

ELECTRICAL CHARACTERISTIC SUMMARY:

N/A

CHANGED PART IDENTIFICATION:

N/A

ATTACHMENT(S):

External attachment(s) FOR this notification can be viewed AT:

[16024_IMX6SDLCE.pdf](https://www.freescale.com/files/shared/doc/pcn/PCN16024.htm)
