General Description

The MIC5211 is a dual µCap 80mA linear voltage regulator with very low dropout voltage (typically 20mV at light loads), very low ground current (225µA at 20mA output current), and better than 3% initial accuracy. This dual device comes in the miniature SOT-23-6 package, featuring independent logic control inputs.

The µCap regulator design is optimized to work with low-value, low-cost ceramic capacitors. The outputs typically require only 0.1µF of output capacitance for stability.

Designed especially for hand-held, battery-powered devices, ground current is minimized using Micrel’s proprietary Super βeta PNP™ technology to prolong battery life. When disabled, power consumption drops nearly to zero.

Key features include SOT-23-6 packaging, current limiting, overtemperature shutdown, and protection against reversed battery conditions.

The MIC5211 is available in dual 1.8V, 2.5V, 2.7V, 2.8V, 3.0V, 3.3V, 3.6V, and 5.0V versions. Certain mixed voltages are also available. Contact Micrel for other voltages.

Features

- Stable with low-value ceramic or tantalum capacitors
- Independent logic controls
- Low quiescent current
- Low dropout voltage
- Mixed voltages available
- Tight load and line regulation
- Low temperature coefficient
- Current and thermal limiting
- Reversed input polarity protection
- Zero off-mode current
- Dual regulator in tiny SOT-23 package
- 2.5V to 16V input range

Applications

- Cellular telephones
- Laptop, notebook, and palmtop computers
- Battery-powered equipment
- Bar code scanners
- SMPS post regulator/dc-to-dc modules
- High-efficiency linear power supplies

Typical Application

![MIC5211 Block Diagram]

- VIN: 5V
- VOUTA: 0.1µF
- VOUTB: 0.1µF
- EnableA
- EnableB
## Pin Configuration

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Psb-Free Part Number</th>
<th>Voltage Side A / Side B</th>
<th>Temperature Range</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MIC5211-1.8BM6</td>
<td>LFBB</td>
<td>LFBB</td>
<td>1.8V</td>
<td>0°C to +125°C</td>
</tr>
<tr>
<td>MIC5211-2.5BM6</td>
<td>LFCC</td>
<td>LECC</td>
<td>2.5V</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>MIC5211-2.7BM6</td>
<td>LFDD</td>
<td>LEDD</td>
<td>2.7V</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>MIC5211-2.8BM6</td>
<td>LFEF</td>
<td>LFD</td>
<td>2.8V</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>MIC5211-3.0BM6</td>
<td>LFGG</td>
<td>LFGE</td>
<td>3.0V</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>MIC5211-3.3BM6</td>
<td>LFL</td>
<td>LFL</td>
<td>3.3V</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>MIC5211-3.6BM6</td>
<td>LFQK</td>
<td>LFQK</td>
<td>3.6V</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>MIC5211-5.0BM6</td>
<td>LFXX</td>
<td>LFXX</td>
<td>5.0V</td>
<td>-40°C to +125°C</td>
</tr>
</tbody>
</table>

### Dual-Voltage Regulators

- MIC5211-1.8/2.5BM6
- MIC5211-1.8/3.3BM6
- MIC5211-2.5/3.3BM6
- MIC5211-3.3/5.0BM6
- MIC5211-3.3/5.0BM6

Other voltages available. Contact Micrel for details.
## Absolute Maximum Ratings (Note 1)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typical</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Input Voltage ($V_{IN}$)</td>
<td>–20V to +20V</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Enable Input Voltage ($V_{EN}$)</td>
<td>–20V to +20V</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Power Dissipation ($P_D$)</td>
<td>Internally Limited</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>–60°C to +150°C</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Lead Temperature (soldering, 5 sec.)</td>
<td>260°C</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
</tbody>
</table>

ESD, (Note 3)

For one-half of dual MIC5211; unless noted.

## Operating Ratings (Note 2)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typical</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Input Voltage ($V_{IN}$)</td>
<td>2.5V to 16V</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Enable Input Voltage ($V_{EN}$)</td>
<td>0V to 16V</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Junction Temperature ($T_J$)</td>
<td>–40°C to +125°C</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>1.8V only</td>
<td>0°C to +125°C</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>6-lead SOT-23-6 ($\theta_JA$)</td>
<td>Note 4</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
</tbody>
</table>

## Electrical Characteristics

$V_{IN} = V_{OUT} + 1V$; $I_L = 1mA$; $C_L = 0.1\mu F$, and $V_{EN} \geq 2.0V$; $T_J = 25°C$, **bold** values indicate –40°C to +125°C; for one-half of dual MIC5211; unless noted.

### Symbol Parameter

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typical</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_O$</td>
<td>Output Voltage</td>
<td>–3</td>
<td>3</td>
<td>–</td>
<td>%</td>
</tr>
<tr>
<td>$\Delta V_O/\Delta T$</td>
<td>Output Voltage Temperature Coefficient</td>
<td>50</td>
<td>200</td>
<td>–</td>
<td>ppm/°C</td>
</tr>
<tr>
<td>$\Delta V_O/V_O$</td>
<td>Line Regulation</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>%</td>
</tr>
<tr>
<td>$\Delta V_O/V_O$</td>
<td>Load Regulation</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>%</td>
</tr>
<tr>
<td>$V_{IN} - V_O$</td>
<td>Dropout Voltage, Note 7</td>
<td>20</td>
<td>450</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td>$I_Q$</td>
<td>Quiescent Current</td>
<td>0.01</td>
<td>10</td>
<td>–</td>
<td>µA</td>
</tr>
<tr>
<td>$I_{GND}$</td>
<td>Ground Pin Current</td>
<td>90</td>
<td>–</td>
<td>–</td>
<td>µA</td>
</tr>
<tr>
<td>$I_{LIMIT}$</td>
<td>Current Limit</td>
<td>140</td>
<td>250</td>
<td>–</td>
<td>mA</td>
</tr>
<tr>
<td>$\Delta V_O/\Delta P_D$</td>
<td>Thermal Regulation</td>
<td>0.05</td>
<td>–</td>
<td>–</td>
<td>%/W</td>
</tr>
</tbody>
</table>

### Enable Input

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Min</th>
<th>Typical</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{IL}$</td>
<td>Enable Input Voltage Level</td>
<td>2.0</td>
<td>–</td>
<td>–</td>
<td>V</td>
</tr>
<tr>
<td>$V_{IH}$</td>
<td>Enable Input Current</td>
<td>0.01</td>
<td>1</td>
<td>–</td>
<td>µA</td>
</tr>
</tbody>
</table>

**Note 1:** Exceeding the absolute maximum rating may damage the device.

**Note 2:** The device is not guaranteed to function outside its operating rating.

**Note 3:** Devices are ESD sensitive. Handling precautions recommended.

**Note 4:** The maximum allowable power dissipation at any $T_A$ (ambient temperature) is $P_D(\text{max}) = (T_J(\text{max}) - T_A) / \theta_JA$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown. The $\theta_JA$ is 220°C/W for the SOT-23-6 mounted on a printed circuit board.

**Note 5:** Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.

**Note 6:** Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 50mA. Change in output voltage due to heating effects are covered by thermal regulation specification.

**Note 7:** Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. For output voltages below 2.5V, dropout voltage is the input-to-output voltage differential with the minimum voltage being 2.5V. Minimum input operating voltage is 2.5V.

**Note 8:** Ground pin current is the quiescent current per regulator plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

**Note 9:** Thermal regulation is defined as the change in output voltage at a time “t” after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a 50mA load pulse at $V_{IN} = 16V$ for $t = 10ms$. 
Typical Characteristics
Applications Information

Enable/Shutdown
ENA and ENB (enable/shutdown) may be controlled separately. Forcing ENA/B high (>2V) enables the regulator. The enable inputs typically draw only 15µA.

While the logic threshold is TTL/CMOS compatible, ENA/B may be forced as high as 20V, independent of VIN. ENA/B may be connected to the supply if the function is not required.

Input Capacitor
A 0.1µF capacitor should be placed from IN to GND if there is more than 10 inches of wire between the input and the ac filter capacitor or when a battery is used as the input.

Output Capacitor
Typical PNP based regulators require an output capacitor to prevent oscillation. The MIC5211 is ultrastable, requiring only 0.1µF of output capacitance per regulator for stability. The regulator is stable with all types of capacitors, including the tiny, low-ESR ceramic chip capacitors. The output capacitor value can be increased without limit to improve transient response.

The capacitor should have a resonant frequency above 500kHz. Ceramic capacitors work, but some dielectrics have poor temperature coefficients, which will affect the value of the output capacitor over temperature. Tantalum capacitors are much more stable over temperature, but typically are larger and more expensive. Aluminum electrolytic capacitors will also work, but they have electrolytes that freeze at about –30°C. Tantalum or ceramic capacitors are recommended for operation below –25°C.

No-Load Stability
The MIC5211 will remain stable and in regulation with no load (other than the internal voltage divider) unlike many other voltage regulators. This is especially important in CMOS RAM keep-alive applications.

Thermal Shutdown
Thermal shutdown is independent on both halves of the dual MIC5211, however, an overtemperature condition in one half may affect the other half because of proximity.

Thermal Considerations
When designing with a dual low-dropout regulator, both sections must be considered for proper operation. The part is designed with thermal shutdown, therefore, the maximum junction temperature must not be exceeded. Since the dual regulators share the same substrate, the total power dissipation must be considered to avoid thermal shutdown. Simple thermal calculations based on the power dissipation of both regulators will allow the user to determine the conditions for proper operation.

The maximum power dissipation for the total regulator system can be determined using the operating temperatures and the thermal resistance of the package. In a minimum footprint configuration, the SOT-23-6 junction-to-ambient thermal resistance \( \theta_{JA} \) is 220°C/W. Since the maximum junction temperature for this device is 125°C, at an operating temperature of 25°C the maximum power dissipation is:

\[
P_{D(\text{max})} = \frac{T_{J(\text{max})} - T_A}{\theta_{JA}}
\]

\[
P_{D(\text{max})} = \frac{125°C - 25°C}{220°C/W}
\]

\[
P_{D(\text{max})} = 455mW
\]

The MIC5211-3.0 can supply 3V to two different loads independently from the same supply voltage. If one of the regulators is supplying 50mA at 3V from an input voltage of 4V, the total power dissipation in this portion of the regulator is:

\[
P_{D1} = (V_{IN} - V_{OUT}) I_{OUT} + V_{IN} \cdot I_{GND}
\]

\[
P_{D1} = (4V - 3V) 50mA + 4V \cdot 0.85mA
\]

\[
P_{D1} = 53.4mW
\]

Up to approximately 400mW can be dissipated by the remaining regulator (455mW – 53.4mW) before reaching the thermal shutdown temperature, allowing up to 50mA of current.

\[
P_{D2} = (V_{IN} - V_{OUT}) I_{OUT} + V_{IN} \cdot I_{GND}
\]

\[
P_{D2} = (4V - 3V) 50mA + 4V \cdot 0.85mA
\]

\[
P_{D2} = 53.4mW
\]

The total power dissipation is:

\[
P_{D1} + P_{D2} = 53.4mW + 53.4mW
\]

\[
P_{D1} + P_{D2} = 106.8mW
\]

Therefore, with a supply voltage of 4V, both outputs can operate safely at room temperature and full load (50mA).

In many applications, the ambient temperature is much higher. By recalculating the maximum power dissipation at 70°C ambient, it can be determined if both outputs can supply full load when powered by a 4V supply.

\[
P_{D(\text{max})} = \frac{T_{J(\text{max})} - T_A}{\theta_{JA}}
\]

\[
P_{D(\text{max})} = \frac{125°C - 70°C}{220°C/W}
\]

\[
P_{D(\text{max})} = 250mW
\]

At 70°C, the device can provide 250mW of power dissipation, suitable for the above application.

When using supply voltages higher than 4V, do not exceed the maximum power dissipation for the device. If the device is operating from a 7.2V-nominal two-cell lithium-ion battery and
both regulators are dropping the voltage to 3.0V, then output current will be limited at higher ambient temperatures.

For example, at 70°C ambient the first regulator can supply 3.0V at 50mA output from a 7.2V supply; however, the second regulator will have limitations on output current to avoid thermal shutdown. The dissipation of the first regulator is:

\[ P_{D1} = (7.2V - 3V) \times 50mA + 7.2V \times 0.85mA \]
\[ P_{D1} = 216mW \]

Since maximum power dissipation for the dual regulator is 250mW at 70°C, the second regulator can only dissipate up to 34mW without going into thermal shutdown. The amount of current the second regulator can supply is:

\[ P_{D2(max)} = 34mW \]
\[ (7.2V - 3V) \times I_{OUT2(max)} = 34mW \]
\[ 4.2V \times I_{OUT2(max)} = 34mW \]
\[ I_{OUT2(max)} = 8mA \]

The second regulator can provide up to 8mA output current, suitable for the keep-alive circuitry often required in handheld applications.

Refer to Application Hint 17 for heat sink requirements when higher power dissipation capability is needed. Refer to Designing with Low Dropout Voltage Regulators for a more thorough discussion of regulator thermal characteristics.

**Dual-Voltage Considerations**

For configurations where two different voltages are needed in the system, the MIC5211 has the option of having two independent output voltages from the same input. For example, a 3.3V rail and a 5.0V rail can be supplied from the MIC5211 for systems that require both voltages. Important considerations must be taken to ensure proper functionality of the part. The input voltage must be high enough for the 5V section to operate correctly, this will ensure the 3.3V section proper operation as well.

Both regulators live off of the same input voltage, therefore the amount of output current each regulator supplies may be limited thermally. The maximum power the MIC5211 can dissipate at room temperature is 455mW, as shown in the “Thermal Considerations” section. If we assume 6V input voltage and 50mA of output current for the 3.3V section of the regulator, then the amount of output current the 5V section can provide can be calculated based on the power dissipation.

\[ P_D = (V_{GND} - V_{OUT}) \times I_{OUT} + V_{GND} \times I_{GND} \]
\[ P_{D(3.3V)} = (6V - 3.3V) \times 50mA + 6V \times 0.85mA \]
\[ P_{D(3.3V)} = 140.1mW \]
\[ P_{D(max)} = 455mW \]
\[ P_{D(max)} - P_{D(3.3V)} = P_{D(5V)} \]
\[ P_{D(5V)} = 455mW - 140.1mW \]
\[ P_{D(5V)} = 314.9mW \]

Based on the power dissipation allowed for the 5V section, the amount of output current it can source is easily calculated.

\[ P_D = 314.9mW \]
\[ 314.9mW = (6V - 5V) \times I_{MAX} - 6V \times I_{GND} \]
\((I_{GND} \text{ typically adds less than 5% to the total power dissipation and in this case can be ignored})\)
\[ 314.9mW = (6V - 5V) \times I_{MAX} \]
\[ I_{MAX} = 314.9mA \]

\(I_{MAX}\) exceeds the maximum current rating of the device. Therefore, for this condition, the MIC5211 can supply 50mA of output current from each section of the regulator.