LOGI-EDU BUNDLE PACKAGE FOR THE LOGI-PI - FPGA HDL EDUCATIONAL PATHWAY

Bundle includes LOGI-PI FPGA development board and LOGI-EDU Education board

The LOGi-EDU package is an expansion board for the LOGi-Pi FPGA board. The LOGi-EDU serves as an educational pathway to allow FPGA beginners to easily learn and implement basics of FPGAs and HDL design. The educational path walks beginners through the basic steps of FPGA design by using examples from the book "FPGA Prototyping By Verilog Examples" or "FPGA Prototyping By VHDL Examples". The examples can be run by using the add-on EDU expansion module that supports the book examples and applications. Using these examples allows the user to quickly get up to speed with the basics of FPGA design and allows the user to migrate to designing and working on and designing greater complexity FPGA applications.

There are many FPGA resources that do not give all of the information needed, or omit some of the relevant information required to fully understand the basic and advanced topics of developing FPGA designs. The Pong Chu books chosen contain the crucial concepts of FPGA design in a practical and concise way that will get the reader up and running in an efficient manner.

The LOGi-EDU was designed to be used after you are done with the educational content. We have added some extra peripherals that make it a well rounded board for many types of general purpose projects.

FEATURES

- 4x Seven Segment Character display
- 9 bit VGA video output
- 2x PS/2 peripheral ports
- 2x PWM or delta sigma audio outputs
- 2x NES (Nintendo original) controllers
- 2x Servo connectors
- Breadboard area utilizing 10 shared IO pins for general purpose use

KIT CONTENTS

- LOGI-EDU: Education Board
- LOGI-PI: FPGA Development Board for the Raspberry PI