RAPPID: RAppID Initialization for Power Architecture

Mfftr PN: RAPPID-564XBSW

RAppID is a family of graphical development tools for the MPC5XXX family of Power Architecture® controllers that enables the user to quickly and easily configure the controller PLUS generate complete documentation. It can also be used as a learning tool to gain an understanding of the controller and it's peripherals. RAppID not only generates C code for initializing the registers, but it also provides a system initialization function that brings the controller up in an orderly sequence. Use RAppID to save time and become an expert on the MPC5XXX Family.

Features

Product Highlights:

- Intuitive, easy-to-use graphical user interface (GUI)
- Comprehensive initialization of the CPU, memory and peripherals
- Automatic DMA register setting from peripherals for basic modes
- Built-in consistency checks to minimize incorrect settings
- Automatic report generation of settings
- Efficient C and assembly code generation for compilers from companies such as Wind River®, Green Hills® and Freescale
- ANSI- or MISRA-compliant code and supporting header files
- Online documentation and built-in tool tips
- Provisions for revision management
- Automatic date and time stamps on generated code and reports
- Enhanced register viewer—browse registers by peripheral
- Modular code generation—generate code for any or all peripherals
- Project import/export capability for distributed development teams
- Ability to define multiple initialization strategies and chose either runtime or compile time for execution
- Full eTPU host initialization and code image download
- Ability to register custom eTPU functions and assign channels
- Wizards for eMIOS initialization and function settings