**FEATURES**

- Single Inductor
- Fixed Frequency Operation with Battery Voltages Above, Below or Equal to the Output
- Synchronous Rectification: Up to 96% Efficiency
- Up to 600mA Continuous Output Current
- No Schottky Diodes Required (V_{OUT} < 4.3V)
- V_{OUT} Disconnected from V_{IN} During Shutdown
- 2.5V to 5.5V Input and Output Range
- Programmable Oscillator Frequency from 300kHz to 2MHz
- Synchronizable Oscillator
- Burst Mode Enable Control
- <1µA Shutdown Current
- Small Thermally Enhanced 10-Pin MSOP and (3mm × 3mm) DFN Packages

**APPLICATIONS**

- Palmtop Computers
- Handheld Instruments
- MP3 Players
- Digital Cameras

**DESCRIPTION**

The LTC®3440 is a high efficiency, fixed frequency, Buck-Boost DC/DC converter that operates from input voltages above, below or equal to the output voltage. The topology incorporated in the IC provides a continuous transfer function through all operating modes, making the product ideal for single lithium-ion, multicell alkaline or NiMH applications where the output voltage is within the battery voltage range.

The device includes two 0.19Ω N-channel MOSFET switches and two 0.22Ω P-channel switches. Switching frequencies up to 2MHz are programmed with an external resistor and the oscillator can be synchronized to an external clock. Quiescent current is only 25µA in Burst Mode operation, maximizing battery life in portable applications. Burst Mode operation is user controlled and can be enabled by driving the MODE/SYNC pin high. If the MODE/SYNC pin has either a clock or is driven low, then fixed frequency switching is enabled.

Other features include a 1µA shutdown, soft-start control, thermal shutdown and current limit. The LTC3440 is available in the 10-pin thermally enhanced MSOP and (3mm × 3mm) DFN packages.

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**, LT, LTC, LTM, Linear Technology, Burst Mode and the Linear logo are registered trademarks and ThinSOT is a trademark of Linear Technology Corporation. All other trademarks are the property of their respective owners."
LTC3440

ABSOLUTE MAXIMUM RATINGS

(Note 1)

V_{IN}, V_{OUT} Voltage .............................................. –0.3V to 6V
SW1, SW2 Voltage ................................................... –0.3V to 6V
V_C, R_T, FB, SHDN/SS, MODE/SYNC Voltage ................. –0.3V to 6V

Operating Temperature Range (Note 2)............–40°C to 85°C
Storage Temperature Range ..........................–65°C to 125°C
Lead Temperature (Soldering, 10 sec).........................300°C

PIN CONFIGURATION

ORDER INFORMATION

LEAD FREE FINISH | TAPE AND REEL | PART MARKING | PACKAGE DESCRIPTION | TEMPERATURE RANGE
---|---|---|---|---
LTC3440EDD#PBF | LTC3440EDD#TRPBF | LBKT | 10-Lead (3mm × 3mm) Plastic DFN | –40°C to 85°C
LTC3440EMS#PBF | LTC3440EMS#TRPBF | LTNP | 10-Lead Plastic MSOP | –40°C to 85°C

Consult LTC Marketing for parts specified with wider operating temperature ranges.
Consult LTC Marketing for information on nonstandard lead based finish parts.

For more information on lead free part marking, go to: http://www.linear.com/leadfree/
For more information on tape and reel specifications, go to: http://www.linear.com/tapeandreel/

ELECTRICAL CHARACTERISTICS

The ● denotes specifications that apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{IN} = V_{OUT} = 3.6V, R_T = 60k, unless otherwise noted.

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Start-Up Voltage</td>
<td>●</td>
<td>2.4</td>
<td>2.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Input Operating Range</td>
<td>●</td>
<td>2.5</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Output Voltage Adjust Range</td>
<td>●</td>
<td>2.5</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Feedback Voltage</td>
<td>●</td>
<td>1.196</td>
<td>1.22</td>
<td>1.244</td>
<td>V</td>
</tr>
<tr>
<td>Feedback Input Current</td>
<td>V_FB = 1.22V</td>
<td>1</td>
<td>50</td>
<td>nA</td>
<td></td>
</tr>
<tr>
<td>Quiescent Current, Burst Mode Operation</td>
<td>V_C = 0V, MODE/SYNC = 3V (Note 3)</td>
<td>25</td>
<td>40</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>Quiescent Current, Shutdown</td>
<td>SHDN = 0V, Not Including Switch Leakage</td>
<td>0.1</td>
<td>1</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>Quiescent Current, Active</td>
<td>V_C = 0V, MODE/SYNC = 0V (Note 3)</td>
<td>600</td>
<td>1000</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>NMOS Switch Leakage</td>
<td>Switches B and C</td>
<td>0.1</td>
<td>5</td>
<td>µA</td>
<td></td>
</tr>
</tbody>
</table>
### Electrical Characteristics

The ● denotes specifications that apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ C$. $V_{IN} = V_{OUT} = 3.6V$, $R_T = 60k$, unless otherwise noted.

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMOS Switch Leakage</td>
<td>Switches A and D</td>
<td>0.1</td>
<td>10</td>
<td>$\mu A$</td>
<td></td>
</tr>
<tr>
<td>NMOS Switch On Resistance</td>
<td>Switches B and C</td>
<td>0.19</td>
<td></td>
<td>$\Omega$</td>
<td></td>
</tr>
<tr>
<td>PMOS Switch On Resistance</td>
<td>Switches A and D</td>
<td>0.22</td>
<td></td>
<td>$\Omega$</td>
<td></td>
</tr>
<tr>
<td>Input Current Limit</td>
<td>●</td>
<td>1</td>
<td></td>
<td>$A$</td>
<td></td>
</tr>
<tr>
<td>Maximum Duty Cycle</td>
<td>Boost (% Switch C On)</td>
<td>●</td>
<td>55</td>
<td>75</td>
<td>$%$</td>
</tr>
<tr>
<td></td>
<td>Buck (% Switch A On)</td>
<td>●</td>
<td>100</td>
<td></td>
<td>$%$</td>
</tr>
<tr>
<td>Minimum Duty Cycle</td>
<td>●</td>
<td>0</td>
<td></td>
<td>$%$</td>
<td></td>
</tr>
<tr>
<td>Frequency Accuracy</td>
<td>●</td>
<td>0.8</td>
<td>1</td>
<td>1.2</td>
<td>MHz</td>
</tr>
<tr>
<td>MODE/SYNC Threshold</td>
<td></td>
<td>0.4</td>
<td>2</td>
<td></td>
<td>$V$</td>
</tr>
<tr>
<td>MODE/SYNC Input Current</td>
<td>$V_{MODE/SYNC} = 5.5V$</td>
<td>0.01</td>
<td>1</td>
<td>$\mu A$</td>
<td></td>
</tr>
<tr>
<td>Error Amp AVOL</td>
<td></td>
<td>90</td>
<td></td>
<td>$dB$</td>
<td></td>
</tr>
<tr>
<td>Error Amp Source Current</td>
<td></td>
<td>15</td>
<td></td>
<td>$\mu A$</td>
<td></td>
</tr>
<tr>
<td>Error Amp Sink Current</td>
<td></td>
<td>380</td>
<td></td>
<td>$\mu A$</td>
<td></td>
</tr>
<tr>
<td>SHDN/SS Threshold</td>
<td>When IC is Enabled</td>
<td>●</td>
<td>0.4</td>
<td>1</td>
<td>1.5</td>
</tr>
<tr>
<td></td>
<td>When EA is at Maximum Boost Duty Cycle</td>
<td></td>
<td></td>
<td>2.2</td>
<td></td>
</tr>
<tr>
<td>SHDN/SS Input Current</td>
<td>$V_{SHDN} = 5.5V$</td>
<td>0.01</td>
<td>1</td>
<td>$\mu A$</td>
<td></td>
</tr>
</tbody>
</table>

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of the device may be impaired.

**Note 2:** The LTC3440E is guaranteed to meet performance specifications from 0°C to 70°C. Specifications over the –40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

**Note 3:** Current measurements are performed when the outputs are not switching.

---

### Typical Performance Characteristics

#### Li-Ion to 3.3V Efficiency (fOSC = 300kHz)

![Graph](3440b21)

#### Li-Ion to 3.3V Efficiency, Power Loss (fOSC = 1MHz)

![Graph](3440b22)

#### Li-Ion to 3.3V Efficiency (fOSC = 2MHz)

![Graph](3440b23)

For more information [www.linear.com/LTC3440](http://www.linear.com/LTC3440)
TYPICAL PERFORMANCE CHARACTERISTICS

Switch Pins During Buck/Boost

SW1 2V/DIV

SW2 2V/DIV

50ns/DIV

VIN = 3.78V
VOUT = 3.3V
IOUT = 250mA

VIN = 5V
VOUT = 3.3V
IOUT = 250mA

VIN = 3.42V
VOUT = 3.3V
IOUT = 250mA

VIN = 4.15V
VOUT = 3.3V
IOUT = 250mA

Active Quiescent Current

Burst Mode Quiescent Current

Error Amp Source Current

For more information www.linear.com/LTC3440
TYPICAL PERFORMANCE CHARACTERISTICS

Output Frequency

\[ V_{IN} = V_{OUT} = 3.6V \]

NMOS \( R_{DS(ON)} \)

\[ V_{IN} = V_{OUT} = 3.6V \]

Feedback Voltage

\[ V_{IN} = V_{OUT} = 3V \]

Feedback Voltage Line Regulation

\[ V_{IN} = V_{OUT} = 2.5V \text{ to } 5.5V \]

Error Amp Sink Current

\[ V_{IN} = V_{OUT} = 3.6V \]

PMOS \( R_{DS(ON)} \)

\[ V_{IN} = V_{OUT} = 3.6V \]

SWITCHES B AND C

\[ \text{TEMPERATURE (°C)} \]

\[ -55 \quad -25 \quad 5 \quad 35 \quad 65 \quad 95 \quad 125 \]

\[ \text{FREQUENCY (MHz)} \]

\[ 0.90 \quad 0.95 \quad 1.00 \quad 1.05 \quad 1.10 \]

\[ \text{TEMPERATURE (°C)} \]

\[ 0.10 \quad 0.15 \quad 0.20 \quad 0.25 \quad 0.30 \]

\[ \text{MNOS R}_{DS(ON)} \quad (\Omega) \]

\[ \text{FEEDBACK VOLTAGE (V)} \]

\[ 1.196 \quad 1.216 \quad 1.236 \]

\[ \text{VIN} = V_{OUT} = 3V \]

\[ \text{SWITCHES A AND D} \]

\[ \text{TEMPERATURE (°C)} \]

\[ -55 \quad -25 \quad 5 \quad 35 \quad 65 \quad 95 \quad 125 \]

\[ \text{LINE REGULATION (dB)} \]

\[ 60 \quad 70 \quad 80 \quad 90 \]

\[ \text{VIN} = V_{OUT} = 2.5V \text{ to } 5.5V \]

\[ \text{E/A SINK CURRENT (µA)} \]

\[ 350 \quad 370 \quad 390 \quad 410 \]

\[ \text{PMOS R}_{DS(ON)} \quad (\Omega) \]

\[ 0.10 \quad 0.15 \quad 0.20 \quad 0.25 \quad 0.30 \]

\[ \text{VIN} = V_{OUT} = 3.6V \]

\[ \text{PEAK SWITCH} \]

\[ \text{CURRENT LIMIT (A)} \]

\[ 1000 \quad 1500 \quad 2000 \quad 2500 \quad 3000 \]

For more information www.linear.com/LTC3440
PIN FUNCTIONS

*R* (Pin 1): Timing Resistor to Program the Oscillator Frequency. The programming frequency range is 300kHz to 2MHz.

\[ f_{\text{OSC}} = \frac{6 \times 10^{10}}{R_T} \text{ Hz} \]

**MODE/SYNC (Pin 2):** MODE/SYNC = External CLK : Synchronization of the internal oscillator. A clock frequency of twice the desired switching frequency and with a pulse width between 100ns and 2µs is applied. The oscillator free running frequency is set slower than the desired synchronized switching frequency to guarantee sync. The oscillator \( R_T \) component value required is given by:

\[ R_T = \frac{8 \times 10^{10}}{f_{\text{SW}}} \]

where \( f_{\text{SW}} \) = desired synchronized switching frequency.

**SW1 (Pin 3):** Switch Pin Where the Internal Switches A and B are Connected. Connect inductor from SW1 to SW2. An optional Schottky diode can be connected from SW1 to ground. Minimize trace length to keep EMI down.

**SW2 (Pin 4):** Switch Pin Where the Internal Switches C and D are Connected. For applications with output voltages over 4.3V, a Schottky diode is required from SW2 to \( V_{\text{OUT}} \) to ensure the SW pin does not exhibit excess voltage.

**GND (Pin 5):** Signal and Power Ground for the IC.

**VOUT (Pin 6):** Output of the Synchronous Rectifier. A filter capacitor is placed from \( V_{\text{OUT}} \) to GND.

**VIN (Pin 7):** Input Supply Pin. Internal \( V_{\text{CC}} \) for the IC. A ceramic bypass capacitor as close to the \( V_{\text{IN}} \) pin and GND (Pin 5) is required.

**SHDN/SS (Pin 8):** Combined Soft-Start and Shutdown. Grounding this pin shuts down the IC. Tie to >1.5V to enable the IC and >2.5V to ensure the error amp is not clamped from soft-start. An RC from the shutdown command signal to this pin will provide a soft-start function by limiting the rise time of the \( V_C \) pin.

**FB (Pin 9):** Feedback Pin. Connect resistor divider tap here. The output voltage can be adjusted from 2.5V to 5.5V. The feedback reference voltage is typically 1.22V.

**VC (Pin 10):** Error Amp Output. A frequency compensation network is connected from this pin to the FB pin to compensate the loop. See the section “Compensating the Feedback Loop” for guidelines.

**Exposed Pad (Pin 11, DFN Package Only):** Ground. This pin must be soldered to the PCB and electrically connected to ground.
The LTC3440 provides high efficiency, low noise power for applications such as portable instrumentation. The LTC proprietary topology allows input voltages above, below or equal to the output voltage by properly phasing the output switches. The error amp output voltage on the VC pin determines the output duty cycle of the switches. Since the VC pin is a filtered signal, it provides rejection of frequencies from well below the switching frequency. The low RDS(ON), low gate charge synchronous switches provide high frequency pulse width modulation control at high efficiency. Schottky diodes across the synchronous switch D and synchronous switch B are not required, but provide a lower drop during the break-before-make time (typically 15ns). The addition of the Schottky diodes will improve peak efficiency by typically 1% to 2% at 600kHz. High efficiency is achieved at light loads when Burst Mode operation is entered and when the IC’s quiescent current is a low 25µA.

**LOW NOISE FIXED FREQUENCY OPERATION**

**Oscillator**

The frequency of operation is user programmable and is set through a resistor from the RT pin to ground where:

\[ f = \frac{6 \times 10^{10}}{RT} \text{ Hz} \]

An internally trimmed timing capacitor resides inside the IC. The oscillator can be synchronized with an external clock applied to the MODE/SYNC pin. A clock frequency of twice the desired switching frequency and with a pulse width between 100ns and 2µs is applied. The oscillator RT component value required is given by:

\[ RT = \frac{8 \times 10^{10}}{f_{SW}} \]

where \( f_{SW} \) = desired synchronized switching frequency.

For example to achieve a 1.2MHz synchronized switching frequency the applied clock frequency to the MODE/SYNC pin is set to 2.4MHz and the timing resistor, RT, is set to 66.5k (closest 1% value).

**Error Amp**

The error amplifier is a voltage mode amplifier. The loop compensation components are configured around the amplifier to provide loop compensation for the converter. The SHDN/SS pin will clamp the error amp output, VC, to provide a soft-start function.

**Supply Current Limit**

The current limit amplifier will shut PMOS switch A off once the current exceeds 2.7A typical. The current amplifier delay to output is typically 50ns.

**Reverse Current Limit**

The reverse current limit amplifier monitors the inductor current from the output through switch D. Once a negative inductor current exceeds –400mA typical, the IC will shut off switch D.

**Output Switch Control**

Figure 1 shows a simplified diagram of how the four internal switches are connected to the inductor, VIN, VOUT and GND. Figure 2 shows the regions of operation for the LTC3440 as a function of the internal control voltage, VCI. The VCI voltage is a level shifted voltage from the output of the error amp (VC pin) (see Figure 5). The output switches are properly phased so the transfer between operation modes is continuous, filtered and transparent to the user. When VIN approaches VOUT the Buck/Boost region is reached where the conduction time of the four switch region is typically 150ns. Referring to Figures 1 and 2, the various regions of operation will now be described.

![Figure 1. Simplified Diagram of Output Switches](3440c)
**OPERATION**

**Buck Region (VIN > VOUT)**

Switch D is always on and switch C is always off during this mode. When the internal control voltage, VCI, is above voltage V1, output A begins to switch. During the off time of switch A, synchronous switch B turns on for the remainder of the time. Switches A and B will alternate similar to a typical synchronous buck regulator. As the control voltage increases, the duty cycle of switch A increases until the maximum duty cycle of the converter in Buck mode reaches DMAX_BUCK, given by:

\[ D_{\text{MAX\_BUCK}} = 100 - \frac{D_{4\text{SW}}}{\%} \]

where D4SW = duty cycle % of the four switch range.

\[ D_{4\text{SW}} = (150\text{ns} \times f) \times 100 \% \]

where f = operating frequency, Hz.

Beyond this point the “four switch,” or Buck/Boost region is reached.

**Buck/Boost or Four Switch (VIN ~ VOUT)**

When the internal control voltage, VCI, is above voltage V2, switch pair AD remain on for duty cycle DMAX_BUCK, and the switch pair AC begins to phase in. As switch pair AC phases in, switch pair BD phases out accordingly. When the VCI voltage reaches the edge of the Buck/Boost range, at voltage V3, the AC switch pair completely phase out the BD pair, and the boost phase begins at duty cycle D4SW.

The input voltage, VIN, where the four switch region begins is given by:

\[ V_{\text{IN}} = \frac{V_{\text{OUT}}}{1 - (150\text{ns} \times f)} \]

The point at which the four switch region ends is given by:

\[ V_{\text{IN}} = V_{\text{OUT}}(1 - D) = V_{\text{OUT}}(1 - 150\text{ns} \times f) \]

**Boost Region (VIN < VOUT)**

Switch A is always on and switch B is always off during this mode. When the internal control voltage, VCI, is above voltage V3, switch pair CD will alternately switch to provide a boosted output voltage. This operation is typical to a synchronous boost regulator. The maximum duty cycle of the converter is limited to 75% typical and is reached when VCI is above V4.

**Burst Mode Operation**

Burst Mode operation is when the IC delivers energy to the output until it is regulated and then goes into a sleep mode where the outputs are off and the IC is consuming only 25µA. In this mode the output ripple has a variable frequency component that depends upon load current.

During the period where the device is delivering energy to the output, the peak current will be equal to 400mA typical and the inductor current will terminate at zero current for each cycle. In this mode the maximum average output current is given by:

\[ I_{\text{OUT\_MAX\_BURST}} = \frac{0.1 \times V_{\text{IN}}}{V_{\text{OUT}} + V_{\text{IN}}} \]

**Figure 2. Switch Control vs Internal Control Voltage, VCI**

- **Buck Region**
  - VIN > VOUT
  - Switch D is always on and switch C is always off.
  - Switch A begins to switch when VCI > V1.
  - Switch B turns on for the remainder of the off time.
  - Switches A and B alternate similar to a typical synchronous buck regulator.

- **Buck/Boost Region**
  - VIN ~ VOUT
  - Switch pair AD remains on.
  - Switch pair AC begins to phase in.
  - Switch pair BD phases out accordingly.

- **Boost Region**
  - VIN < VOUT
  - Switch A is always on.
  - Switch B is always off.
  - Switch pair CD alternately switches.

**Table:**

<table>
<thead>
<tr>
<th>Voltage</th>
<th>Description</th>
</tr>
</thead>
</table>
| V1 (≈0.9V) | Buck Region |}

For more information visit [www.linear.com/LTC3440](http://www.linear.com/LTC3440)
OPERATION

therefore full-time 4-switch mode is required to maintain the Buck/Boost function. The efficiency below 1mA becomes dominated primarily by the quiescent current and not the peak efficiency. The equation is given by:

Efficiency Burst ≈ \( \frac{(\eta_{bm}) \cdot I_{LOAD}}{25 \mu A + I_{LOAD}} \).

where \( \eta_{bm} \) is typically 79% during Burst Mode operation for an ESR of the inductor of 50mΩ. For 200mΩ of inductor ESR, the peak efficiency \( \eta_{bm} \) drops to 75%.

Burst Mode Operation to Fixed Frequency Transient Response

When transitioning from Burst Mode operation to fixed frequency, the system exhibits a transient since the modes of operation have changed. For most systems this transient is acceptable, but the application may have stringent input current and/or output voltage requirements that dictate a broad-band voltage loop to minimize the transient. Lowering the DC gain of the loop will facilitate the task (10M FB to \( V_C \)) at the expense of DC load regulation. Type 3 compensation is also recommended to broad band the loop and roll off past the two pole response of the LC of the converter (see Closing the Feedback Loop).

Figure 3. Inductor Charge Cycle During Burst Mode Operation

Figure 4. Inductor Discharge Cycle During Burst Mode Operation
**OPERATION**

**SOFT-START**

The soft-start function is combined with shutdown. When the SHDN/SS pin is brought above typically 1V, the IC is enabled but the EA duty cycle is clamped from the VC pin. A detailed diagram of this function is shown in Figure 5. The components R_{SS} and C_{SS} provide a slow ramping voltage on the SHDN/SS pin to provide a soft-start function.

![Figure 5. Soft-Start Circuitry](image)

**APPLICATIONS INFORMATION**

**COMPONENT SELECTION**

**Inductor Selection**

The high frequency operation of the LTC3440 allows the use of small surface mount inductors. The inductor current ripple is typically set to 20% to 40% of the maximum inductor current. For a given ripple the inductance terms are given as follows:

\[
L > \frac{V_{\text{IN(MIN)}} \cdot (V_{\text{OUT}} - V_{\text{IN(MIN)}})}{f \cdot I_{\text{OUT(MAX)}} \cdot \text{Ripple} \cdot V_{\text{OUT}}} \mu\text{H},
\]

\[
L > \frac{V_{\text{OUT}} \cdot (V_{\text{IN(MAX)}} - V_{\text{OUT}})}{f \cdot I_{\text{OUT(MAX)}} \cdot \text{Ripple} \cdot V_{\text{IN(MAX)}}} \mu\text{H}
\]

where \(f\) = operating frequency, MHz

![Figure 6. Recommended Component Placement. Traces Carrying High Current are Direct. Trace Area at FB and VC Pins are Kept Low. Lead Length to Battery Should be Kept Short](image)
Ripple = allowable inductor current ripple 
(e.g., 0.2 = 20%) 
\( V_{\text{IN(MIN)}} = \) minimum input voltage, V 
\( V_{\text{IN(MAX)}} = \) maximum input voltage, V 
\( V_{\text{OUT}} = \) output voltage, V 
\( I_{\text{OUT(MAX)}} = \) maximum output load current 

For high efficiency, choose an inductor with a high frequency core material, such as ferrite, to reduce core losses. The inductor should have low ESR (equivalent series resistance) to reduce the \( I^2R \) losses, and must be able to handle the peak inductor current without saturating. Molded chokes or chip inductors usually do not have enough core to support the peak inductor currents in the 1A to 2A region. To minimize radiated noise, use a toroid, pot core or shielded bobbin inductor. See Table 1 for suggested components and Table 2 for a list of component suppliers.

### Table 1. Inductor Vendor Information

<table>
<thead>
<tr>
<th>SUPPLIER</th>
<th>PHONE</th>
<th>FAX</th>
<th>WEB SITE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Coilcraft</td>
<td>(847) 639-6400</td>
<td>(847) 639-1469</td>
<td><a href="http://www.coilcraft.com">www.coilcraft.com</a></td>
</tr>
<tr>
<td>Coiltronics</td>
<td>(561) 241-7876</td>
<td>(561) 241-9339</td>
<td><a href="http://www.coiltronics.com">www.coiltronics.com</a></td>
</tr>
<tr>
<td>Murata</td>
<td>USA: (814) 237-1431, (800) 831-9172</td>
<td>USA: (814) 238-0490</td>
<td><a href="http://www.murata.com">www.murata.com</a></td>
</tr>
<tr>
<td>Sumida</td>
<td>USA: (847) 956-0666, Japan: 81(3) 3607-5111</td>
<td>(847) 956-0702</td>
<td><a href="http://www.japanlink.com/sumida">www.japanlink.com/sumida</a></td>
</tr>
</tbody>
</table>

### Output Capacitor Selection

The bulk value of the capacitor is set to reduce the ripple due to charge into the capacitor each cycle. The steady state ripple due to charge is given by:

\[
\% \text{Ripple}_{\text{Boost}} = \frac{I_{\text{OUT(MAX)}} \cdot (V_{\text{OUT}} - V_{\text{IN(MIN)}})}{C_{\text{OUT}} \cdot V_{\text{OUT}}^2 \cdot f} \cdot 100\% \\
\% \text{Ripple}_{\text{Buck}} = \frac{I_{\text{OUT(MAX)}} \cdot (V_{\text{IN(MAX)}} - V_{\text{OUT}})}{C_{\text{OUT}} \cdot V_{\text{IN(MAX)}} \cdot V_{\text{OUT}} \cdot f} \cdot 100\%
\]

where \( C_{\text{OUT}} = \) output filter capacitor, \( F \)

The output capacitance is usually many times larger in order to handle the transient response of the converter. For a rule of thumb, the ratio of the operating frequency to the unity-gain bandwidth of the converter is the amount the output capacitance will have to increase from the above calculations in order to maintain the desired transient response.

The other component of ripple is due to the ESR (equivalent series resistance) of the output capacitor. Low ESR capacitors should be used to minimize output voltage ripple. For surface mount applications, Taiyo Yuden ceramic capacitors, AVX TPS series tantalum capacitors or Sanyo POSCAP are recommended.

### Input Capacitor Selection

Since the \( V_{\text{IN}} \) pin is the supply voltage for the IC it is recommended to place at least a 4.7\( \mu \)F, low ESR bypass capacitor.

### Optional Schottky Diodes

To achieve a 1%-2% efficiency improvement above 50mW, Schottky diodes can be added across synchronous switches B (SW1 to GND) and D (SW2 to \( V_{\text{OUT}} \)). The Schottky diodes will provide a lower voltage drop during the break-before-make time (typically 15ns) of the NMOS to PMOS transition. General purpose diodes such as a 1N914 are not recommended due to the slow recovery times and will compromise efficiency. If desired a large Schottky diode, such as an MBRM120T3, can be used from SW2 to \( V_{\text{OUT}} \). A low capacitance Schottky diode is recommended from GND to SW1 such as a Phillips PMEG2010EA or equivalent.
APPLICATIONS INFORMATION

Output Voltage > 4.3V
A Schottky diode from SW to VOUT is required for output voltages over 4.3V. The diode must be located as close to the pins as possible in order to reduce the peak voltage on SW2 due to the parasitic lead and trace inductance.

Input Voltage > 4.5V
For applications with input voltages above 4.5V which could exhibit an overload or short-circuit condition, a 2Ω/1nF series snubber is required between the SW1 pin and GND. A Schottky diode such as the Phillips PMEG2010EA or equivalent from SW1 to VIN should also be added as close to the pins as possible. For the higher input voltages VIN bypassing becomes more critical, therefore, a ceramic bypass capacitor as close to the VIN and GND pins as possible is also required.

Operating Frequency Selection
There are several considerations in selecting the operating frequency of the converter. The first is, what are the sensitive frequency bands that cannot tolerate any spectral noise? For example, in products incorporating RF communications, the 455kHz IF frequency is sensitive to any noise, therefore switching above 600kHz is desired. Some communications have sensitivity to 1.1MHz and in that case a 2MHz converter frequency may be employed.

Other considerations are the physical size of the converter and efficiency. As the operating frequency goes up, the inductor and filter capacitors go down in value and size. The trade off is in efficiency since the switching losses due to gate charge are going up proportional with frequency.

Additional quiescent current due to the output switches GATE charge is given by:
- Buck: $500e^{-12} \cdot V_{IN} \cdot F$
- Boost: $250e^{-12} \cdot (V_{IN} + V_{OUT}) \cdot F$
- Buck/Boost: $F \cdot (750e^{-12} \cdot V_{IN} + 250e^{-12} \cdot V_{OUT})$

where $F =$ switching frequency

Closing the Feedback Loop
The LTC3440 incorporates voltage mode PWM control. The control to output gain varies with operation region (Buck, Boost, Buck-Boost), but is usually no greater than 15. The output filter exhibits a double pole response is given by:

$$f_{FILTER\_POLE} = \frac{1}{2 \cdot \pi \cdot \sqrt{L \cdot C_{OUT}}} \text{ Hz (in Buck mode)}$$

$$f_{FILTER\_POLE} = \frac{V_{IN}}{2 \cdot V_{OUT} \cdot \pi \cdot \sqrt{L \cdot C_{OUT}}} \text{ Hz (in Boost mode)}$$

Where $L$ is in Henries and $C_{OUT}$ is the output filter capacitor in Farads.

The output filter zero is given by:

$$f_{FILTER\_ZERO} = \frac{1}{2 \cdot \pi \cdot R_{ESR} \cdot C_{OUT}} \text{ Hz}$$

where $R_{ESR}$ is the capacitor equivalent series resistance.

A troublesome feature in Boost mode is the right-half plane zero (RHP), and is given by:

$$f_{RHPZ} = \frac{V_{IN}}{2 \cdot \pi \cdot I_{OUT} \cdot L \cdot V_{OUT}} \text{ Hz}$$

The loop gain is typically rolled off before the RHP zero frequency.

A simple Type I compensation network can be incorporated to stabilize the loop but at a cost of reduced bandwidth and slower transient response. To ensure proper phase margin, the loop requires to be crossed over a decade before the LC double pole.

The unity-gain frequency of the error amplifier with the Type I compensation is given by:

$$f_{UG} = \frac{1}{2 \cdot \pi \cdot R1 \cdot C1} \text{ Hz}$$

Most applications demand an improved transient response to allow a smaller output filter capacitor. To achieve a higher bandwidth, Type III compensation is required. Two zeros are required to compensate for the double-pole response.
Short-Circuit Improvements

The LTC3440 is current limited to 2.7A peak to protect the IC from damage. At input voltages above 4.5V a current limit condition may produce undesirable voltages to the IC due to the series inductance of the package, as well as the traces and external components. Following the recommendations for output voltage >4.3V and input voltage >4.5V will improve this condition. Additional short-circuit protection can be accomplished with some external circuitry.

In an overload or short-circuit condition the LTC3440 voltage loop opens and the error amp control voltage on the VC pin slams to the upper clamp level. This condition forces boost mode operation in order to attempt to provide more output voltage and the IC hits a peak switch current limit of 2.7A. When switch current limit is reached switches B and D turn on for the remainder of the cycle to reverse the volts • seconds on the inductor. Although this prevents current run away, this condition produces four switch operation producing a current foldback characteristic and the average input current drops. The IC is trimmed to guarantee greater than 1A average input current to meet the maximum load demand, but in a short-circuit or overload condition the foldback characteristic will occur producing higher peak switch currents. To minimize this affect during this condition the following circuits can be utilized.

Restart Circuit

For a sustained short-circuit the circuit in Figure 9 will force a soft-start condition. The only design constraint is that R2/C2 time constant must be longer than the soft-start components R1/C1 to ensure start-up.
**APPLICATIONS INFORMATION**

**Simple Average Input Current Control**

A simple average current limit circuit is shown in Figure 10. Once the input current of the IC is above approximately 1A, Q1 will start sourcing current into the FB pin and lower the output voltage to maintain the average input current. Since the voltage loop is utilized to perform average current limit, the voltage control loop is maintained and the $V_C$ voltage does not slam. The averaging function of current comes from the fact that voltage loop compensation is also used with this circuit.

**TYPICAL APPLICATIONS**

3-Cell to 3.3V at 600mA Converter

![Circuit Diagram](image)

- **$V_{IN}$** = 2.7V to 4.5V
- **$L_{T}$** = 4.7µH
- **$D_{1}$** and **$D_{2}$**: CENTRAL SEMICONDUCTOR CMDSH2-3
- **$C_{1}$**: TAIYO YUDEN JMK212BJ106MG
- **$C_{2}$**: TAIYO YUDEN JMK325BJ226MM
- **$L_{1}$**: SUMIDA CDR43-4R7M
- **R_{1}**: 340k
- **R_{2}**: 200k
- **R_{3}**: 15k
- **R_{4}**: 45.3k
- **C_{3}**: 33µF
- **C_{4}**: 150pF
- **C_{5}**: 10pF

*1 = Burst Mode OPERATION
0 = FIXED FREQUENCY

Many applications

For more information [www.linear.com/LTC3440](http://www.linear.com/LTC3440)
**TYPICAL APPLICATIONS**

### 3-Cell to 5V Boost Converter with Output Disconnect

- **VIN** = 2.5V to 4.5V
- **VOUT** = 3.3V, 200mA
- Components:
  - R1: 619k
  - R2: 200k
  - C1: TAIYO YUDEN JMK212BJ106MG
  - C2: TAIYO YUDEN JMK325BJ226MM
  - D1: ON SEMICONDUCTOR MBRM120T3
  - L1: SUMIDA CDRH4D28-100

*1 = Burst Mode OPERATION
0 = FIXED FREQUENCY
** LOCATE COMPONENTS AS CLOSE TO IC AS POSSIBLE

### 3-Cell to 5V Boost Efficiency

- **VIN**: 2.7V to 4.5V
- **VOUT**: 5V, 300mA
- Components:
  - R1: 340k
  - R2: 200k
  - C1: TAIYO YUDEN JMK212BJ106MG
  - C2: TAIYO YUDEN JMK325BJ226MM
  - D1: ON SEMICONDUCTOR MBRM120T3
  - L1: SUMIDA CDRH4D28-100

*1 = Burst Mode OPERATION
0 = FIXED FREQUENCY
** LOCATE COMPONENTS AS CLOSE TO IC AS POSSIBLE

### Low Profile (<1.1mm) Li-Ion to 3.3V at 200mA Converter

- **VIN**: 2.5V to 4.2V
- **VOUT**: 3.3V, 200mA
- Components:
  - R1: 340k
  - R2: 200k
  - C1: TAIYO YUDEN JMK212BJ475MG
  - C2: TAIYO YUDEN JMK212BJ475MG

*1 = Burst Mode OPERATION
0 = FIXED FREQUENCY
fOSC = 2MHz

### Efficiency

- **VIN**: 2.5V to 4.2V
- **VOUT**: 3.3V, 200mA
- Components:
  - R1: 340k
  - R2: 200k

For more information visit www.linear.com/LTC3440
TYPICAL APPLICATIONS

**WCDMA Power Amp Power Supply with Dynamic Voltage Control**

Vin = 2.5V TO 4.2V

Li-ion

Vin = 2.5V TO 4.2V

C1: TAIYO YUDEN JMK212BJ106MG
C2: TAIYO YUDEN JMK325BJ226MM
L1: SUMIDA CDRH-4D28-100

*1 = Burst Mode OPERATION
0 = FIXED FREQUENCY
** LOCATE COMPONENTS AS CLOSE TO IC AS POSSIBLE

**GSM Modem Powered from USB or PCMCIA with 500mA Input Current Limit**

Vin = 2.5V TO 5.5V
USB/PCMCIA POWER 500mA MAX

C1: TAIYO YUDEN JMK212BJ106MG
C2: TAIYO YUDEN JMK258J225BM
L1: SUMIDA CDRH-4D28-100

*1 = Burst Mode OPERATION
0 = FIXED FREQUENCY
PACKAGE DESCRIPTION

Please refer to http://www.linear.com/designtools/packaging/ for the most recent package drawings.

DD Package
10-Lead Plastic DFN (3mm × 3mm)
(Reference LTC DWG # 05-08-1699 Rev C)

NOTE:
1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE MD-229 VARIATION OF (WEED-2).
2. DRAWING NOT TO SCALE
3. ALL DIMENSIONS ARE IN MILLIMETERS
4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
5. EXPOSED PAD SHALL BE SOLDER PLATED
6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON THE TOP AND BOTTOM OF PACKAGE

MS Package
10-Lead Plastic MSOP
(Reference LTC DWG # 05-08-1661 Rev F)

NOTE:
1. DIMENSIONS IN MILLIMETER/(INCH)
2. DRAWING NOT TO SCALE
3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
4. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX
6. SEATING PLANE AND GAUGE PLANE ARE MOUNTING PLANES ONLY. TOP AND BOTTOM OF PACKAGE SHALL BE 0.1016 ±0.0508 (.004 ±.002) MIN PER SIDE

For more information www.linear.com/LTC3440
Information furnished by Linear Technology Corporation is believed to be accurate and reliable. However, no responsibility is assumed for its use. Linear Technology Corporation makes no representation that the interconnection of its circuits as described herein will not infringe on existing patent rights.

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**REVISION HISTORY**  (Revision history begins at Rev C)

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<th>REV</th>
<th>DATE</th>
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<tr>
<td>C</td>
<td>8/14</td>
<td>Modified filter pole equation in Closing the Feedback Loop section</td>
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**TYPICAL APPLICATION**

Li-ion to 3.3V at 600mA Buck-Boost Converter

**Efficiency**

**RELATED PARTS**

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<tr>
<td>LT1613</td>
<td>550mA(ISW), 1.4MHz, High Efficiency Step-Up DC/DC Converter</td>
<td>90% Efficiency, V_IN: 0.9V to 10V, V_OUT(MIN) = 34V, I_Q = 3mA, I_SD = &lt;1µA, ThinSOT™ Package</td>
</tr>
<tr>
<td>LT1618</td>
<td>1.5A(ISW), 1.25MHz, High Efficiency Step-Up DC/DC Converter</td>
<td>90% Efficiency, V_IN: 1.6V to 18V, V_OUT(MIN) = 35V, I_Q = 1.8mA, I_SD = &lt;1µA, MS10 Package</td>
</tr>
<tr>
<td>LTC1877</td>
<td>600mA(I_OUT), 550kHz, Synchronous Step-Down DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.7V to 10V, V_OUT(MIN) = 0.8V, I_Q = 10µA, I_SD = &lt;1µA, MS8 Package</td>
</tr>
<tr>
<td>LTC1878</td>
<td>600mA(I_OUT), 550kHz, Synchronous Step-Down DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.7V to 6V, V_OUT(MIN) = 0.8V, I_Q = 10µA, I_SD = &lt;1µA, MS8 Package</td>
</tr>
<tr>
<td>LTC1879</td>
<td>1.2A(I_OUT), 550kHz, Synchronous Step-Down DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.7V to 10V, V_OUT(MIN) = 0.8V, I_Q = 15µA, I_SD = &lt;1µA, TSOP16 Package</td>
</tr>
<tr>
<td>LT1961</td>
<td>1.5A(ISW), 1.25MHz, High Efficiency Step-Up DC/DC Converter</td>
<td>90% Efficiency, V_IN: 3V to 25V, V_OUT(MIN) = 35V, I_Q = 0.9mA, I_SD = 6µA, MS8E Package</td>
</tr>
<tr>
<td>LTC3400/LTC3400B</td>
<td>600mA(ISW), 1.2MHz, Synchronous Step-Up DC/DC Converter</td>
<td>92% Efficiency, V_IN: 0.85V to 5V, V_OUT(MIN) = 5V, I_Q = 19µA/300µA, I_SD = &lt;1µA, ThinSOT Package</td>
</tr>
<tr>
<td>LTC3401</td>
<td>1A(ISW), 3MHz, Synchronous Step-Up DC/DC Converter</td>
<td>97% Efficiency, V_IN: 0.5V to 5V, V_OUT(MIN) = 6V, I_Q = 38µA, I_SD = &lt;1µA, MS10 Package</td>
</tr>
<tr>
<td>LTC3402</td>
<td>2A(ISW), 3MHz, Synchronous Step-Up DC/DC Converter</td>
<td>97% Efficiency, V_IN: 0.5V to 5V, V_OUT(MIN) = 6V, I_Q = 38µA, I_SD = &lt;1µA, MS10 Package</td>
</tr>
<tr>
<td>LTC3405/LTC3405A</td>
<td>300mA(I_OUT), 1.5MHz, Synchronous Step-Down DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.7V to 6V, V_OUT(MIN) = 0.8V, I_Q = 20µA, I_SD = &lt;1µA, ThinSOT Package</td>
</tr>
<tr>
<td>LTC3406/LTC3406B</td>
<td>600mA(I_OUT), 1.5MHz, Synchronous Step-Down DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.5V to 5.5V, V_OUT(MIN) = 0.6V, I_Q = 20µA, I_SD = &lt;1µA, ThinSOT Package</td>
</tr>
<tr>
<td>LTC3411</td>
<td>1.25A(I_OUT), 4MHz, Synchronous Step-Down DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.5V to 5.5V, V_OUT(MIN) = 0.8V, I_Q = 60µA, I_SD = &lt;1µA, MS10 Package</td>
</tr>
<tr>
<td>LTC3412</td>
<td>2.5A(I_OUT), 4MHz, Synchronous Step-Down DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.5V to 5.5V, V_OUT(MIN) = 0.8V, I_Q = 60µA, I_SD = &lt;1µA, TSOP16E Package</td>
</tr>
<tr>
<td>LTC3441/LTC3443</td>
<td>1.2A(I_OUT), 1MHz/0.6MHz, Micropower Synchronous Buck-Boost DC/DC Converter</td>
<td>95% Efficiency, V_IN: 2.4V to 5.5V, V_OUT(MIN): 2.4V to 5.25V, I_Q = 25µA, I_SD = &lt;1µA, DFN Package</td>
</tr>
</tbody>
</table>