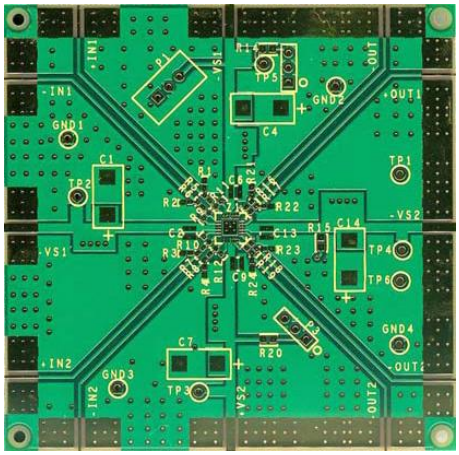
ADA4937-2 Evaluation Board

Part No. ADA4937-2YCP-EBZ



These evaluation boards are designed to help users evaluate the ADA4937-2. The evaluation board is a BARE BOARD. There are no components or amplifiers soldered to the board. These parts must be obtained separately from the table below or on the product page. Free samples may also be requested. The unpopulated board enables users to quickly customize and prototype a variety of op amp circuits, which minimizes risk and reduces time to market.

The User Guide contains all the required documentation to build the board, including schematics, assembly drawing, and Bill of Materials.

ANALOG DEVICES

Evaluation Board User Guide
UG-018

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Evaluation Board for Dual High Speed Differential Amplifiers

FEATURES

Enables quick breadboarding/prototyping
User-defined circuit configuration
Edge-mounted SMA connector provisions
Easy connection to test equipment and other circuits
Two independent circuits enhance flexibility

GENERAL DESCRIPTION

The EB-D24CP44-2Z is designed, to aid in the evaluation of dual high speed differential amplifiers. The EB-D24CP44-2Z is a bare board (that is, there are no components soldered to the board) that enables users to quickly prototype a variety of differential amplifier circuits, which minimizes risk and reduces time to market. The EB-D24CP44-2Z evaluation board supports any of Analog Devices, Inc., dual high speed differential amplifiers in 4 mm × 4 mm lead frame chip scale packages (LFCSP).

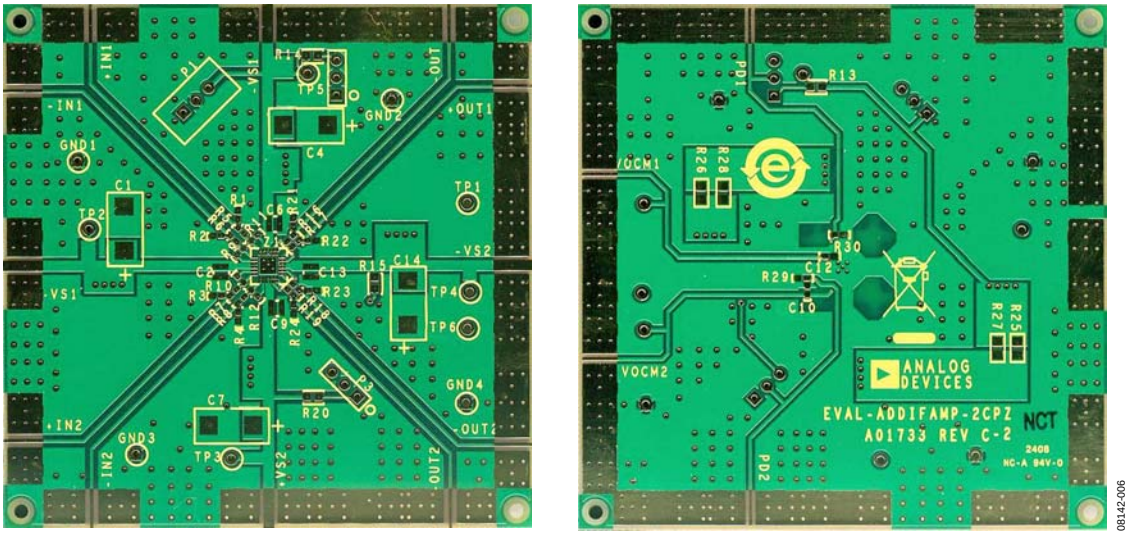
Figure 1 shows the component side and circuit side of the evaluation board. Figure 2 shows the evaluation board schematic.

The 4-layer evaluation board accepts edge-mounted SMA connectors on both inputs and outputs, which allows efficient and quick connection to test equipment or other circuitry.

The board ground plane, component placement, and power supply bypassing have been optimized for maximum circuit flexibility and performance. The evaluation board uses a variety of SMT component case sizes: 0402, 0508, 0805, and 7343.

Figure 3 and Figure 5 show the evaluation board assembly drawings. The metal layout pattern for connecting the board to the op amp and to the supporting circuitry is shown in Figure 4 and Figure 6.

DIGITAL PICTURE OF THE EVALUATION BOARD



COMPONENT SIDE CIRCUIT SIDE

NOTES
1. THE EVALUATION BOARD SILKSCREEN PART NUMBER LABELING ON YOUR BOARD MAY BE DIFFERENT FROM WHAT IS SHOWN HERE.

Figure 1. Component and Circuit Side of PCB

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REVISION HISTORY

2/10—Rev. 0 to Rev. A

Changes to General Description and Figure 1 1

7/09—Revision 0: Initial Version

EVALUATION BOARD SCHEMATIC

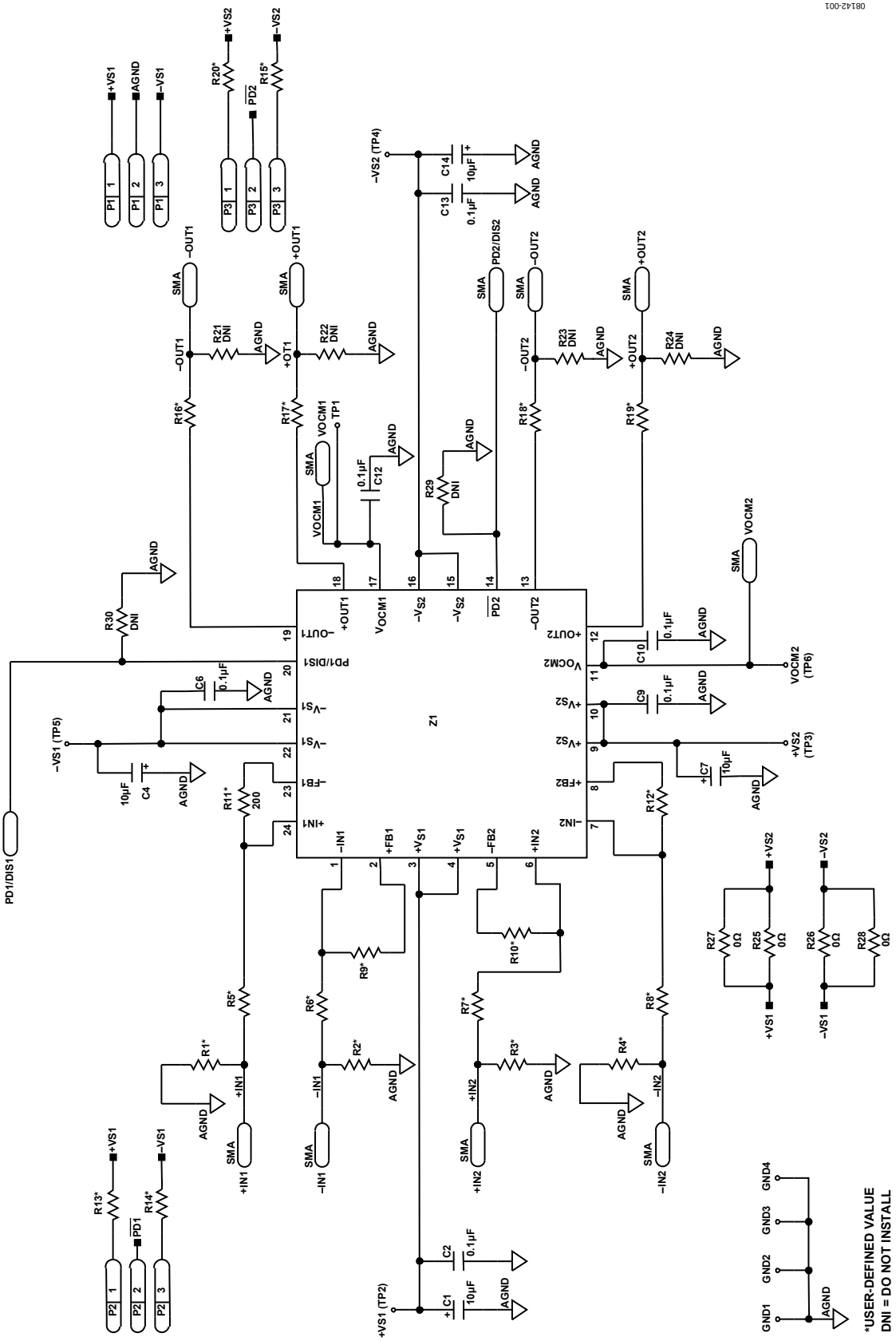

Figure 2. Dual Differential Amplifier Universal Evaluation Board Schematic

ASSEMBLY DRAWING AND BOARD LAYOUT

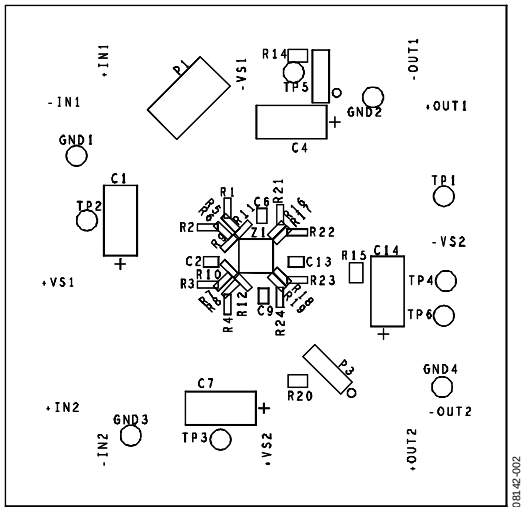

Figure 3. Board Assembly Drawing, Component Side

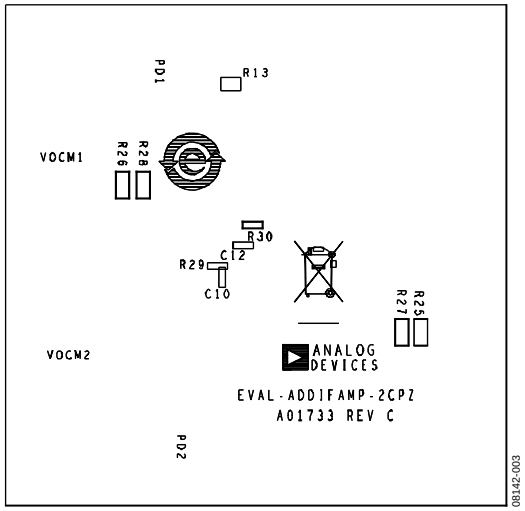

Figure 5. Board Assembly Drawing, Circuit Side

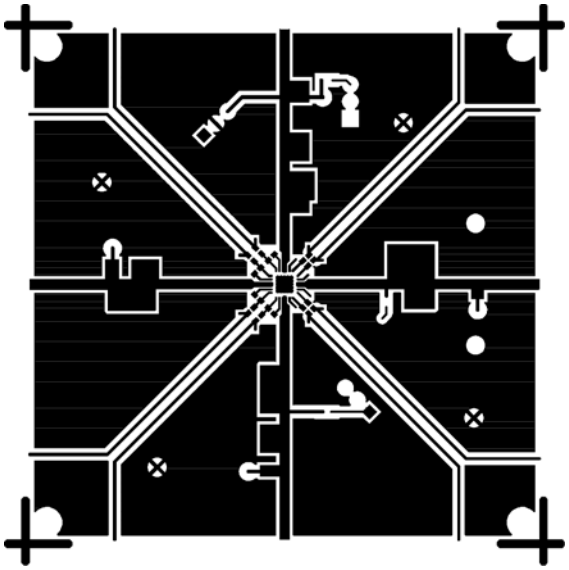

Figure 4. Board Layout Pattern, Component Side

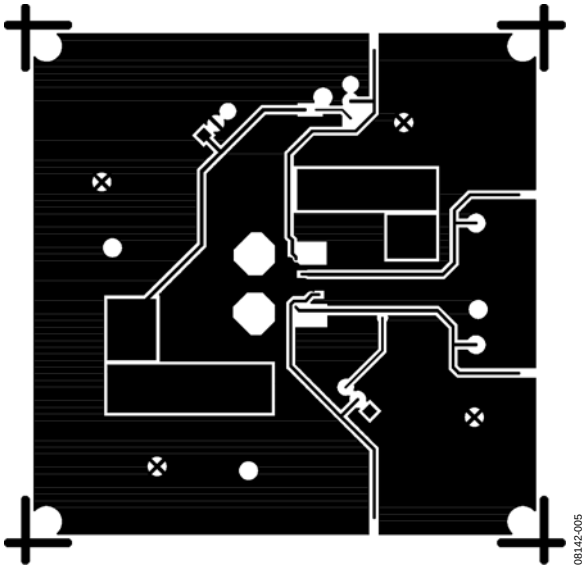

Figure 6. Board Layout Pattern, Circuit Side

ESD CAUTION

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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