

## DM74ALS09

# **Quad 2-Input AND Gate with Open Collector Outputs**

## **General Description**

This device contains four independent gates, each of which performs the logic AND function. The open-collector outputs require external pull-up resistors for proper logical operation.

**Pull-Up Resistor Equations** 

$$R_{MAX} = \frac{V_{CC} \left(Min\right) - V_{OH}}{N_1 \left(I_{OH}\right) + N_2 \left(I_{IH}\right)}$$

$$\mathsf{R}_{MIN} = \frac{\mathsf{V}_{CC} \left(\mathsf{Max}\right) - \mathsf{V}_{OL}}{\mathsf{I}_{OL} - \mathsf{N}_{3} \left(\mathsf{I}_{|L}\right)}$$

Where:

 $N_1$  ( $I_{OH}$ ) = total maximum output high current for all outputs tied to pull-up resistor

 $N_2$  ( $I_{IH}$ ) = total maximum input high current for all inputs tied to pull-up resistor

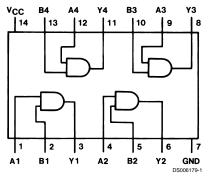
 $N_3$  ( $I_{IL}$ ) = total maximum input low current for all inputs tied to pull-up resistor

#### **Features**

- Switching specifications at 50 pF
- Switching specifications guaranteed over full temperature and V<sub>CC</sub> range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky and low power Schottky TTL counterpart
- Improved AC performance over Schottky and low power Schottky counterparts

## **Connection Diagram**

#### **Dual-In-Line Package**



Order Number DM74ALS09M or DM74ALS09N See Package Number M14A or N14A

### **Function Table**

Y = AB

In	outs	Output		
Α	В	Y		
L	L	L		
L	Н	L		
Н	L	L		
Н	н	н		

H = High Logic Level L = Low Logic Level Absolute Maximum Ratings (Note 1) DM74ALS 0°C to +70°C Storage Temperature Range -65°C to +150°C

Supply Voltage 7V Storage Temperature Range 75 Typical  $\theta_{\rm JA}$  N Package 86.5°C/W N Package 86.5°C/W

116.0°C/W

Operating Free Air Temperature
Range

M Package

## **Recommended Operating Conditions**

Symbol	Parameter	DM74ALS09			Units
		Min	Nom	Max	
V <sub>cc</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	High Level Input Voltage	2			V
V <sub>IL</sub>	Low Level Input Voltage			0.8	V
V <sub>OH</sub>	High Level Output Voltage			5.5	V
I <sub>OL</sub>	Low Level Output Current			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## **Electrical Characteristics**

over recommended operating free air temperature range. All typical values are measured at  $V_{CC}$  = 5V,  $T_A$  = 25°C.

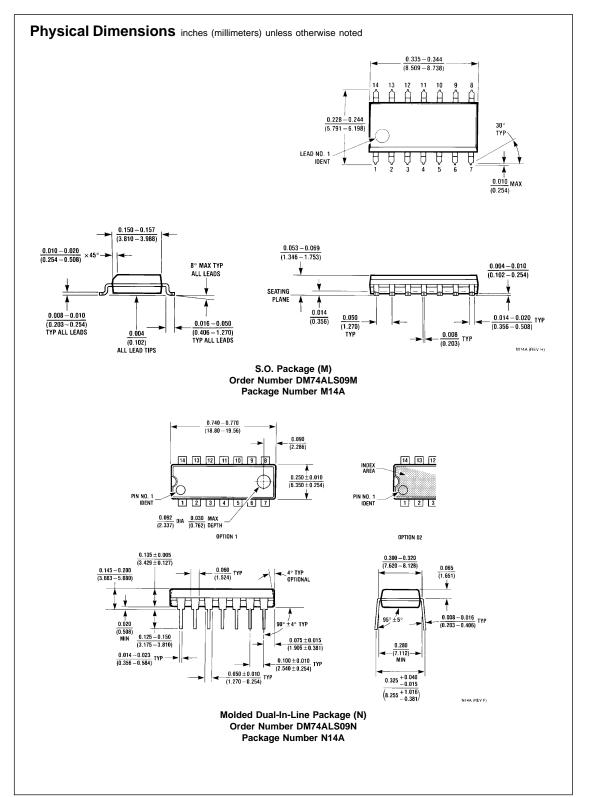
Symbol	Parameter	Con	ditions	Min	Tvn	Max	Units
Syllibol	Parameter	Con	uitions	IVIIII	Тур	IVIAX	Units
V <sub>IK</sub>	Input Clamp Voltage	$V_{CC} = 4.5V, I_{I} = -18 \text{ mA}$				-1.5	V
I <sub>OH</sub>	High Level Output Current	V <sub>CC</sub> = 4.5V, V <sub>OH</sub> = 5.5V				100	μA
V <sub>OL</sub>	Low Level Output	V <sub>CC</sub> = 4.5V	I <sub>OL</sub> =4 mA		0.25	0.4	V
	Voltage		$I_{OL} = 8 \text{ mA}$		0.35	0.5	V
I <sub>I</sub>	Input Current @ Max	$V_{CC}$ = 5.5V, $V_{IH}$	= 7V			0.1	mA
	Input Voltage						
I <sub>IH</sub>	High Level Input Current	V <sub>CC</sub> = 5.5V, V <sub>IH</sub> = 2.7V				20	μA
I <sub>IL</sub>	Low Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.1	mA
I <sub>cc</sub>	Supply Current	V <sub>CC</sub> = 5.5V	Outputs High		1.3	2.4	mA
			Outputs Low		2.2	4	mA

### **Switching Characteristics**

over recommended operating free air temperature range. (Note 2)

Symbol	Parameter	Conditions	DM74	DM74ALS09	
			Min	Max	
t <sub>PLH</sub>	Propagation Delay Time	V <sub>CC</sub> = 4.5V to 5.5V	23	54	ns
	Low to High Level Output	$R_{L} = 2 k\Omega, C_{L} = 50 pF$			
t <sub>PHL</sub>	Propagation Delay Time		5	15	ns
	High to Low Level Output				

Note 2: See Section 1 for test waveforms and output load.



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