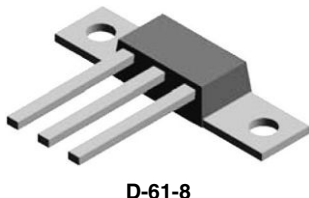
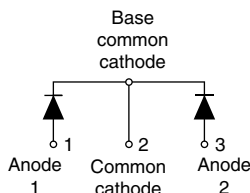


Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

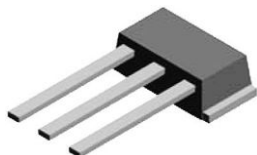
VS-83CNQ...APbF



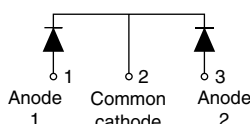
D-61-8



VS-83CNQ...ASMPbF



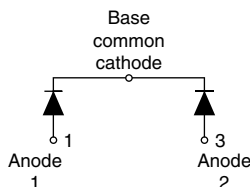
D-61-8-SM



VS-83CNQ...ASLPbF



D-61-8-SL



FEATURES

- 175 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level



Available
RoHS*
COMPLIANT

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

PRODUCT SUMMARY

$I_{F(AV)}$	2 x 40 A
V_R	80 V/100 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	80	A
V_{RRM}		80/100	V
I_{FSM}	$t_p = 5 \mu s$ sine	7000	A
V_F	40 Apk, $T_J = 125^\circ C$ (per leg)	0.67	V
T_J	Range	- 55 to 175	$^\circ C$

VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-83CNQ080APbF	VS-83CNQ100APbF	UNITS
Maximum DC reverse voltage	V_R	80	100	V
Maximum working peak reverse voltage	V_{RWM}			

* Pb containing terminations are not RoHS compliant, exemptions may apply

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 132\text{ }^{\circ}\text{C}$, rectangular waveform	80	A
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse	7000	
		10 ms sine or 6 ms rect. pulse	720	
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25\text{ }^{\circ}\text{C}$, $I_{AS} = 1\text{ A}$, $L = 30\text{ mH}$	15	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	1	A

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	40 A	0.81	V
		80 A	1.00	
		40 A	0.67	
		80 A	0.82	
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	1.5	mA
		$T_J = 125\text{ }^{\circ}\text{C}$	35	
Maximum junction capacitance per leg	C_T	$V_R = 5\text{ V}_{DC}$ (test signal range 100 kHz to 1 MHz), $25\text{ }^{\circ}\text{C}$	1400	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body	5.5	nH
Maximum voltage rate of change	dV/dt	Rated V_R	10 000	V/ μs

Note(1) Pulse width < 300 μs , duty cycle < 2 %**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		- 55 to 175	$^{\circ}\text{C}$
Maximum thermal resistance, junction to case per leg per package	R_{thJC}	DC operation See fig. 4	0.85	$^{\circ}\text{C/W}$
		DC operation	0.42	
Typical thermal resistance, case to heatsink (D-61-8 only)	R_{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
Approximate weight			7.8	g
			0.28	oz.
Mounting torque minimum maximum		Recommended hardware 3M stainless screw	12 (10)	kgf · cm
			24 (20)	(lbf · in)
Marking device		Case style D-61	83CNQ080A	
			83CNQ100A	
		Case style D-61-8-SM	83CNQ080ASM	
			83CNQ100ASM	
		Case style D-61-8-SL	83CNQ080ASL	
			83CNQ100ASL	

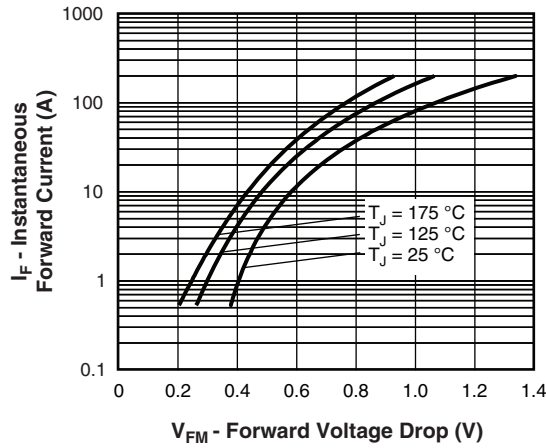


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

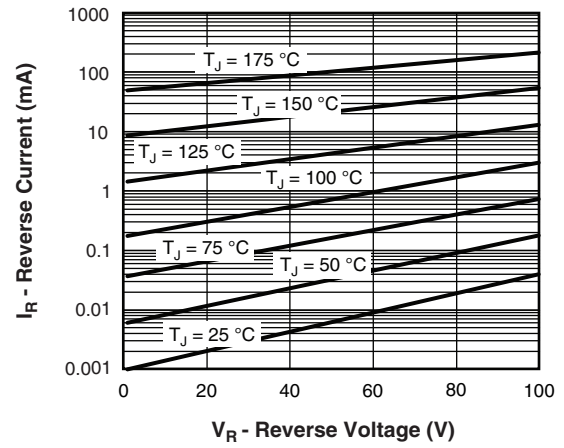


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

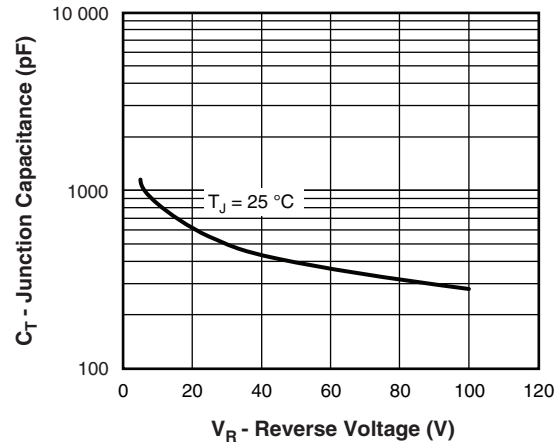


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

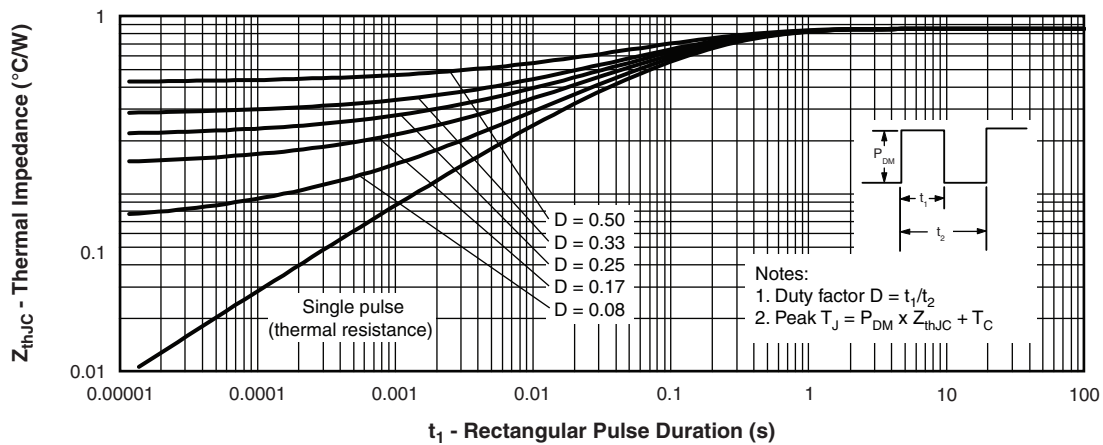


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

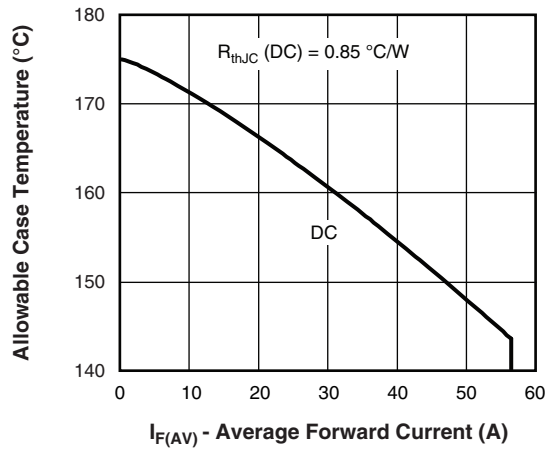


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

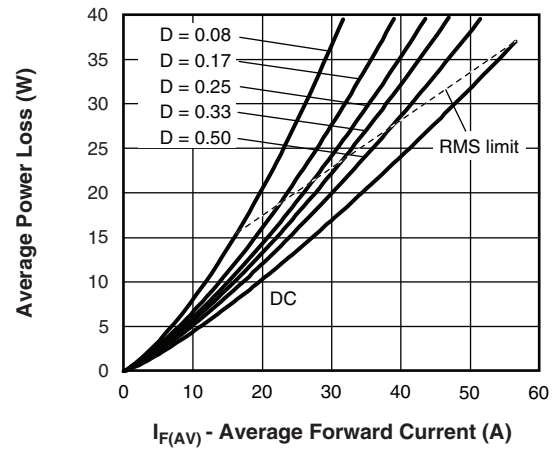


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

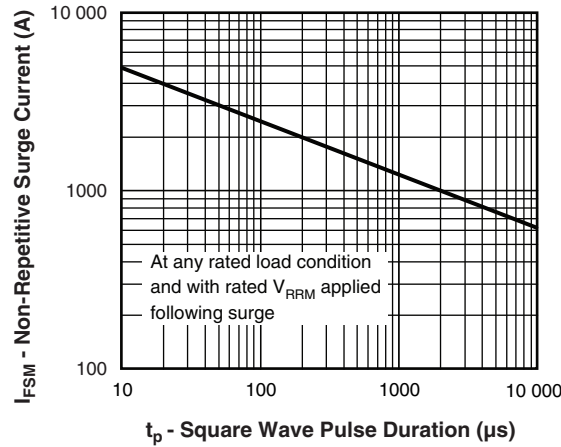


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

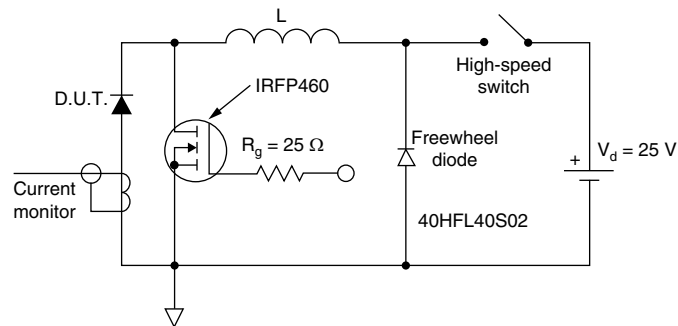


Fig. 8 - Unclamped Inductive Test Circuit



ORDERING INFORMATION TABLE

Device code	VS-	83	C	N	Q	100	A	PbF
	①	②	③	④	⑤	⑥	⑦	⑧

- | | | | |
|----------|---|------------------------------|---------------------------|
| 1 | - | HPP product suffix | |
| 2 | - | Current rating (80 A) | |
| 3 | - | Circuit configuration: | |
| | | C = Common cathode | |
| 4 | - | Package: | |
| | | N = D-61 | |
| 5 | - | Schottky "Q" series | |
| 6 | - | Voltage ratings | 080 = 80 V
100 = 100 V |
| 7 | - | Package style: | |
| | | • A = D-61-8 | |
| | | • ASM = D-61-8-SM | |
| | | • ASL = D-61-8-SL | |
| 8 | - | • None = Standard production | |
| | | • PbF = Lead (Pb)-free | |

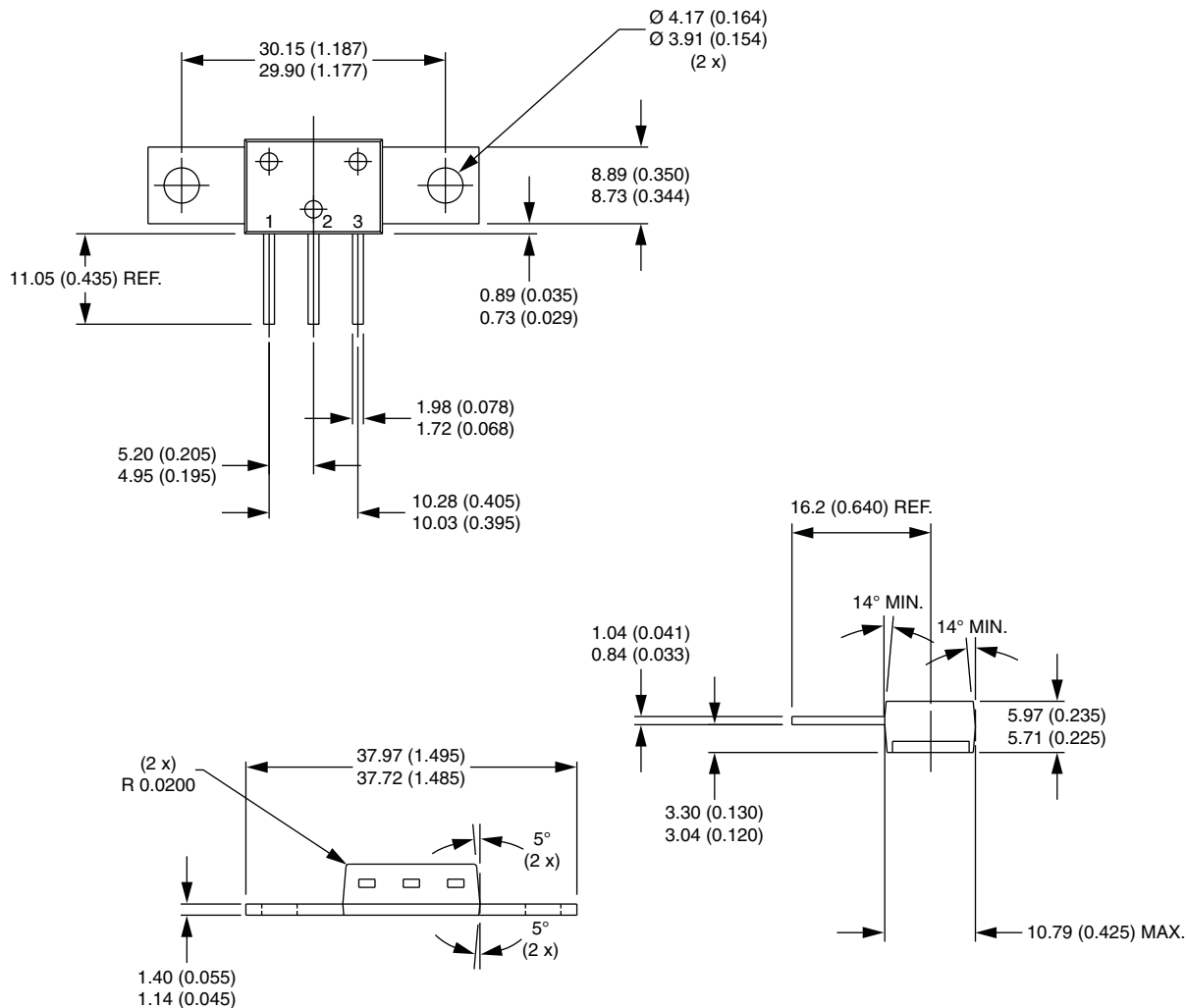
Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95354
Part marking information	www.vishay.com/doc?95356
SPICE model	www.vishay.com/doc?95290



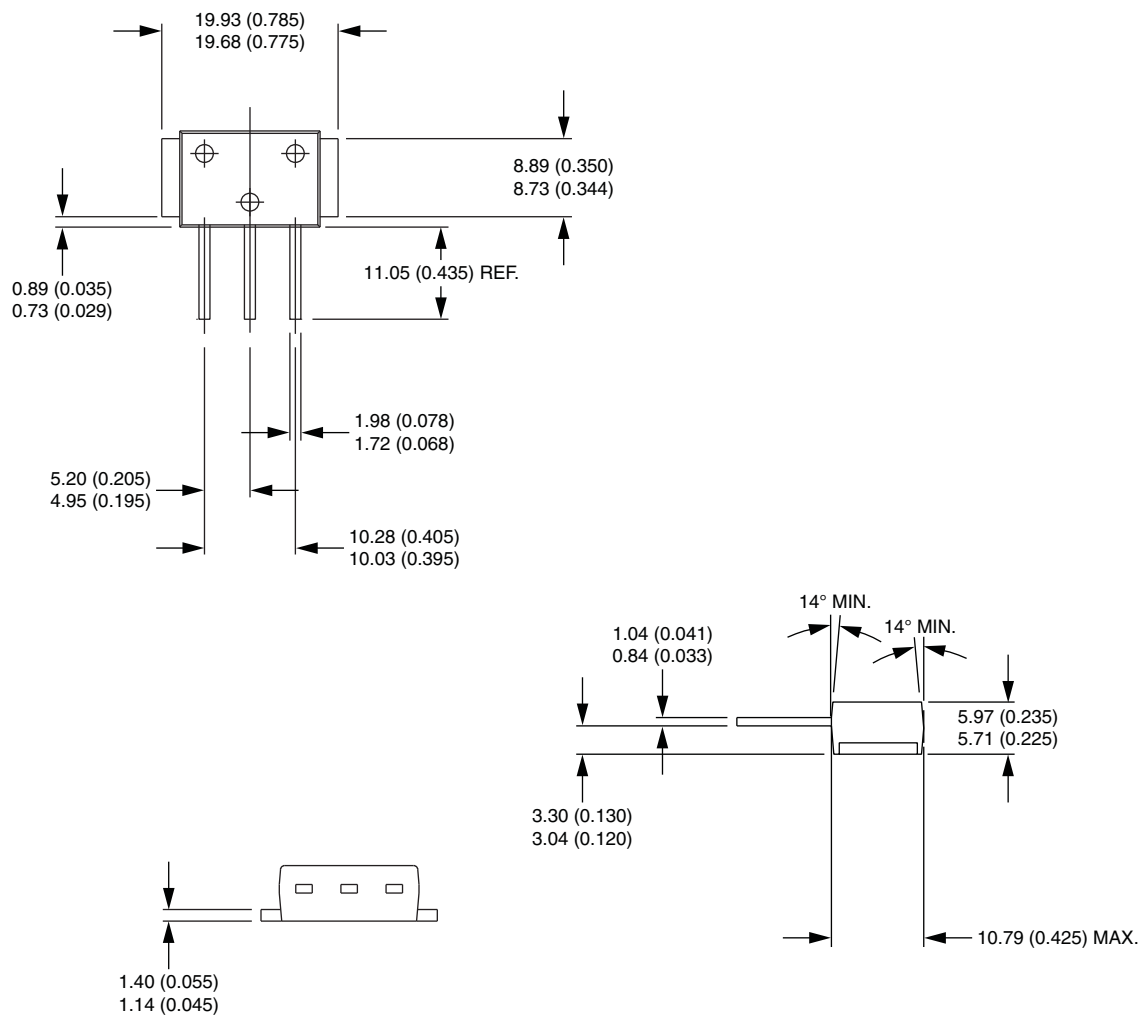
D-61-8, D-61-8-SM, D-61-8-SL

DIMENSIONS - D-61-8 in millimeters (inches)



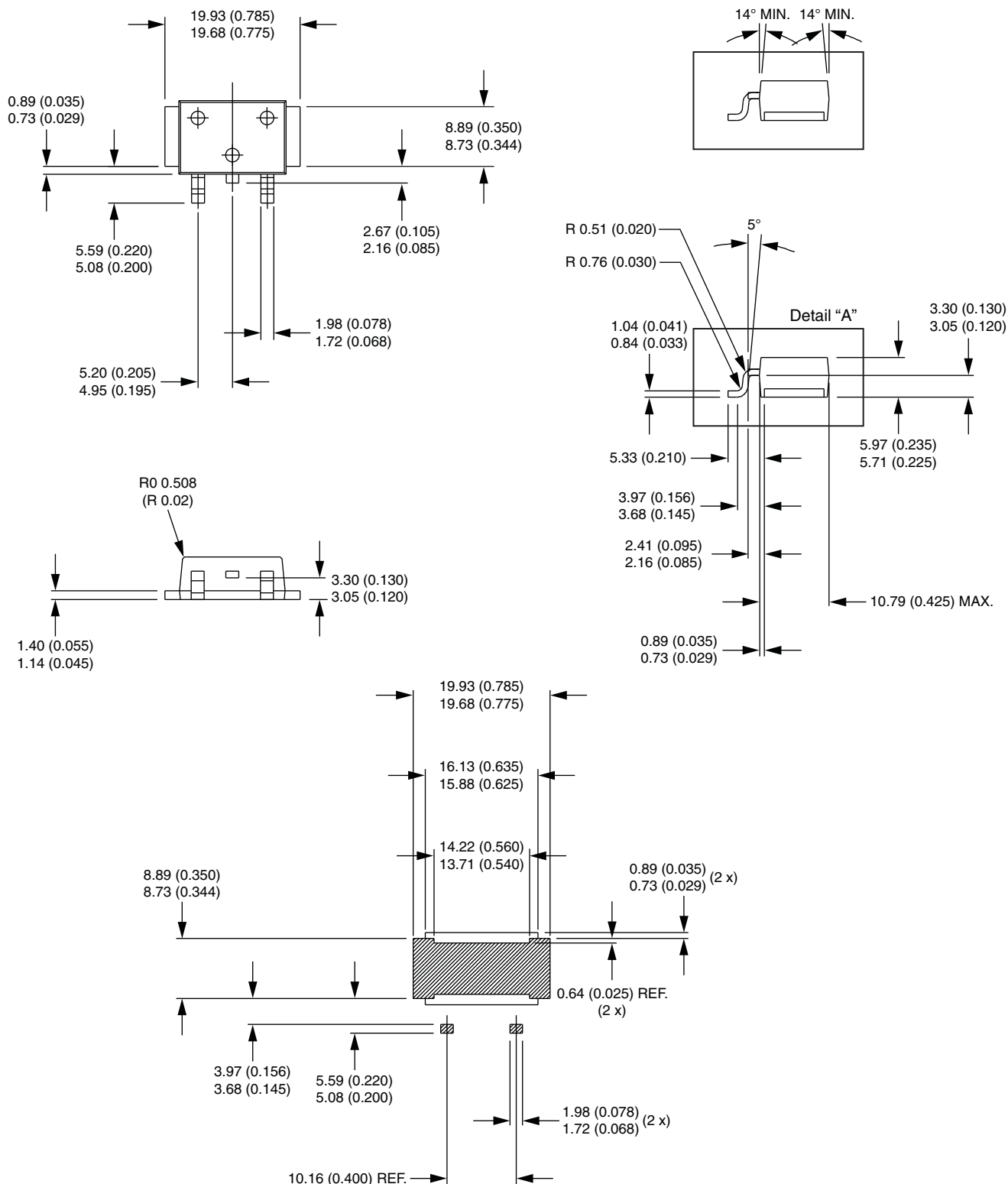


DIMENSIONS - D-61-8-SM in millimeters (inches)





DIMENSIONS - D-61-8-SL in millimeters (inches)





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