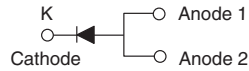


## Ultrafast Rectifier, 6 A FRED Pt<sup>®</sup>



TO-277A (SMPC)



### FEATURES

- Ultrafast recovery time, reduced  $Q_{rr}$ , and soft recovery
- 175 °C maximum operating junction temperature
- For PFC, CRM snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 17
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?999912](http://www.vishay.com/doc?999912)


**RoHS**  
 COMPLIANT  
 HALOGEN  
**FREE**

PRODUCT SUMMARY	
Package	TO-277A (SMPC)
$I_{F(AV)}$	6 A
$V_R$	600 V
$V_F$ at $I_F$	1.3 V
$t_{rr}$ (typ.)	42 ns
$T_J$ max.	175 °C
Diode variation	Single die

### DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current	$I_{F(AV)}$	$T_{Sp} = 150\text{ °C}$	6	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$	120	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\ \mu\text{A}$	600	-	-	V
Forward voltage	$V_F$	$I_F = 6\text{ A}$	-	1.10	1.30	
		$I_F = 6\text{ A}, T_J = 150\text{ °C}$	-	0.95	1.15	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	5	$\mu\text{A}$
		$T_J = 150\text{ °C}, V_R = V_R$ rated	-	25	150	
Junction capacitance	$C_T$	$V_R = 600\text{ V}$	-	8	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	$t_{rr}$	$I_F = 1\text{ A}$ , $dI_F/dt = 50\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	42	-	ns	
		$I_F = 0.5\text{ A}$ , $I_R = 1\text{ A}$ , $I_{rr} = 0.25\text{ A}$	-	-	60		
		$T_J = 25\text{ }^\circ\text{C}$	-	58	-		
		$T_J = 125\text{ }^\circ\text{C}$	-	85	-		
Peak recovery current	$I_{RRM}$	$I_F = 6\text{ A}$ $dI_F/dt = 500\text{ A}/\mu\text{s}$ $V_R = 400\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$	-	10	-	A
			$T_J = 125\text{ }^\circ\text{C}$	-	15	-	
Reverse recovery charge	$Q_{rr}$		$T_J = 25\text{ }^\circ\text{C}$	-	290	-	nC
			$T_J = 125\text{ }^\circ\text{C}$	-	620	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-65	-	175	$^\circ\text{C}$
Thermal resistance, junction to solder pad	$R_{thJ-Sp}$		-	2.4	3.5	$^\circ\text{C}/\text{W}$
Approximate weight			0.1			g
			0.0035			oz.
Marking device		Case style TO-277A (SMPC)	NEU6			

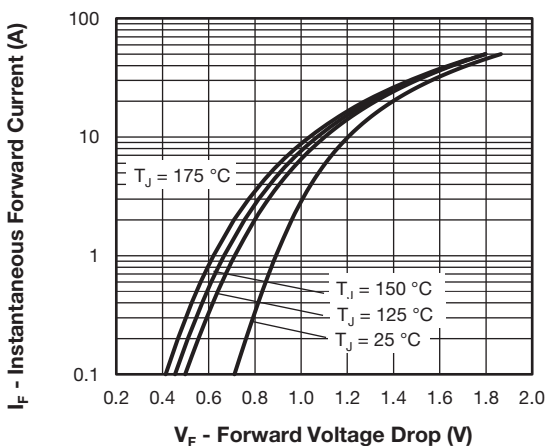


Fig. 1 - Typical Forward Voltage Drop Characteristics

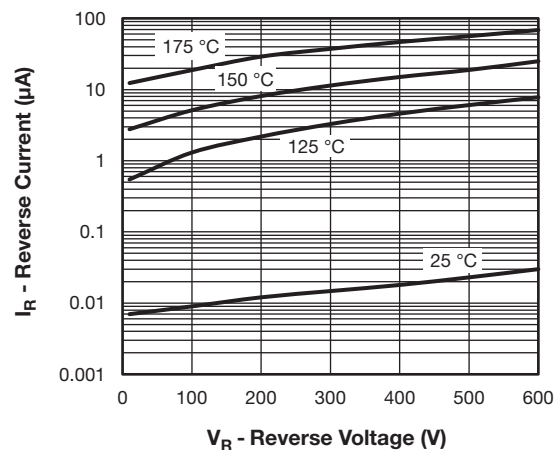


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

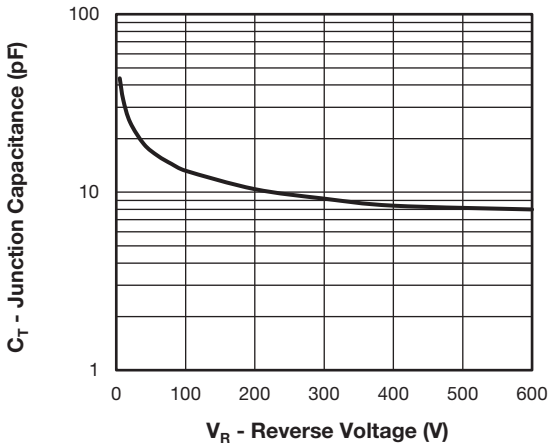


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

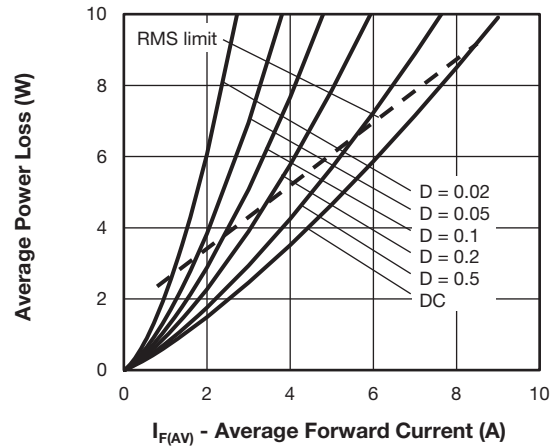


Fig. 5 - Forward Power Loss Characteristics

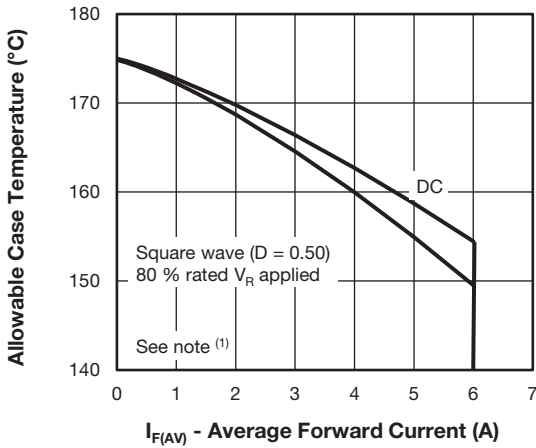


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

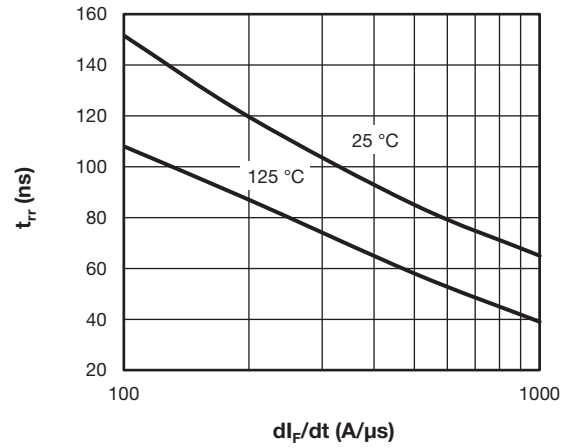


Fig. 6 - Typical Reverse Recovery Time vs.  $di_F/dt$

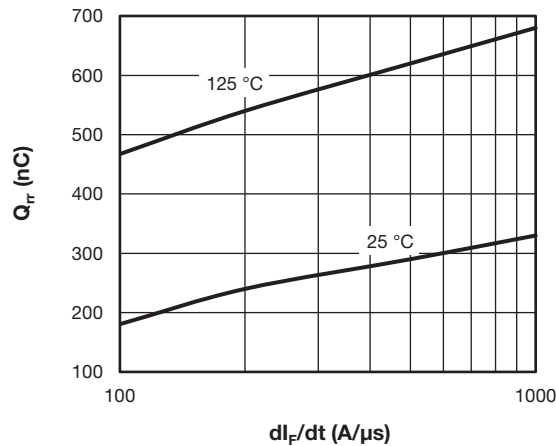


Fig. 7 - Typical Stored Charge vs.  $di_F/dt$

**Note**

- (1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  
 $Pd$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 5);  
 $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$



- (1)  $dI_F/dt$  - rate of change of current through zero crossing
- (2)  $I_{RRM}$  - peak reverse recovery current
- (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.
- (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$
- $$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$
- (5)  $dI_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 8 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	<b>VS-</b>	<b>6</b>	<b>E</b>	<b>S</b>	<b>U</b>	<b>06</b>	<b>-M3</b>
	①	②	③	④	⑤	⑥	⑦

- ① - Vishay Semiconductors product
- ② - Current rating (6 = 6 A)
- ③ - Circuit configuration:  
E = single diode
- ④ - S = SMPC package
- ⑤ - Process type,  
U = ultrafast recovery
- ⑥ - Voltage code (06 = 600 V)
- ⑦ - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-6ESU06-M3/86A	1500	1500	7" diameter plastic tape and reel
VS-6ESU06-M3/87A	6500	6500	13" diameter plastic tape and reel

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95570">www.vishay.com/doc?95570</a>
Part marking information	<a href="http://www.vishay.com/doc?95565">www.vishay.com/doc?95565</a>
Packaging information	<a href="http://www.vishay.com/doc?88869">www.vishay.com/doc?88869</a>



## TO-277A (SMPC)

**DIMENSIONS** in inches (millimeters)



### Mounting Pad Layout



Conform to JEDEC® TO-277A



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**Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.**

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