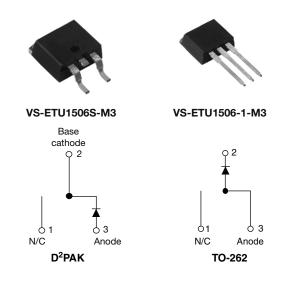
# VS-ETU1506S-M3, VS-ETU1506-1-M3



**Vishay Semiconductors** 

# Ultrafast Rectifier, 15 A FRED Pt<sup>®</sup>



PRODUCT SUMMARY	
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA
I <sub>F(AV)</sub>	15 A
V <sub>R</sub>	600 V
V <sub>F</sub> at I <sub>F</sub>	1.9 V
t <sub>rr</sub> (typ.)	24 ns
T <sub>J</sub> max.	175 °C
Diode variation	Single die

#### FEATURES

- Low forward voltage drop
- Ultrafast recovery time
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21
  FREE
  definition
- Designed and qualified according to JEDEC-JESD47

### **DESCRIPTION/APPLICATIONS**

State of the art, ultralow  $V_F$ , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

#### APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 143 °C	15	٨			
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	160	A			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER SYMBOL TEST CONDITIONS		TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-			
For and allows	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	1.35	1.9	V		
Forward voltage		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.1	1.3			
Reverse leakage current	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	0.01	15			
neverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	20	200	μA		
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	12	-	pF		
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH		

Revision: 09-Nov-11

1

Document Number: 93590

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**RoHS** COMPLIANT



www.vishay.com

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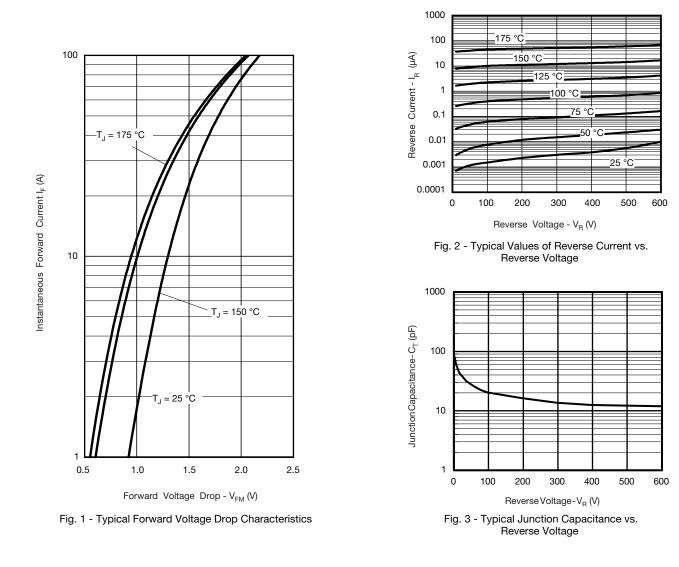
DYNAMIC RECOVERY CH	IARACTER	<b>RISTICS</b> ( $T_J = 25$	°C unless otherw	ise speci	fied)		
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 1$	00 A/µs, V <sub>R</sub> = 30 V	-	24	28	
Reverse recovery time	+	I <sub>F</sub> = 15 A, dI <sub>F</sub> /dt =	100 A/µs, V <sub>R</sub> = 30 V	-	36	47	ns
Neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	40	-	115
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 390 V	-	87	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	5	-	А
reak recovery current		T <sub>J</sub> = 125 °C		-	9.0	-	~
	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	107	-	С
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	430	-	U
Reverse recovery time	t <sub>rr</sub>		I <sub>F</sub> = 15 A	-	53	-	ns
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	dI <sub>F</sub> /dt = 800 A/µs	-	25	-	А
Reverse recovery charge	Q <sub>rr</sub>		V <sub>R</sub> = 390 V	-	730	-	nC

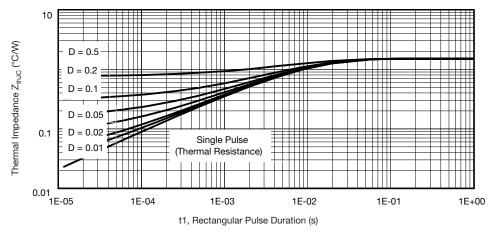
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C		
Thermal resistance, junction to case	R <sub>thJC</sub>		-	1.3	1.51	°C/W		
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70			
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-			
Maight			-	2.0	-	g		
Weight			-	0.07	-	oz.		
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)		
Marking davias		Case style D <sup>2</sup> PAK modified		ETU1506S				
Marking device		Case style TO-262	ETU1506-1					

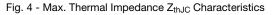


# VS-ETU1506S-M3, VS-ETU1506-1-M3

### **Vishay Semiconductors**





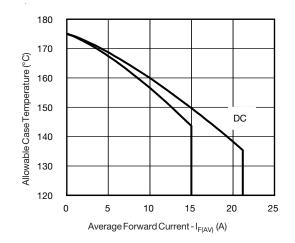


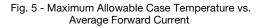
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# VS-ETU1506S-M3, VS-ETU1506-1-M3

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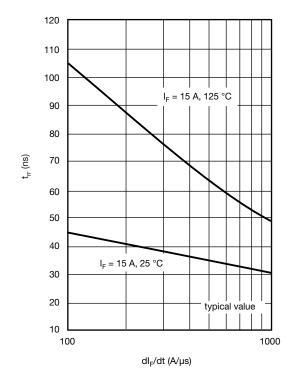


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

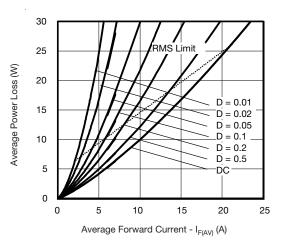
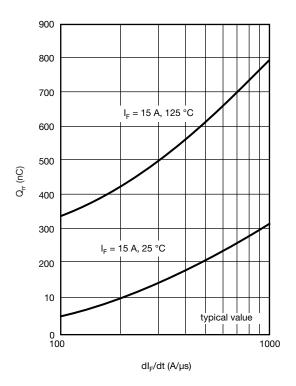
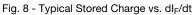


Fig. 6 - Forward Power Loss Characteristics





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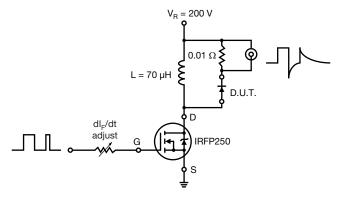


Fig. 9 - Reverse Recovery Parameter Test Circuit

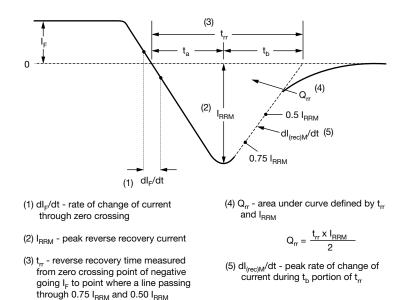


Fig. 10 - Reverse Recovery Waveform and Definitions

extrapolated to zero current.



Dev

# VS-ETU1506S-M3, VS-ETU1506-1-M3

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#### **ORDERING INFORMATION TABLE**

vice code	VS-	E	т	U	15	06	S	TRL	-M3
	1	2	3	4	5	6	7	8	9
		E = Single diode      -    T = TO-220      -    U = Ultrafast recovery time      -    Current code (15 = 15 A)							
		• \$	= D <sup>2</sup> PA = TO-2	ĸ	,				
	8	• • No	one = Ti	ube (50	pieces)				
	-			be and r				•	-
	-	• TF	RR = Ta	pe and ı	eel (rigl	nt orient	ted, for	D <sup>2</sup> PAK	packag
	9	· -M3	3 = Halo	gen-free	, RoHS	complia	ant, and	d termin	ations I

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-ETU1506S-M3	50	1000	Antistatic plastic tube				
VS-ETU1506-1-M3	50	1000	Antistatic plastic tube				
VS-ETU1506STRR-M3	800	800	13" diameter reel				
VS-ETU1506STRL-M3	800	800	13" diameter reel				

LINKS TO RELATED DOCUMENTS							
Dimensions	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95046					
	TO-262AA	www.vishay.com/doc?95419					
Part marking information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95444					
	TO-262AA	www.vishay.com/doc?95443					
Packaging information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95032					

## **Outline Dimensions**

**Vishay Semiconductors** 

D<sup>2</sup>PAK



Conforms to JEDEC outline D<sup>2</sup>PAK (SMD-220) в Pad layout (2)(3)A 11.00 MIN.-(E) F (0.43)ŧ (3) L1 4 ( |(0.38)<sup>MIN.</sup> (D1) (3) Detail A D 17.90 (0.70) Н 15.00 (0.625) (2) З 0.15)<sup>0.01</sup> Ľ L2 Ĥ ţ В В 2.32 MIN. (0.08) 2.64 (0.103) 2.41 (0.096) (3)Ċ 2 x b2 С View A - A 2 x h // ± 0.004 M B ⊕ 0.010 M A M B Base Plating (4) Metal 2 x e Н b1, b3 Gauge plane c1 (4) (c) В 0° to 8° ŧ. Seating Lead assignments plane L3 A1 Lead tip (b, b2) Diodes Section B - B and C - C 1. - Anode (two die)/open (one die) Scale: None 2., 4. - Cathode Detail "A" 3. - Anode

Rotated 90 °CW Scale: 8:1

SYMBOL	MILLIMETERS		INCHES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
с	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing per ASME Y14.5 M-1994  $\,$ 

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

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### **DIMENSIONS** in millimeters and inches

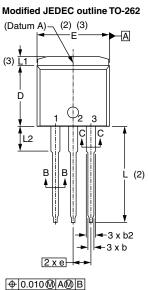


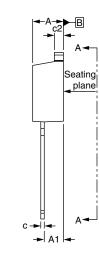
## **Outline Dimensions**

**Vishay Semiconductors** 

**TO-262** 

#### **DIMENSIONS** in millimeters and inches

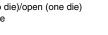


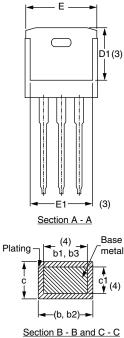


Lead assignments



**Diodes** 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode





Scale: None

SYMBOL	MILLIM	ETERS	INC	NOTEO	
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.10		
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

#### Notes

Revision: 04-Oct-10

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.