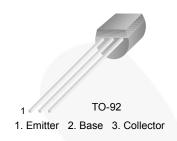


KSP44 / KSP45 NPN Epitaxial Silicon Transistor

Features

- High-Voltage Transistor
- Collector-Emitter Voltage: V_{CEO} = KSP44: 400 V KSP45: 350 V



Ordering Information

Part Number	Top Mark	Package	Packing Method
KSP44BU	KSP44	TO-92 3L	Bulk
KSP44TA	KSP44	TO-92 3L	Ammo
KSP44TF	KSP44	TO-92 3L	Tape and Reel
KSP45TA	KSP45	TO-92 3L	Ammo

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit		
		KSP44	500	V	
V _{CBO}	Collector-Base Voltage	KSP45	400		
V _{CEO} Co	Collector-Emitter Voltage	KSP44	400	V	
		KSP45	350		
V _{EBO}	Emitter-Base Voltage		6	V	
۱ _C	Collector Current		300	mA	
TJ	Junction Temperature		150	°C	
T _{STG}	Storage Temperature		-55 to 150	°C	

October 2014

Thermal Characteristics⁽¹⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value	Unit
р	Power Dissipation	T _A = 25°C	625	mW
PD		T _C = 25°C	1.5	W
R _{θJC}	Thermal Resistance, Junction-to-Case		83.3	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient		200	°C/W

Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

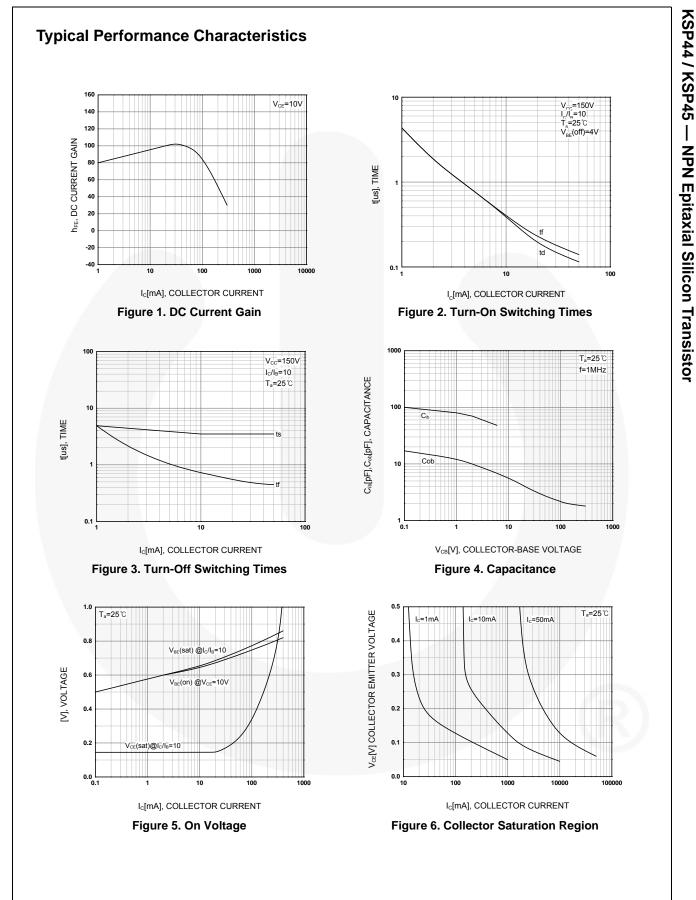
Electrical Characteristics

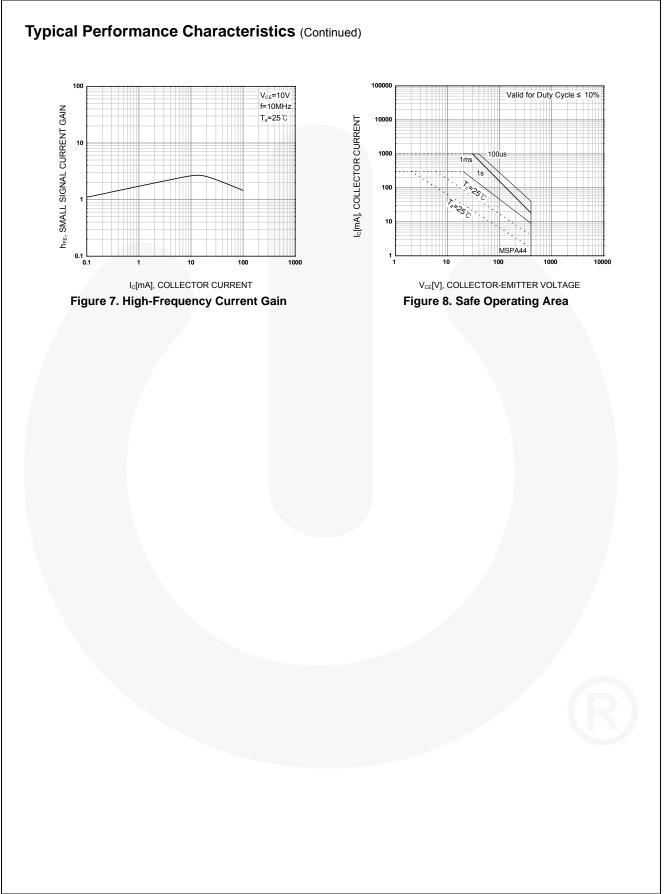
Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

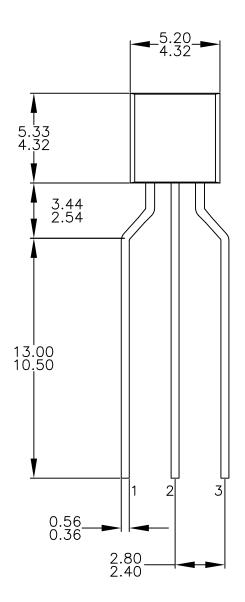
Symbol	Parameter		Conditions	Min.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	KSP44	l _C = 100 μA, l _E = 0	500		v
		KSP45		400		
D\/	Collector-Emitter Breakdown Voltage ⁽²⁾	KSP44	- 1 m - 0	400		- v
BV _{CEO}		KSP45		350		
BV_{EBO}	Emitter-Base Breakdown Voltage		$I_{E} = 100 \ \mu A, I_{C} = 0$	6		V
	Collector Cut-Off Current	KSP44	V_{CB} = 400 V, I _E = 0		0.1	- μΑ
I _{CBO}		KSP45	V_{CB} = 320 V, I _E = 0		0.1	
I _{CES}	Collector Cut-Off Current	KSP44	V_{CE} = 400 V, I _B = 0		0.5	- μA
		KSP45	V_{CE} = 320 V, I _B = 0		0.5	μΑ
I _{EBO}	Emitter Cut-Off Current		$V_{EB} = 4 V, I_{C} = 0$		0.1	μA
	DC Current Gain ⁽²⁾		V_{CE} = 10 V, I _C = 1 mA	40		
h			V_{CE} = 10 V, I _C = 10 mA	50	200	
h _{FE}			V_{CE} = 10 V, I _C = 50 mA	45		
			V_{CE} = 10 V, I _C = 100 mA	40		
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽²⁾		I _C = 1 mA, I _B = 0.1 mA		0.40	v
			I _C = 10 mA, I _B = 1 mA		0.50	
			I _C = 50 mA, I _B = 5 mA		0.75	
V _{BE} (sat)	Base-Emitter Saturation Voltage ⁽²⁾		I _C = 10 mA, I _B = 1 mA		0.75	V
C _{ob}	Output Capacitance		V _{CB} = 20 V, I _E = 0, f = 1 MHz		7	pF

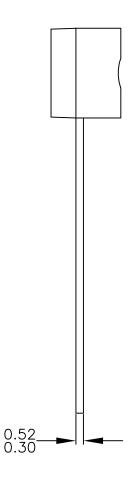
Note:

2. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2%.



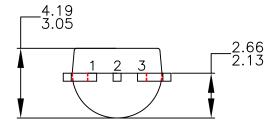


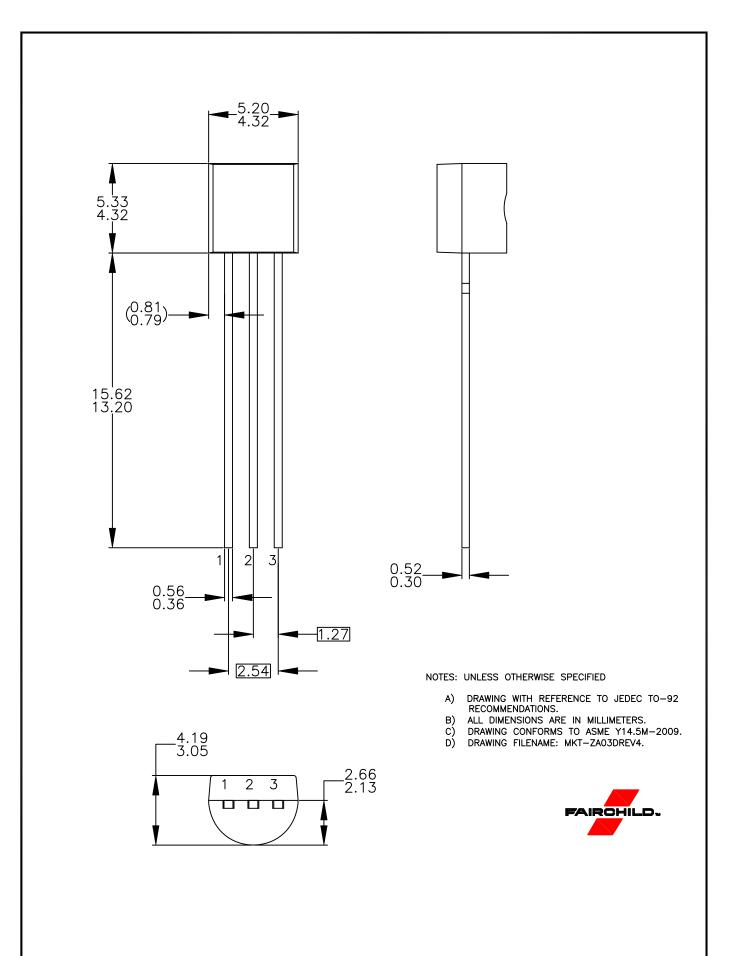


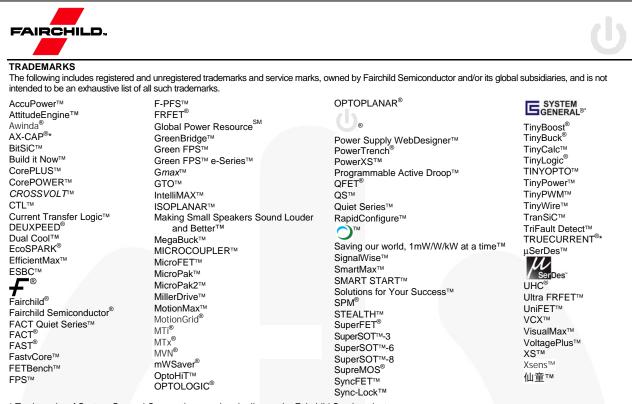


NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR. Α.
- В. С. D. Е.







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