



# DB2441700L

Silicon epitaxial planar type

For rectification

■ Features

- Low forward voltage VF
- Forward current (Average) IF(AV) = 5 A rectification is possible
- Halogen-free / RoHS compliant  
 (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)

■ Marking Symbol: 4W

■ Packaging

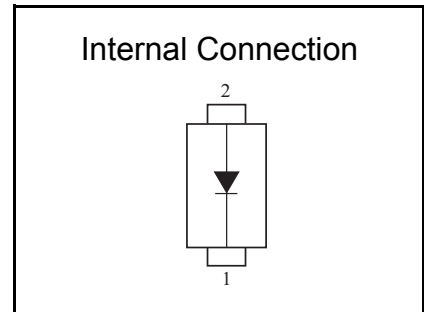
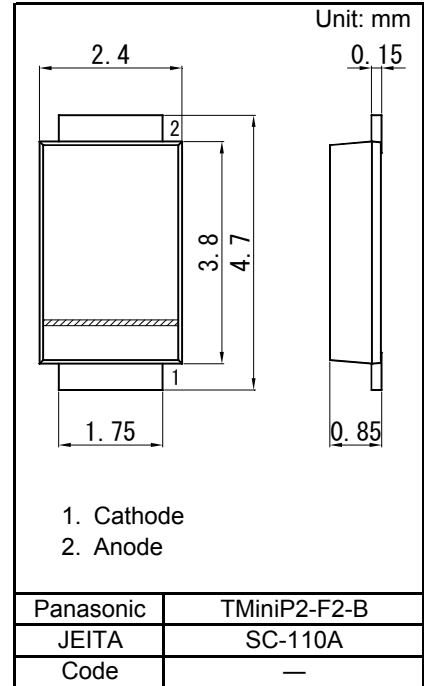
Embossed type (Thermo-compression sealing) : 3 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Reverse voltage	VR	40	V
Repetitive peak reverse voltage	VRRM	40	V
Forward current (Average) <sup>*1</sup>	IF(AV)	5.0	A
Non-repetitive peak forward surge current <sup>*2</sup>	IFSM	50	A
Junction temperature	Tj	125	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-40 to +125	°C

Note: \*1 For embedded alumina substrate (substrate size: 5 cm× 5 cm)

\*2 50 Hz sine wave 1 cycle (Non-repetitive peak current)





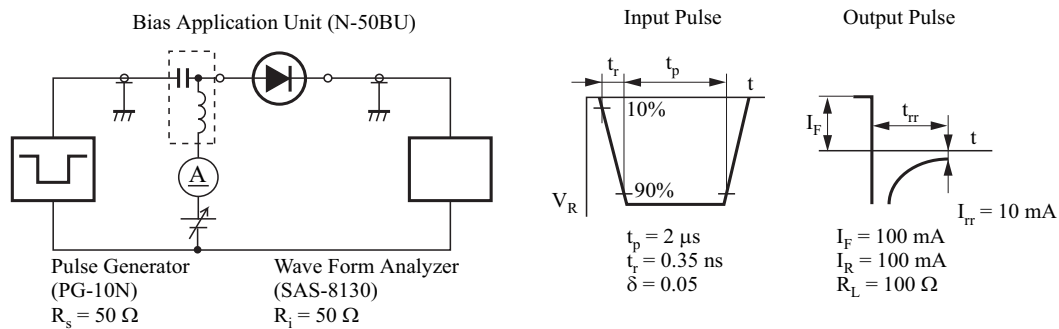
■ Electrical Characteristics  $T_a = 25\text{ }^\circ\text{C} \pm 3\text{ }^\circ\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 5.0 A		0.47	0.54	V
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 40 V		60	300	μA
Terminal capacitance	C <sub>t</sub>	V <sub>R</sub> = 10 V, f = 1 MHz		95		pF
Reverse recovery time *1	t <sub>rr</sub>	I <sub>F</sub> = I <sub>R</sub> = 100 mA I <sub>rr</sub> = 10 mA, R <sub>L</sub> = 100 Ω		30		ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 Measuring methods for Diodes.

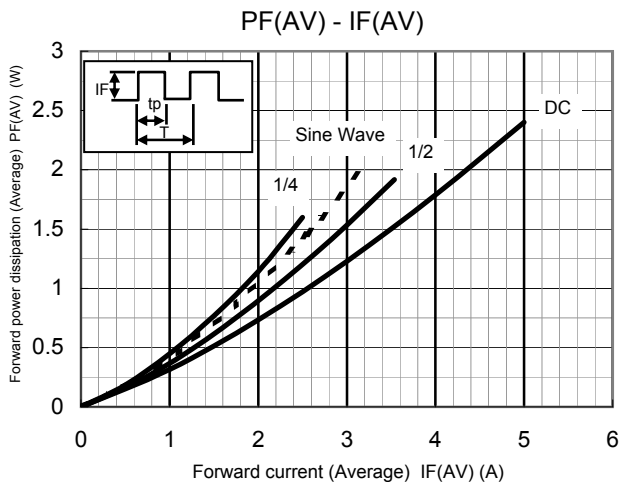
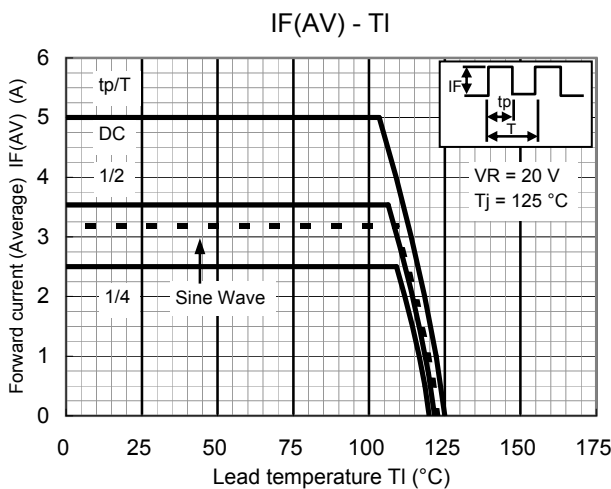
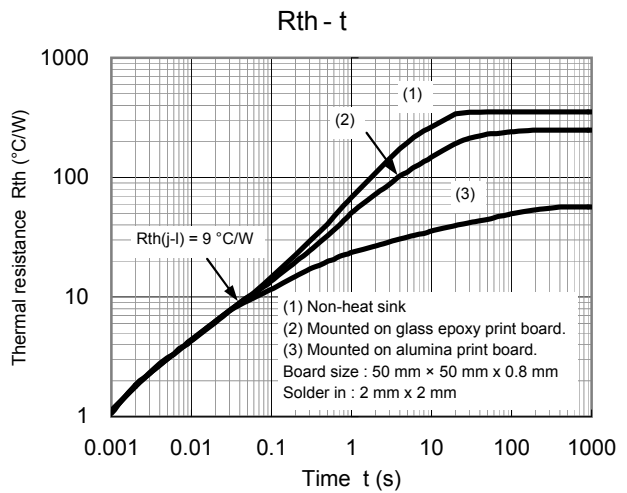
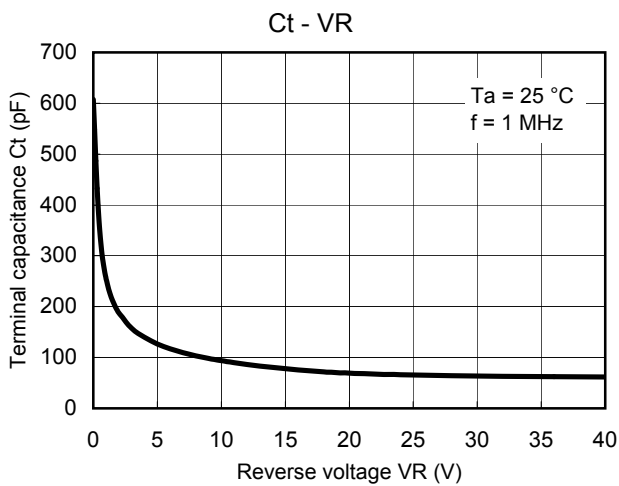
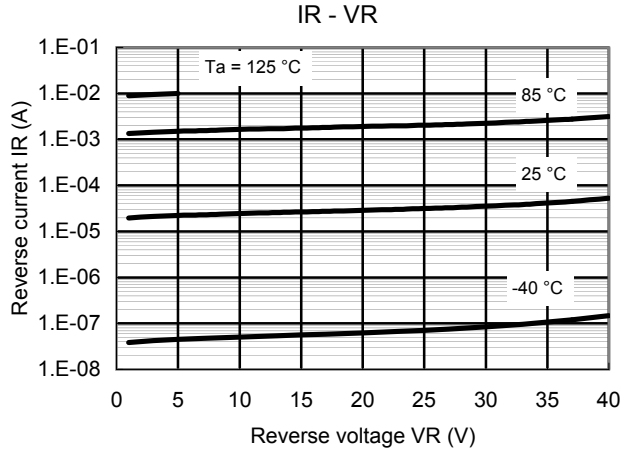
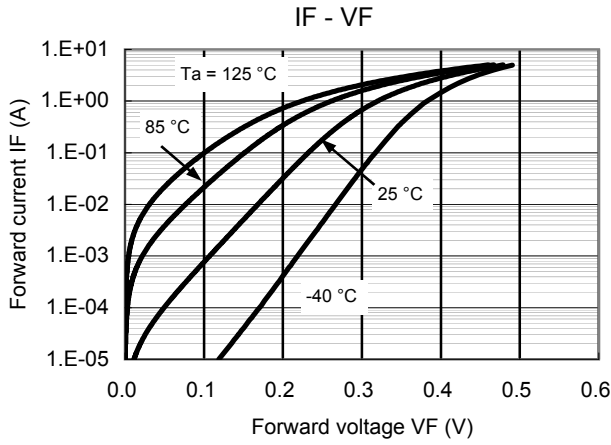
2. This product is sensitive to electric shock (static electricity, etc.). Due attention must be paid on the charge of a human body and the leakage of current from the operating equipment.

3. \*1 t<sub>rr</sub> test circuit





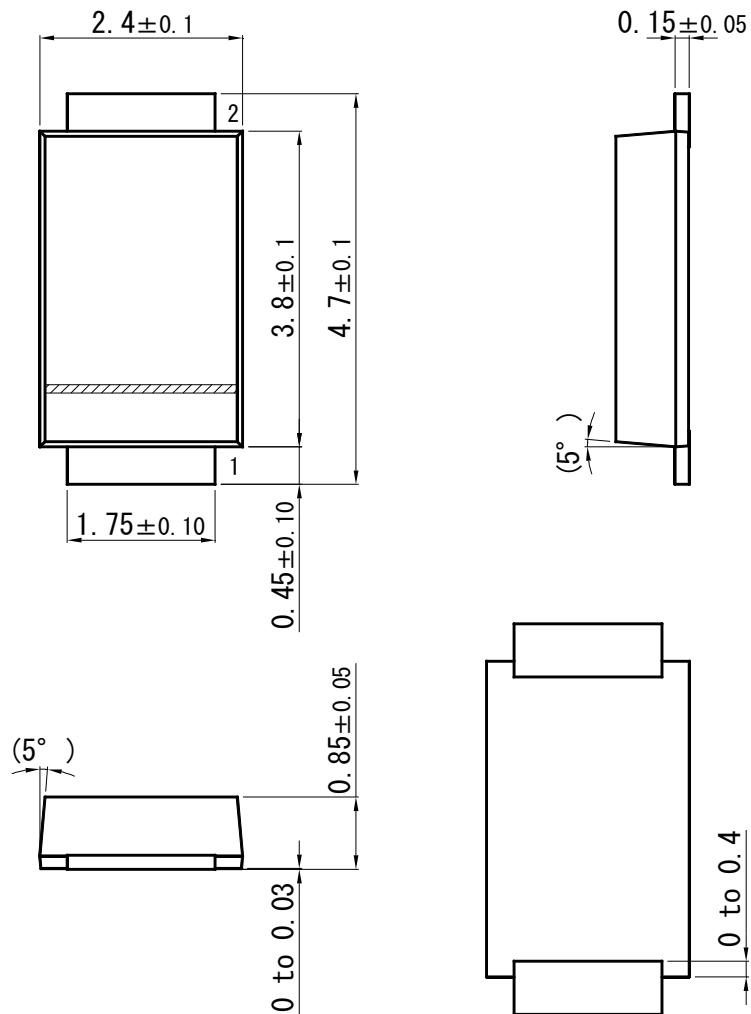
Technical Data ( reference )



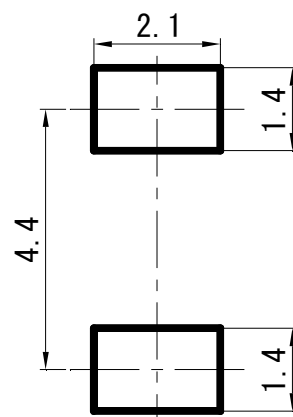


### TMiniP2-F2-B

Unit: mm



#### ■ Land Pattern (Reference) (Unit: mm)



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