Revision: 09-Dec-13

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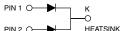
Dual Low-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.33$ V at $I_F = 10$ A



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V60D45C



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 30 A			
V _{RRM}	45 V			
I _{FSM}	320 A			
V_F at $I_F = 30$ A	0.48 V			
T _J max.	150 °C			
Package	TO-263AC (SMPD)			
Diode variations	Dual Common Cathode			

FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- AEC-Q101 qualified
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-263AC (SMPD)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V60D45C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	60	^	
	per diode		30	A	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load		I _{FSM}	320	A	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C	



COMPLIANT

HALOGEN

FREE

V60D45C

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 10 A	T _A = 25 °C	V _F ⁽¹⁾	0.44	-	V	
	I _F = 15 A			0.47	-		
	I _F = 30 A			0.55	0.64		
	I _F = 10 A	T _A = 125 °C		0.33	-		
	I _F = 15 A			0.37	-		
	I _F = 30 A			0.48	0.56		
Reverse current per diode	$\mathcal{M} = 4E\mathcal{M}$	T _A = 25 °C	I _R ⁽²⁾	-	2500	μA	
	$V_{\rm R} = 45 \text{ V}$ T	T _A = 125 °C		19	60	mA	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)					
PARAMETER		SYMBOL	V60D45C	UNIT	
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	1.5	°C/W	
	per device		0.8		
	per device	R _{0JA} ⁽¹⁾⁽²⁾	45		

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

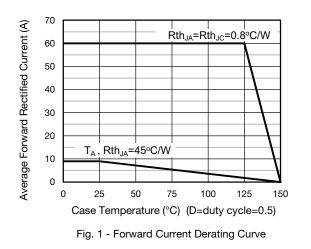
⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AC (SMPD)	V60D45C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel
TO-263AC (SMPD)	V60D45CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



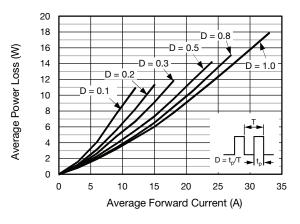


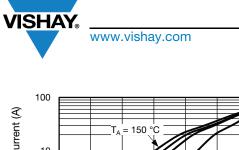
Fig. 2 - Forward Power Loss Characteristics Per Diode

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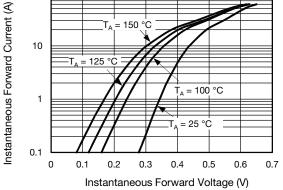
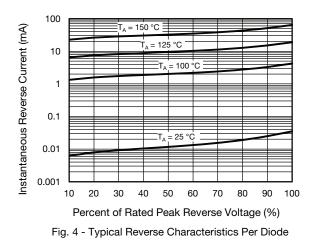


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode



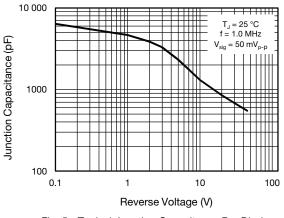


Fig. 5 - Typical Junction Capacitance Per Diode

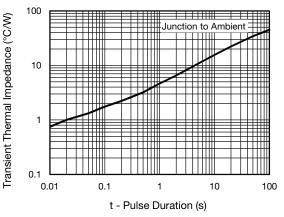


Fig. 6 - Typical Transient Thermal Impedance Per Diode

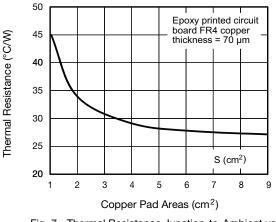


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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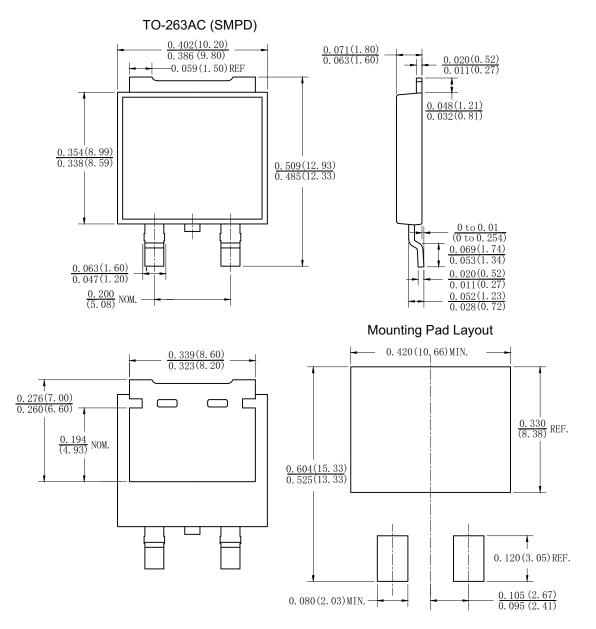
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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