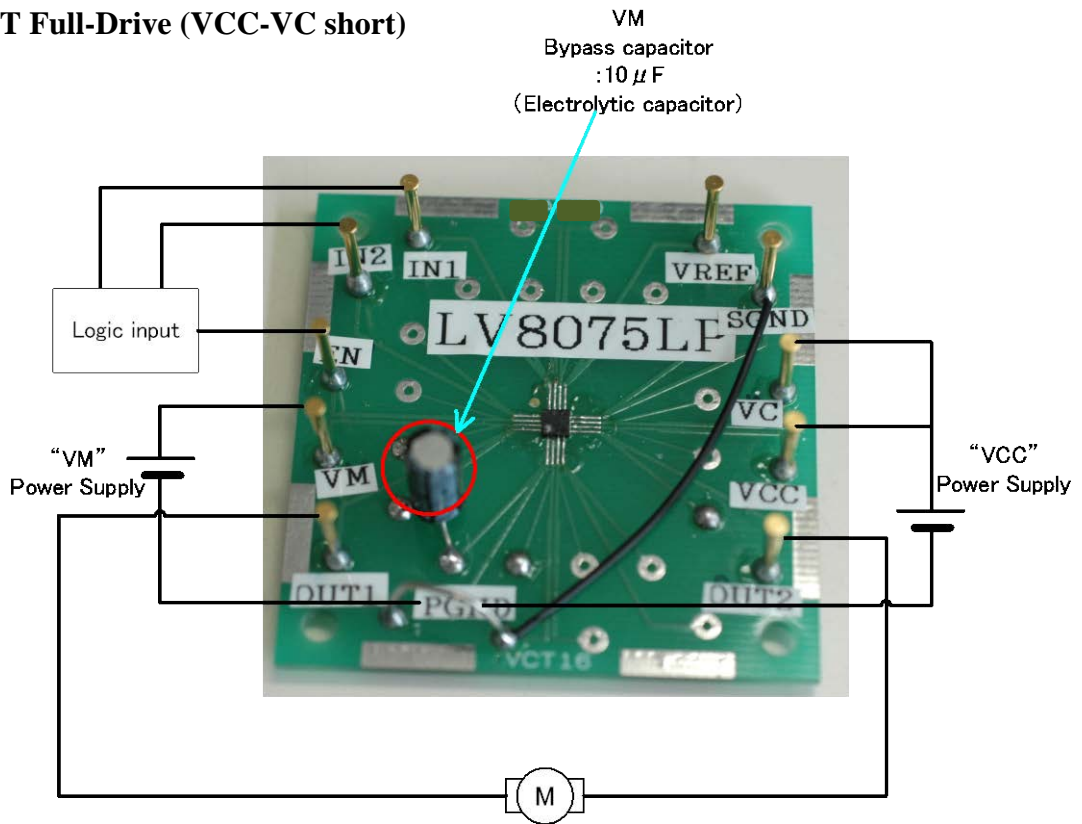


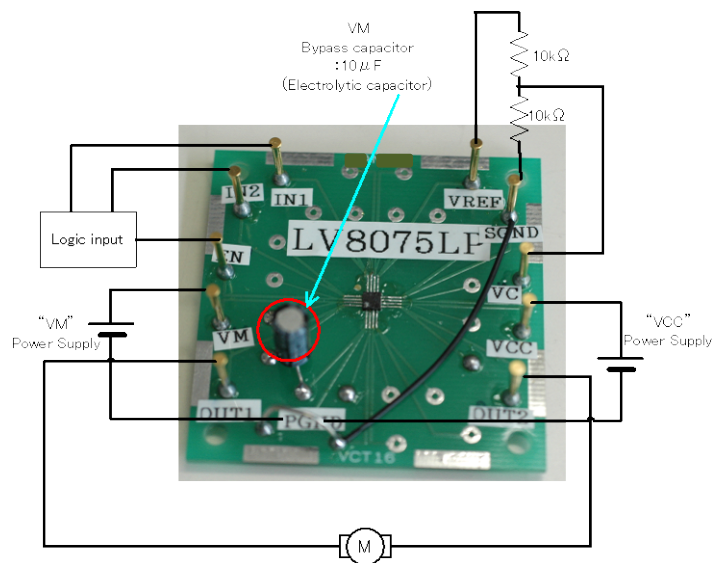
Test Procedure for the LV8075LPGEVB Evaluation Board

OUTPUT Full-Drive (VCC-VC short)



LV8075LP (50.0 mm × 50.0 mm × 1.6 mm, glass epoxy 1-layer board) base on VCT16 basic board.

OUTPUT constant voltage 1.5V drive (VC voltage setting)



Supply Voltage:

- VCC (2.5 to 5.5V): Control voltage Supply for LSI
- VM (2.5 to 5.5V): Power Supply for LSI
- VC(0 to VCC):Constant voltage control setting

Testing Procedure for DC Motor Control:

1. **Motor Connection:** Connect the Motor(s) between OUT1 and OUT2.
2. **Power Supply:** Supply DC voltage to VCC, VM.
3. **Motor Operation:** Set EN and IN1 and IN2 terminals according to the purpose (See LV8075LP datasheet).

Truth Table
Constant voltage output H-bridge

EN	IN1	IN2	OUT1	OUT2	Mode
H	H	H	L	L	Brake
	H	L	H	L	Forward evolution
	L	H	L	H	Reverse rotation
	L	L	off	off	Stand by
L	-	-	off	off	Stand by

“-“ entries indicate don't care state, “off” indicates output off state, insert 20kΩ impedance across PGND.

Constant voltage output value : $V (OUT) = V (VC) \times 2.0$

DCmotor load VCC=VM=3V VC=3.0V IN2=“H”

(Current waveform example “brake current”)

