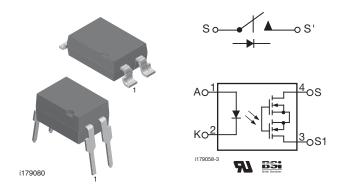
### **LH1546AD, LH1546ADF, LH1546ADFTR**

Vishay Semiconductors

# 1 Form A Solid-State Relay



#### **DESCRIPTION**

The LH1546AD (4 pin DIP) is robust, ideal for telecom and ground fault applications. It is an SPST normally open switch (1 form A) that replaces electromechanical relays in many applications. It is constructed using a GaAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated BCDMOS technology, is comprised of a photodiode array, switch control circuitry and MOSFET switches. In addition, it employs current-limiting circuitry which meets lightning surge testing as per ANSI/TIA-968-B and other regulatory voltage surge requirements when overvoltage protection is provided.

#### **FEATURES**

- · Current limit protection
- Isolation test voltage 5300 V<sub>RMS</sub>
- Typical R<sub>ON</sub> 28 Ω
- Load voltage 350 V
- Load current 120 mA
- · High surge capability
- · Clean bounce free switching
- Low power consumption
- · High reliability monolithic receptor
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

#### **APPLICATIONS**

- General telecom switching
  - On/off hook control
  - Ring relay
  - Dial pulse
  - Ground start
  - Ground fault protection
- Instrumentation
- · Industrial controls

#### Note

• See "solid-state relays" (application note 56)

### **AGENCY APPROVALS**

UL1577: file no. E52744 system code H, double

protection

BSI/BABT: certification no. 7980

ORDERING INFORMATION				
L H 1 5 4 6 A  PART NUMBER ELECTR. VARIATION	# # T R  PACKAGE TAPE AND REEL  7.62 mm			
PACKAGE	UL, BSI			
SMD-4, gullwing, tubes	LH1546ADF			
SMD-4, gullwing, tape and reel	LH1546ADFTR			
DIP-4, tubes	LH1546AD			



# **LH1546AD, LH1546ADF, LH1546ADFTR**

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
LED continuous forward current		I <sub>F</sub>	50	mA	
LED reverse voltage	I <sub>R</sub> ≤ 10 μA	$V_{R}$	8	V	
OUTPUT					
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	$V_{L}$	350	V	
Continuous DC load current at 25 °C		ΙL	120	mA	
SSR					
SSR output power dissipation (continuous)		$P_{diss}$	550	mW	
Ambient temperature range		T <sub>amb</sub>	- 40 to + 85	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 150	°C	
Soldering temperature (1)	t = 10 s max.	$T_{sld}$	260	°C	
Isolation test voltage	t = 1 s	V <sub>ISO</sub>	5300	$V_{RMS}$	
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω	
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω	

#### Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED forward current, switch turn-on	$I_L = 100 \text{ mA}, t = 10 \text{ ms}$	I <sub>Fon</sub>		1.7	3	mA
LED forward current, switch turn-off	$V_{L} = \pm 300 \text{ V}$	I <sub>Foff</sub>	0.2	1.6		mA
LED forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	1.15	1.2	1.45	V
OUTPUT						
On-resistance, AC/DC: pin 3 (±) to 4 (±)	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	R <sub>ON</sub>		28	35	Ω
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R <sub>OFF</sub>	0.5	300		GΩ
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Io		0.32	200	nA
Output capacitance pin 3 to 4	$I_F = 0$ mA, $V_L = 1$ V	Co		55		pF
		Co		10		pF
TRANSFER						
Capacitance (input to output)	V <sub>ISO</sub> = 1 V	C <sub>IO</sub>		0.5		pF

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t <sub>on</sub>		2	3	ms
Turn-off time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t <sub>off</sub>		0.08	3	ms

### Vishay Semiconductors

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

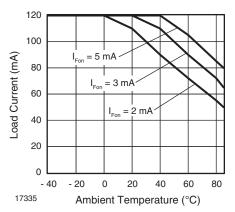


Fig. 1 - Recommended Operating Conditions

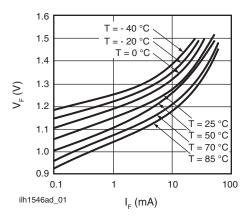


Fig. 2 - LED Voltage vs. Temperature

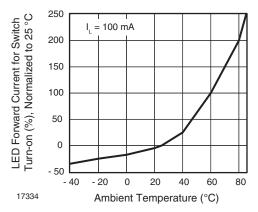


Fig. 3 - LED Current for Switch Turn-on vs. Temperature

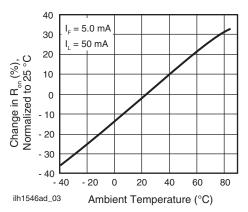


Fig. 4 - CTR<sub>CB</sub> vs. LED Current

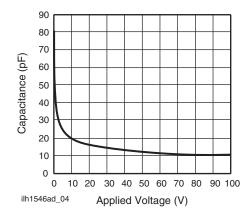


Fig. 5 - Switch Capacitance vs. Applied Voltage

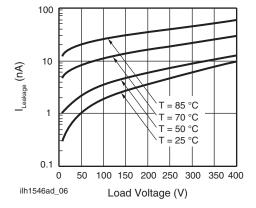


Fig. 6 - Leakage Current vs. Applied Voltage

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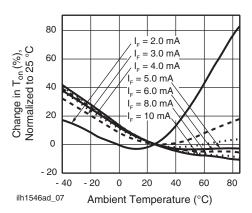


Fig. 7 - Turn-on Time vs. Temperature

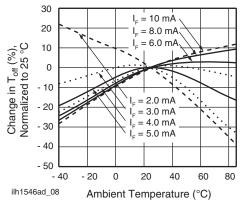


Fig. 8 - Turn-off Time vs. Temperature

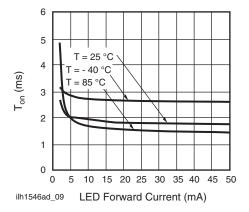


Fig. 9 - Turn-on Time vs. LED Current

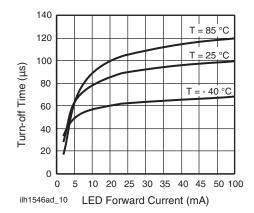
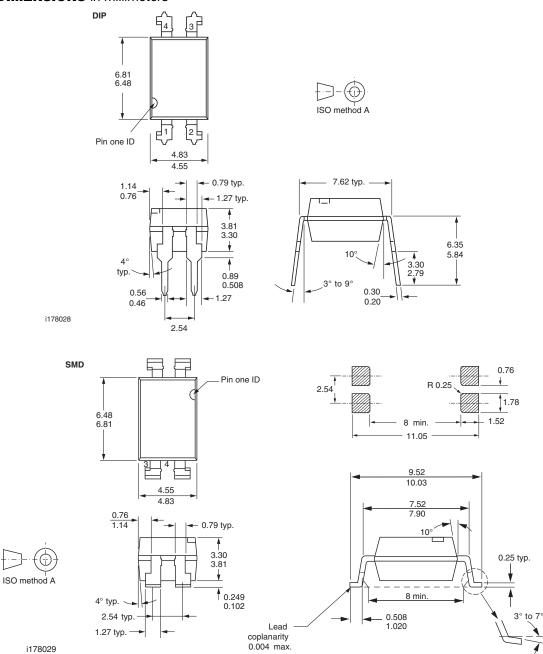


Fig. 10 - Turn-off Time vs. LED Current

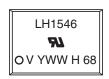
# **LH1546AD, LH1546ADFTR**

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#### **PACKAGE DIMENSIONS** in millimeters



#### **PACKAGE MARKING** (example)



#### Note

• Tape and reel suffix (TR) is not part of the package marking.



### **Legal Disclaimer Notice**

Vishay

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