

High Temperature Silicon Carbide Power Schottky Diode

650 V V_{RRM} 45 A I_{F (Tc=25°C)} Q_{C} 66 nC

Features

- 650 V Schottky rectifier
- 250 °C maximum operating temperature
- Zero reverse recovery charge
- · Superior surge current capability
- Positive temperature coefficient of V_F
- Temperature independent switching behavior
- Lowest figure of merit Q_C/I_F
- Available screened to Mil-PRF-19500

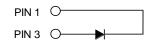
Advantages

- High temperature operation
- Improved circuit efficiency (Lower overall cost)
- · Low switching losses
- · Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Industry's lowest reverse recovery charge
- Industry's lowest device capacitance
- Ideal for output switching of power supplies
- Best in class reverse leakage current at operating temperature

Package

RoHS Compliant





SMD0.5 / TO - 276 (Hermetic Package)

Applications

- Down Hole Oil Drilling
- Geothermal Instrumentation
- Solenoid Actuators
- General Purpose High-Temperature Switching
- Amplifiers
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)

Maximum Ratings at T_i = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		650	V
Continuous forward current	l _F	T _C = 25 °C	45	Α
Continuous forward current	l _F	T _C ≤ 225 °C	14.6	Α
RMS forward current	I _{F(RMS)}	T _C ≤ 225 °C	26	Α
Surge non-repetitive forward current, Half Sine Wave	I _{F,SM}	$T_C = 25 ^{\circ}C, t_P = 10 \text{ms}$	140	А
Non-repetitive peak forward current	$I_{F,max}$	$T_C = 25 ^{\circ}\text{C}, t_P = 10 \mu\text{s}$	650	Α
I ² t value	∫i² dt	$T_C = 25 ^{\circ}C, t_P = 10 \text{ms}$	98	A ² S
Power dissipation	P _{tot}	T _C = 25 °C	453	W
Operating and storage temperature	T_{j} , T_{stg}		-55 to 250	°C

Electrical Characteristics at T_j = 250 °C, unless otherwise specified

Danamatan	Compleal	Conditions -		Values		11!4	
Parameter	Symbol			min.	typ.	max.	Unit
Diodo forward voltago	V _F	I _F = 15 A, T _j = 25 °C		1.5		V	
Diode forward voltage	VF	I _F = 15 A, T _j = 210 °C		2.2			
Reverse current	1	$V_R = 650 \text{ V}, T_j = 25 \text{ °C}$		1	5	μΑ	
	I _R	$V_R = 650 \text{ V}, T_j = 250 ^{\circ}\text{C}$		50	200		
Total capacitive charge	Q _C	$I_F \le I_{F,MAX}$	V _R = 400 V		66		nC
Switching time	t _s	dI _F /dt = 200 A/μs T _i = 210 °C	V _R = 400 V		< 49		ns
		$V_R = 1 V, f = 1 MHz,$	T _j = 25 °C		1107		
Total capacitance	С	$V_R = 400 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$		103		pF	
		V _P = 650 V. f = 1 MH ₂	z. T _i = 25 °C		99		

Thermal Characteristics

I hermal resistance, junction - case	R_{thJC}	0.49	°C/VV
Machanical Proporties			

Mechanical Properties

moonamour roportioo			
Mounting torque	M	0.6	Nm



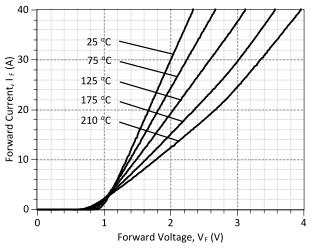


Figure 1: Typical Forward Characteristics

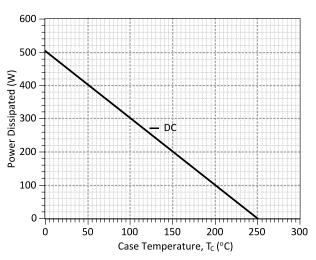


Figure 3: Power Derating Curve

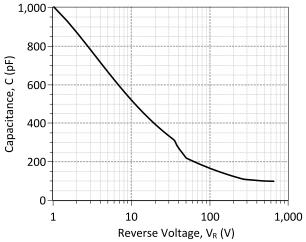


Figure 5: Typical Junction Capacitance vs Reverse Voltage Characteristics

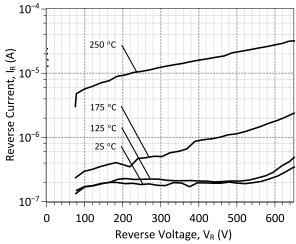


Figure 2: Typical Reverse Characteristics

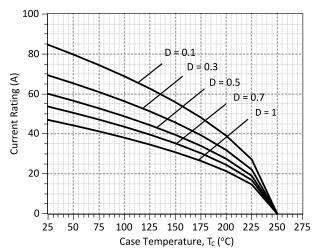


Figure 4: Current Derating Curves (D = t_P/T , t_P = 400 μ s) (Considering worst case Z_{th} conditions)

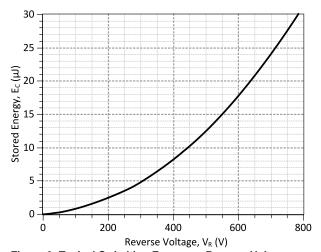


Figure 6: Typical Switching Energy vs Reverse Voltage Characteristics

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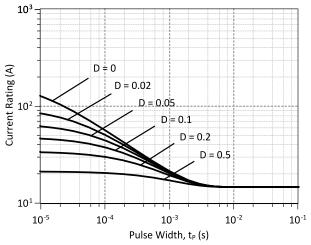


Figure 7: Current vs Pulse Duration Curves at $T_c = 225$ °C

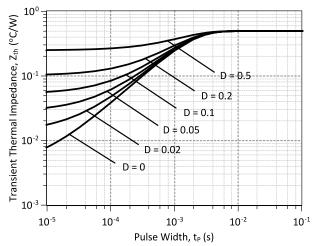
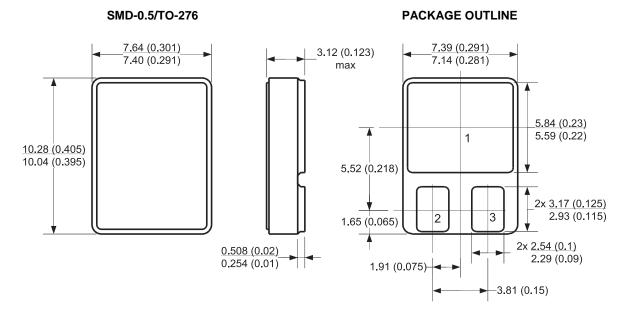


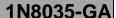
Figure 8: Transient Thermal Impedance

Package Dimensions:



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS





Revision History					
Date	Revision	Comments	Supersedes		
2014/08/26	1	Updated Electrical Characteristics			
2012/04/24	0	Initial release			

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SPICE Model Parameters

Copy the following code into a SPICE software program for simulation of the 1N8035-GA device.

```
MODEL OF GeneSiC Semiconductor Inc.
     $Revision: 1.0
     $Date: 05-SEP-2013
     GeneSiC Semiconductor Inc.
     43670 Trade Center Place Ste. 155
     Dulles, VA 20166
     http://www.genesicsemi.com/index.php/hit-sic/schottky
     COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
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* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
* Start of 1N8035-GA SPICE Model
.SUBCKT 1N8035 ANODE KATHODE
D1 ANODE KATHODE 1N8035 25C; Call the Schottky Diode Model
D2 ANODE KATHODE 1N8035 PIN; Call the PiN Diode Model
.MODEL 1N8035 25C D
+ IS
      8.46E-17
                          RS
                                     0.0319
                                     1000
+ N
          1
                          IKF
+ EG
         1.2
                         XTI
+ TRS1
         0.0038
                         TRS2
                                    3.00E-05
         1.26E-09
                         VJ
+ CJO
                                     0.438
         1.5278
                                     0.5
+ M
                         FC
+ TT
         1.00E-10
                          BV
                                     650
+ IBV
          1.00E-03
                          VPK
                                     650
          20
+ IAVE
                          TYPE
                                     SiC Schottky
      GeneSiC_Semiconductor
+ MFG
.MODEL 1N8035 PIN D
+ IS 2.77E-10
                         RS
                                     0.086693
          3.3505
+ N
                          IKF
                                     3.67E-06
+ EG
          3.23
                         XTI
                                     -10
+ FC
         0.5
                         TT
+ BV
          650
                          IBV
                                     1.00E-03
         650
                                     20
+ VPK
                          IAVE
+ TYPE
          SiC PiN
.ENDS
```

^{*} End of 1N8035-GA SPICE Model