

# MURF1660CTG

## Switch-mode Power Rectifier

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

### Features

- Ultrafast 60 Nanosecond Recovery Times
- 150°C Operating Junction Temperature
- Epoxy Meets UL 94 V-0 @ 0.125 in
- High Temperature Glass Passivated Junction
- Low Leakage Specified @ 150°C Case Temperature
- Current Derating @ Both Case and Ambient Temperatures
- Electrically Isolated. No Isolation Hardware Required.
- This is a Pb-Free Package\*

### Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds

### MAXIMUM RATINGS (Per Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	600	V
Average Rectified Forward Current Total Device, (Rated $V_R$ ), $T_C = 150^\circ\text{C}$ Per Diode Per Device	$I_{F(AV)}$	8 16	A
Peak Repetitive Forward Current (Rated $V_R$ , Square Wave, 20 kHz), $T_C = 150^\circ\text{C}$	$I_{FM}$	16	A
Non-repetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	$I_{FSM}$	100	A
Operating Junction and Storage Temperature	$T_J, T_{stg}$	- 65 to +150	°C
RMS Isolation Voltage ( $t = 0.3$ second, R.H. $\leq 30\%$ , $T_A = 25^\circ\text{C}$ ) (Note 1) Per Figure 3	$V_{iso1}$	4500	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Proper strike and creepage distance must be provided.

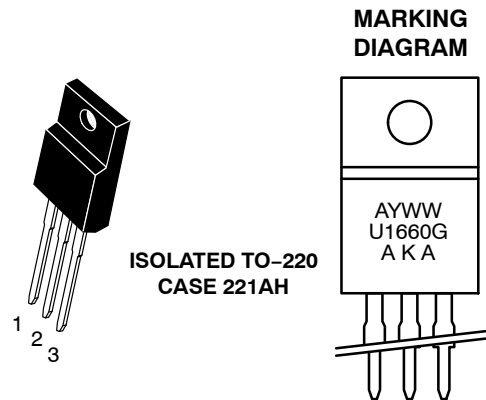
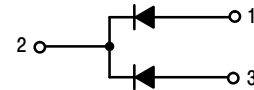
\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



ON Semiconductor®

<http://onsemi.com>

## ULTRAFAST RECTIFIER 16 AMPERES, 600 VOLTS



ISOLATED TO-220  
CASE 221AH

A = Assembly Location  
Y = Year  
WW = Work Week  
U1660 = Device Code  
G = Pb-Free Package  
AKA = Diode Polarity

### ORDERING INFORMATION

Device	Package	Shipping
MURF1660CTG	TO-220 (Pb-Free)	50 Units / Rail

# MURF1660CTG

## THERMAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.0	$^{\circ}C/W$
Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	$T_L$	260	$^{\circ}C$

## ELECTRICAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 2) ( $i_F = 8.0\text{ A}$ , $T_C = 150^{\circ}C$ ) ( $i_F = 8.0\text{ A}$ , $T_C = 25^{\circ}C$ )	$V_F$	1.20 1.50	V
Maximum Instantaneous Reverse Current (Note 2) (Rated DC Voltage, $T_C = 150^{\circ}C$ ) (Rated DC Voltage, $T_C = 25^{\circ}C$ )	$i_R$	500 10	$\mu A$
Maximum Reverse Recovery Time ( $I_F = 1.0\text{ A}$ , $di/dt = 50\text{ A}/\mu s$ ) ( $I_F = 0.5\text{ A}$ , $i_R = 1.0\text{ A}$ , $I_{REC} = 0.25\text{ A}$ )	$t_{rr}$	60 50	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width = 300  $\mu s$ , Duty Cycle  $\leq 2.0\%$ .

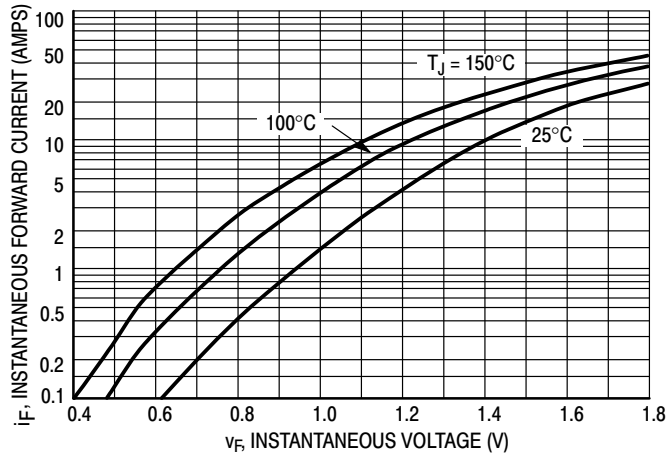


Figure 1. Typical Forward Voltage, Per Leg

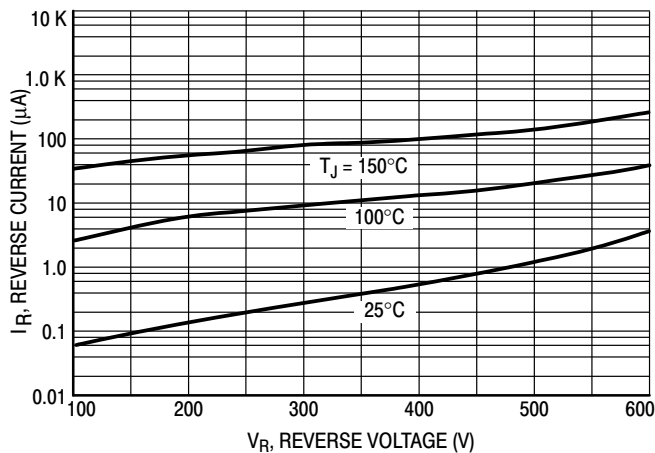
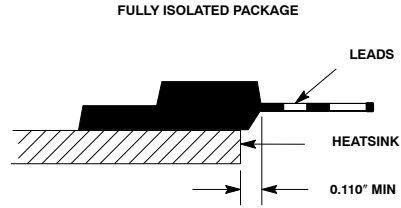


Figure 2. Typical Reverse Current, Per Leg\*

# MURF1660CTG

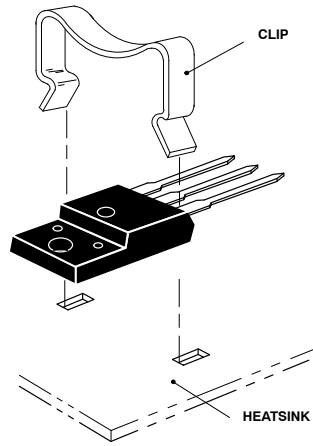
## TEST CONDITION FOR ISOLATION TEST\*



**Figure 3. Mounting Position**

\* Measurement made between leads and heatsink with all leads shorted together.

## MOUNTING INFORMATION



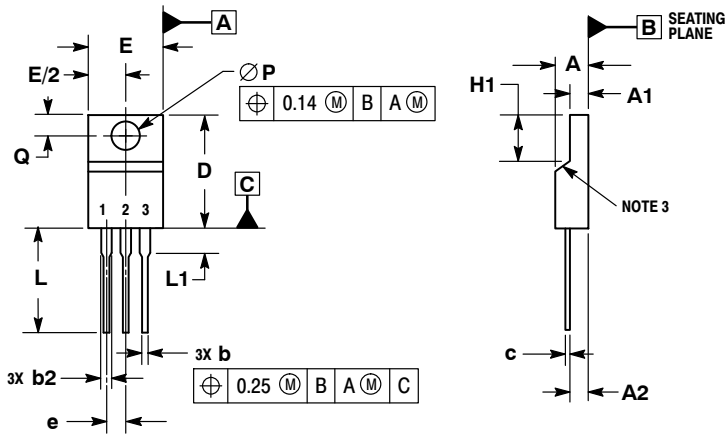
**Clip-Mounted**

**Figure 4. Typical Mounting Technique**

# MURF1660CTG

## PACKAGE DIMENSIONS

### TO-220 FULLPACK, 3-LEAD CASE 221AH ISSUE E



#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. CONTOUR UNCONTROLLED IN THIS AREA.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST EXTREME OF THE PLASTIC BODY.
5. DIMENSION b2 DOES NOT INCLUDE DAMBAR PROTRUSION. LEAD WIDTH INCLUDING PROTRUSION SHALL NOT EXCEED 2.00.

DIM	MILLIMETERS	
	MIN	MAX
A	4.30	4.70
A1	2.50	2.90
A2	2.50	2.90
b	0.54	0.84
b2	1.10	1.40
c	0.49	0.79
D	14.70	15.30
E	9.70	10.30
e	2.54 BSC	
H1	6.70	7.10
L	12.70	14.73
L1	---	2.10
P	3.00	3.40
Q	2.80	3.20

**ON Semiconductor** and **ON** are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

##### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
Email: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada  
**Europe, Middle East and Africa Technical Support:**  
Phone: 421 33 790 2910  
**Japan Customer Focus Center**  
Phone: 81-3-5817-1050

**ON Semiconductor Website:** [www.onsemi.com](http://www.onsemi.com)  
**Order Literature:** <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative