

RAPPID: RAppID Initialization for Power Architecture

Description

RAppID is a family of graphical development tools for the MPC5XXX family of Power Architecture® controllers that enables the user to quickly and easily configure the controller PLUS generate complete documentation. It can also be used as a learning tool to gain an understanding of the controller and its peripherals. RAppID not only generates C code for initializing the registers, but it also provides a system initialization function that brings the controller up in an orderly sequence. Use RAppID to save time and become an expert on the MPC5XXX Family.



Features

- Intuitive, easy-to-use graphical user interface (GUI)
- Comprehensive initialization of the CPU, memory and peripherals
- Automatic DMA register setting from peripherals for basic modes
- Built-in consistency checks to minimize incorrect settings
- Automatic report generation of settings
- Efficient C and assembly code generation for compilers from companies such as Wind River®, Green Hills® and Freescale
- ANSI- or MISRA-compliant code and supporting header files
- Online documentation and built-in tool tips
- Provisions for revision management
- Automatic date and time stamps on generated code and reports
- Enhanced register viewer—browse registers by peripheral
- Modular code generation—generate code for any or all peripherals
- Project import/export capability for distributed development teams
- Ability to define multiple initialization strategies and chose either runtime or compile time for execution
- Full eTPU host initialization and code image download
- Ability to register custom eTPU functions and assign channels
- Wizards for eMIOS initialization and function settings

Supported Devices

- MPC5534: Freescale 32-bit MCU for Powertrain Applications
- MPC5553: Freescale 32-bit MCU for Powertrain Applications
- MPC5554: Freescale 32-bit MCU for Powertrain Applications
- MPC5561: Freescale 32-bit MCU for Advanced Driver Assistance Applications
- MPC5566: Freescale 32-bit MCU for Powertrain Applications
- MPC5567: Freescale 32-bit MCU for Powertrain Applications

Order Codes

Products	Part ID
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC564xB	RAPPID-564XBSW
PHYSICAL LICENSED SHIPMENT Processor Expert, RAppID Suite for MPC5748G	RAPPID5748GSW-N
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for the MPC5676R Family	RAPPID-567XRSW
PHYSICAL LICENSED SHIPMENT Processor Expert, RAppID Suite for MPC5746M	RAPPID5746MSW-N
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC564xL	RAPPID-564XLSW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC564xA	RAPPID-564XASW
PHYSICAL LICENSED SHIPMENT Processor Expert, RAppID Suite for MPC577xK	RAPPID577XKSW-N
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC5565	VG-RAPPID-5565SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC560xS	RAPPID-560XSSW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC560xB	RAPPID-560XBSW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC5553	VG-RAPPID-5553SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC563xM	RAPPID-563XMSW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC567xF	RAPPID-567XFSW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC567xK	RAPPID-567XKSW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC5533/34	VG-RAPPID-5534SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC5567	VG-RAPPID-5567SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool MPC5516	VG-RAPPID-5516SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool MPC5566	VG-RAPPID-5566SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC5561	VG-RAPPID-5561SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC5554	VG-RAPPID-5554SW
PHYSICAL LICENSED SHIPMENT RAppID Initialization Tool for MPC560xP	RAPPID-560XPSW
RAppID Init for the PXR40 Family of MCUs	RAPPID-PXR40SW
RAppID Init for the PXS30 Family of MCUs	RAPPID-PXS30SW
RAppID Init for the PXS20 Family of MCUs	RAPPID-PXS20SW