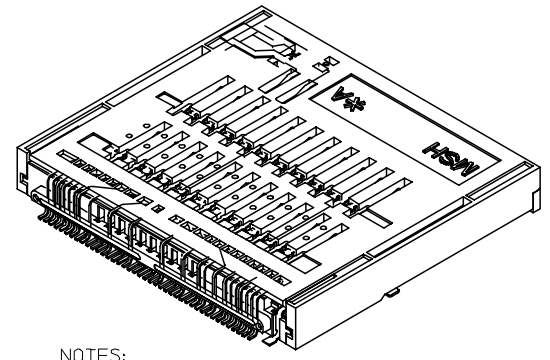
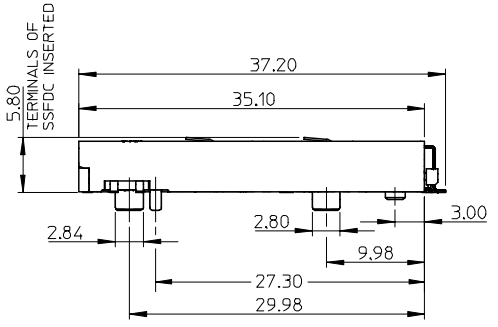


ENTRY OF SYSTEM PANEL



NOTES:

1. MATERIALS
HOUSING: HIGH TEMPERATURE THERMOPLASTIC, BLACK, UL94 V-0.
TERMINALS AND SWITCHES: COPPER ALLOYS.
2. FINISHS
TERMINALS: NICKEL OVERALL UNDERPLATED
SELECTIVE GOLD ON CONTACT AREA
TIN ALLOY ON SOLDER AREA
SWITCHES: NICKEL OVERALL PLATED
TIN ALLOY ON SOLDER AREA
3. COPLANARITY TO BE 0.15mm MAX.
4. PRODUCT SPECIFICATION: PS-48071-001
5. PACKAGE DRWAING NO.: PK-48071-001
- △ DATUM Z IS SMT SURFACE
7. SD CONTACT SEQUENCE:
(1) SW PIN
(2) VSS (PIN 3) & VDD (PIN 4)
(3) CLK, CMD, DAT0, DAT1 & VSS (PIN 6)
(4) CD / DAT3 (PIN 1)

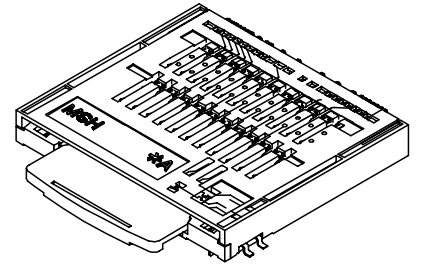
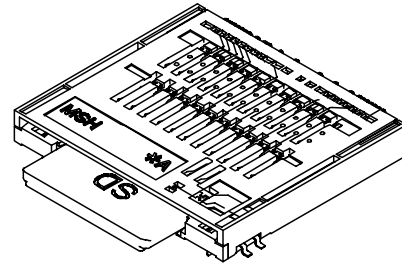
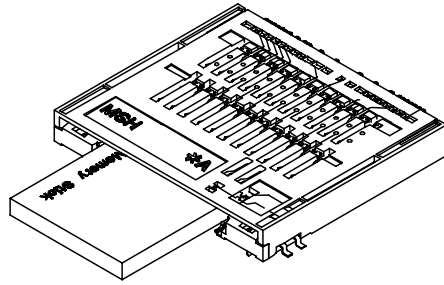
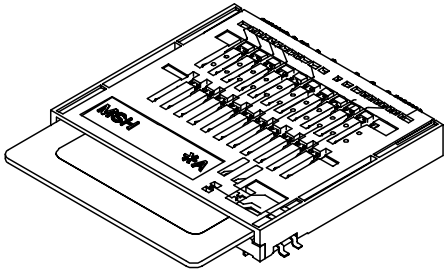
5 IN 1 TYPE (SD, SDIO, MMC, MS & SSFDC)	48071-2001	1.50	PURE TIN TYPE	
6 IN 1 TYPE (SD, SDIO, MMC, MS, SSFDC & xD)	48071-3001			
5 IN 1 TYPE (SD, SDIO, MMC, MS & SSFDC)	48071-0001	2.00		
6 IN 1 TYPE (SD, SDIO, MMC, MS, SSFDC & xD)	48071-1001			
PRODUCT TYPE		PRODUCT NUMBER	DIM A	REMARK

EC NO: SH2005-0276 DRWN:GRMA 2005/12/15 CHKD: APPR:	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	SCALE 2:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY
	▽-0	4 PLACES ± --- ± ---	DIMENSION STYLE MM ONLY		TITLE	
	∇-0	3 PLACES ± --- ± ---	DRAWN BY G.YANG	DATE 2003108/14	6 IN 1 AND 5 IN 1 COMBO MEMORY CARD CONNECTOR (TOP MOUNT TYPE)	
	∇-0	2 PLACES ± 0.25 ± ---	CHECKED BY W.CHANG	DATE 2003/08/22	MOLEX INCORPORATED	
	∇-0	1 PLACE ± 0.25 ± ---	APPROVED BY E.LAN	DATE 2003/08/22	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-48071-001
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			

10 9 8 7 6 5 4 3 2 1

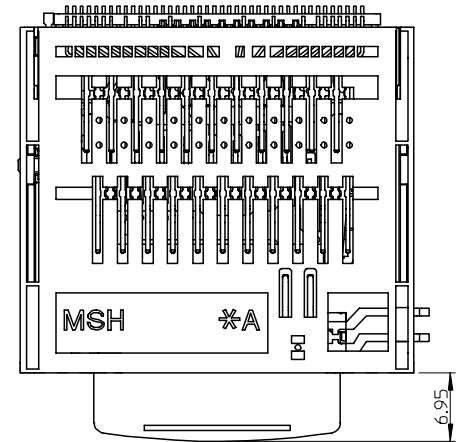
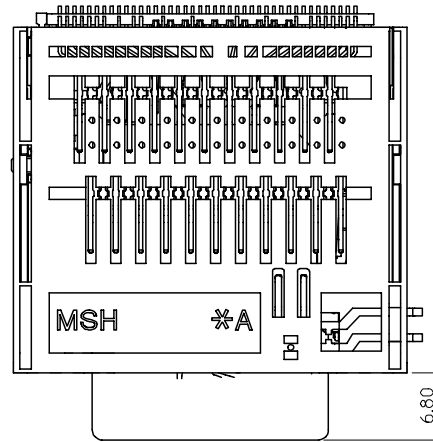
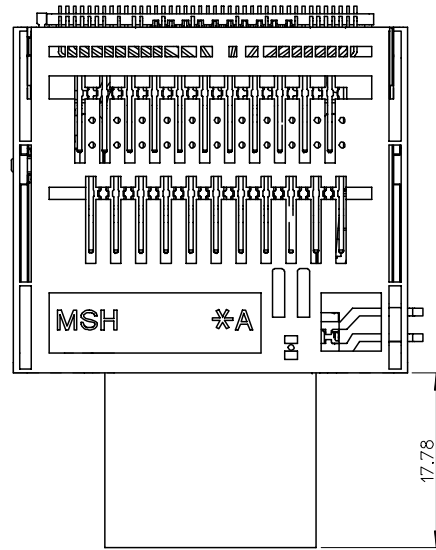
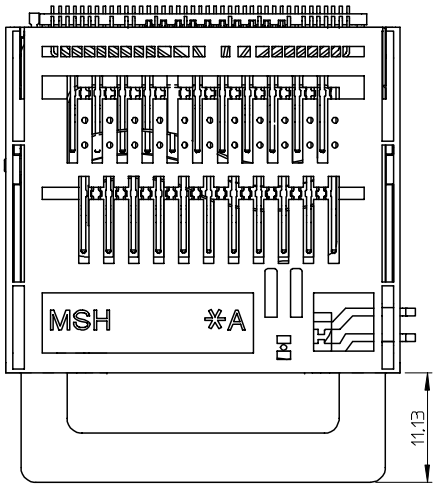
F

F



E

E



D

D

C

C

SSFDC CARD

MEMORY STICK

SD/SDIO/MMC CARD

xD CARD
(OPTION FOR 6 IN 1 TYPE)

B

B

A

A

EC NO: SH2005-0276 DRWN:GRMA 2005/12/15 CHKD: APPR:	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	SCALE 2:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY																		
	▼ - 0	<table border="1"> <thead> <tr> <th></th> <th>mm</th> <th>INCH</th> </tr> </thead> <tbody> <tr> <td>4 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>3 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>2 PLACES</td> <td>± 0.25</td> <td>± ---</td> </tr> <tr> <td>1 PLACE</td> <td>± 0.25</td> <td>± ---</td> </tr> <tr> <td colspan="3">ANGULAR ± 3 °</td> </tr> </tbody> </table>		mm	INCH	4 PLACES	± ---	± ---	3 PLACES	± ---	± ---	2 PLACES	± 0.25	± ---	1 PLACE	± 0.25	± ---	ANGULAR ± 3 °			DIMENSION STYLE MM ONLY	TITLE	6 IN 1 AND 5 IN 1 COMBO MEMORY CARD CONNECTOR (TOP MOUNT TYPE)	
		mm	INCH																					
	4 PLACES	± ---	± ---																					
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1 PLACE	± 0.25	± ---																						
ANGULAR ± 3 °																								
∇ - 0	DRAFT WHERE APPLICABLE	DRAWN BY G.YANG DATE 2003108/14 CHECKED BY W.CHANG DATE 2003/08/22 APPROVED BY E.LAN DATE 2003/08/22	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-48071-001	SHEET NO. 2 OF 5																			
∇ - 0	MUST REMAIN WITHIN DIMENSIONS	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION																						

9 8 7 6 5 4 3 2 1

A3

10 9 8 7 6 5 4 3 2 1

F

E

D

C

B

A

F

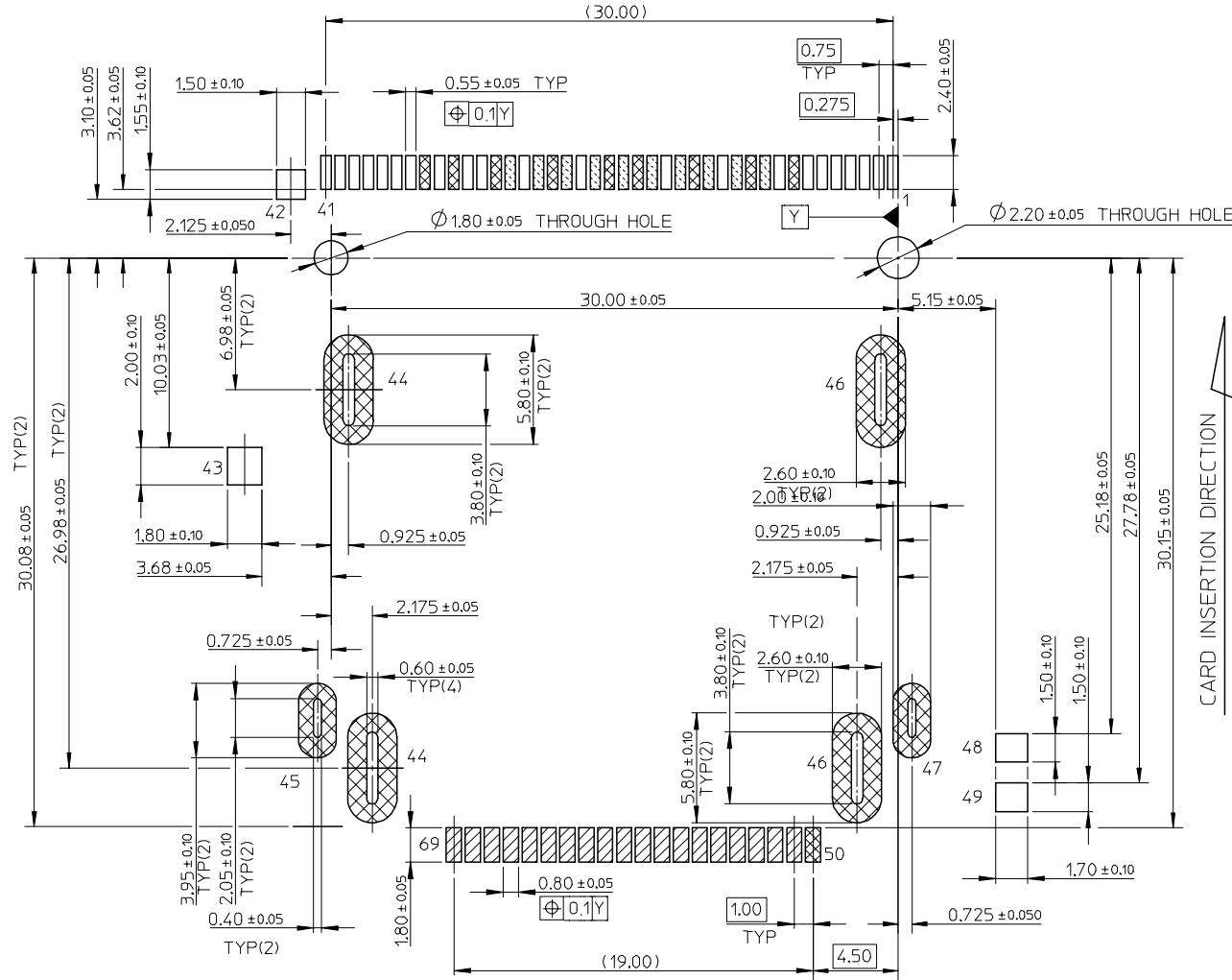
E

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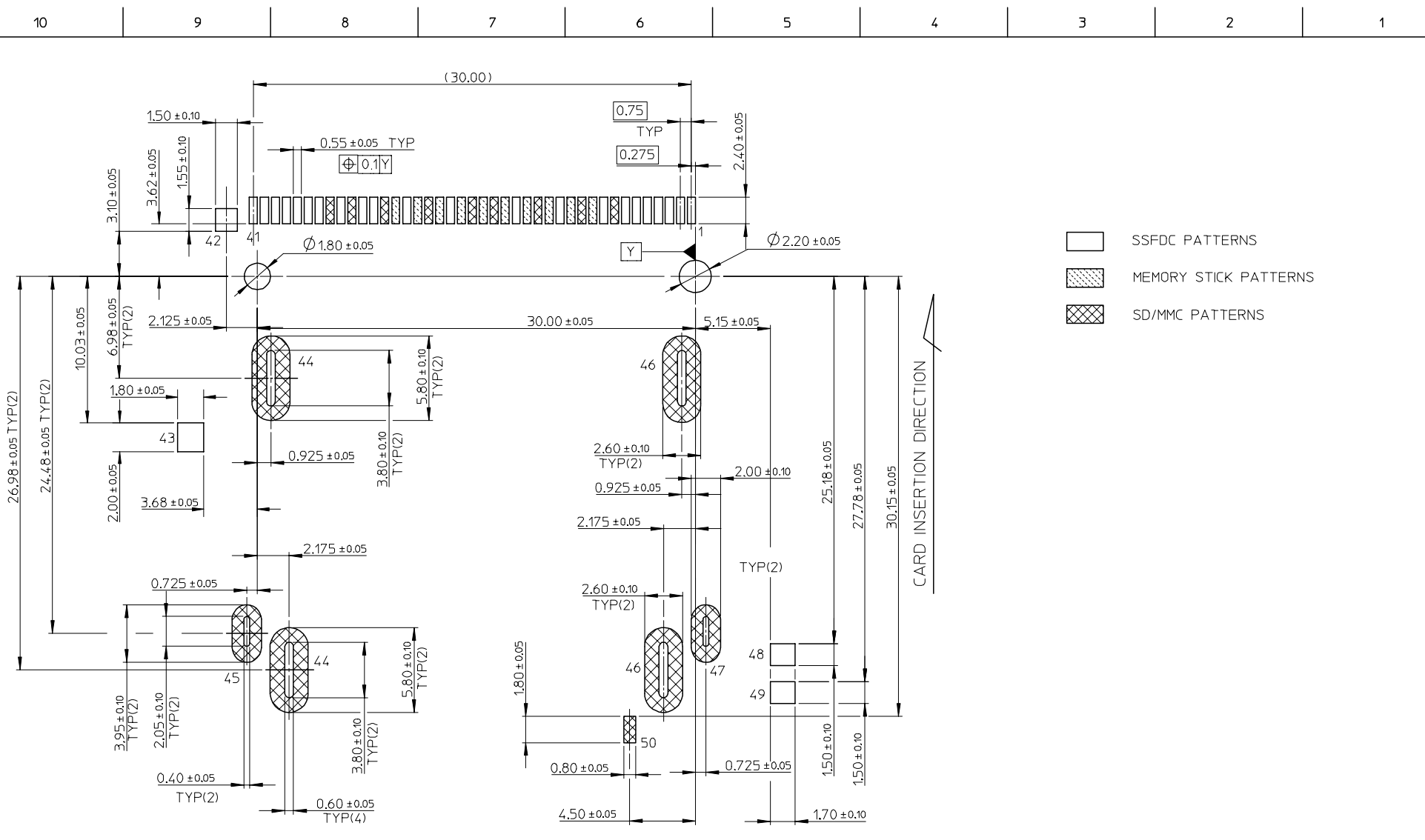
- SSFDC PATTERNS
- xD PATTERNS
- MEMORY STICK PATTERNS
- SD/MMC PATTERNS

CARD INSERTION DIRECTION

RECOMMENDED PCB LAYOUT AND PIN ASSIGNMENT OF 6 IN 1 TYPE (SD, SDIO, MMC, MS, SSFDC & xD)

EC NO: SH2005-0276 DRWN:GRMA 2005/12/15 CHKD: APPR:	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	SCALE 4:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY
	▼ - 0 ∇ - 0 S - 0	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	DIMENSION STYLE MM ONLY	TITLE		
			4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.25 ± --- 1 PLACE ± 0.25 ± ---	6 IN 1 AND 5 IN 1 COMBO MEMORY CARD CONNECTOR (TOP MOUNT TYPE)		
			ANGULAR ± 3 °	MOLEX INCORPORATED		
REV	DESCRIPTION	APPROVED BY E.LAN	DATE 2003/08/22	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-48071-001	SHEET NO. 3 OF 5

9 8 7 6 5 4 3 2 1



- SSFDC PATTERNS
- MEMORY STICK PATTERNS
- SD/MMC PATTERNS

RECOMMENDED PCB LAYOUT AND PIN ASSIGNMENT
OF 5 IN 1 TYPE (SD,SDIO, MMC, MS & SSFDC)

EC NO: SH2005-0276 DRWN:GRMA 2005/12/15 CHKD: APPR:	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		SCALE 4:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY																
	- 0 - 0 - 0	<table border="1"> <thead> <tr> <th></th> <th>MM</th> <th>INCH</th> </tr> </thead> <tbody> <tr> <td>4 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>3 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>2 PLACES</td> <td>± 0.25</td> <td>± ---</td> </tr> <tr> <td>1 PLACE</td> <td>± 0.25</td> <td>± ---</td> </tr> </tbody> </table>			MM	INCH	4 PLACES	± ---	± ---	3 PLACES	± ---	± ---	2 PLACES	± 0.25	± ---	1 PLACE	± 0.25	± ---	DIMENSION STYLE MM ONLY		TITLE		
			MM	INCH																			
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1 PLACE	± 0.25	± ---																					
DRAFT WHERE APPLICABLE		APPROVED BY E.LAN 2003/08/22		MOLEX INCORPORATED MATERIAL NO. SEE TABLE DOCUMENT NO. SD-48071-001 SHEET NO. 4 OF 5																			
MUST REMAIN WITHIN DIMENSIONS		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION																					

10 9 8 7 6 5 4 3 2 1

PIN ASSIGNMENT

1	SSFDC-1P (GND)	15	SD-2P (CMD)	29	SD-6P (Vss)	43	SSFDC-P (CD1)	57	xD-6P (ALE) *
2	SSFDC-22P (Vcc)	16	MS-7P (RSV-2)	30	SSFDC-7P (D1)	44	SD-SW (GND)	58	xD-7P (-WE) *
3	SSFDC-2P (CLE)	17	SSFDC-18P (GND)	31	SSFDC-16P (D7)	45	SD-SW (WP)	59	xD-8P (-WP) *
4	SSFDC-21P (-CE)	18	MS-6P (INS)	32	SD-7P (DAT0)	46	SD-SW (GND)	60	xD-9P (GND) *
5	SSFDC-3P (ALE)	19	SD-3P (Vss)	33	SSFDC-8P (D2)	47	SD-SW	61	xD-10P (D0) *
6	SSFDC-20P (-RE)	20	MS-5P (RSV-1)	34	SD-8P (DAT1)	48	SSFDC-SW (WP1)	62	xD-11P (D1) *
7	SSFDC-4P (-WE)	21	SD-4P (Vdd)	35	SSFDC-15P (D6)	49	SSFDC-SW (WP2/GND)	63	xD-12P (D2) *
8	SD-9P (DAT2)	22	MS-4P (SDIO)	36	SSFDC-9P (D3)	50	SD-I/O	64	xD-13P (D3) *
9	SSFDC-19P (R/-B)	23	SSFDC-6P (D0)	37	SSFDC-14P (D5)	51	xD-SW (CD) *	65	xD-14P (D4) *
10	MS-10P (Vss)	24	MS-3P (Vcc)	38	SSFDC-10P (GND)	52	xD-1P (GND) *	66	xD-15P (D5) *
11	SD-1P (CD/DAT3)	25	SD-5P (CLK)	39	SSFDC-13P (D4)	53	xD-2P (R/-B) *	67	xD-16P (D6) *
12	MS-9P (Vcc)	26	MS-2P (BS)	40	SSFDC-11P (-CD)	54	xD-3P (-RE) *	68	xD-17P (D7) *
13	SSFDC-5P (-WP)	27	SSFDC-17P (LVD)	41	SSFDC-12P (Vcc)	55	xD-4P (-CE) *	69	xD-18P (Vcc) *
14	MS-8P (SCLK)	28	MS-1P (Vss)	42	SSFDC-P (CD2/GND)	56	xD-5P (CLE) *		

* PIN NO. FROM 51 TO 68 ARE VOID FOR 4 IN 1 OPTION

SD/MMC CARD CIRCUIT

WITHOUT CARD
#45 WP 0-----0 #44 GND
#47 SW 0-----0 #46 GND

CARD INSERTED
WRITE PROTECT STATUS
#45 WP 0-----0 #44 GND
#47 SW 0-- --0 #46 GND

CARD INSERTED
WRITE ENABLE STATUS
#45 WP 0-- --0 #44 GND
#47 SW 0-- --0 #46 GND

SSFDC CARD CIRCUIT

WITHOUT CARD
#48 WP 0-- --0 #49 GND
#43 CD 0-- --0 #42 GND

CARD INSERTED
WRITE PROTECT STATUS
#48 WP 0-----0 #49 GND
#43 CD 0-----0 #42 GND

CARD INSERTED
WRITE ENABLE STATUS
#48 WP 0-- --0 #49 GND
#43 CD 0-----0 #42 GND

EC NO: SH2005-0276 DRWN:GRMA 2005/12/15 CHKD: APPR:	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	SCALE 1:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY	
	DESCRIPTION ▴ - 0 ▽ - 0 ▽ - 0	DIMENSION STYLE 4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.25 ± --- 1 PLACE ± 0.25 ± --- ANGULAR ± 3 °	mm INCH DIMENSION STYLE MM ONLY	TITLE		6 IN 1 AND 5 IN 1 COMBO MEMORY CARD CONNECTOR (TOP MOUNT TYPE)	
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	DRAWN BY DATE G.YANG 2003/08/14 CHECKED BY DATE W.CHANG 2003/08/22 APPROVED BY DATE E.LAN 2003/08/22	MOLEX INCORPORATED MATERIAL NO. SEE TABLE DOCUMENT NO. SD-48071-001 SHEET NO. 5 OF 5			
			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

9 8 7 6 5 4 3 2 1