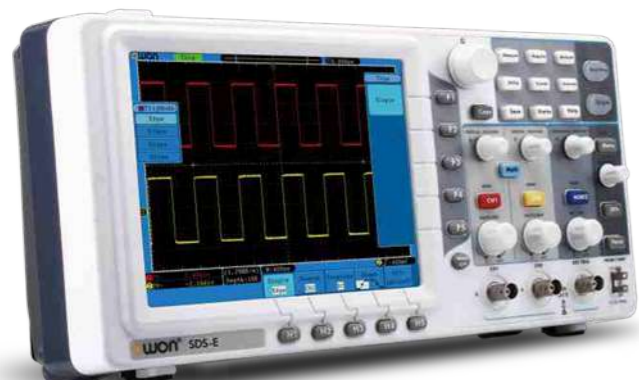


SDS-E Series 2nd generation economical type digital storage oscilloscope



- + Bandwidth : 30MHz - 125MHz
- + Sample rate : 250MS/s - 1GS/s
- + 100K record length
(10M optional, excluding SDS5032E)
- + Ultra-thin body
- + 8 inch high resolution LCD
- + Pass / Fail function
- + SCPI Supported (excluding SDS5032E)



+ Performance Specifications

Model	SDS5032E	SDS6062E	SDS7072E	SDS7102E	SDS7122E
Bandwidth	30MHz	60MHz	70MHz	100MHz	125MHz
Sample Rate	250MS/s	500MS/s	1GS/s		
Horizontal Scale (s/div)	5ns/div - 100s/div, step by 1 - 2 - 5		2ns/div - 100s/div, step by 1 - 2 - 5		
Rise Time (at input, typical)	≤11ns	≤5.8ns	≤5ns	≤3.5ns	≤2.8ns
Channel	2 + 1 (external)				
Display	8" color LCD, 800 x 600 pixels				
Input Impedance	1MΩ ± 2%, in parallel with 10pF ± 5pF		1MΩ ± 2%, in parallel with 15pF ± 3pF		
Channel Isolation	50Hz : 100 : 1, 10MHz : 40 : 1				
Max Input Voltage	400V (PK - PK) (DC+AC, PK - PK)				
DC Gain Accuracy	±3%				
Record Length	10K	100K (optional 10M)			
DC Accuracy (average)	average ≥ 16 : ±(3% reading + 0.05 div) for ΔV				
Probe Attenuation Factor	1X, 10X, 100X, 1000X				
LF Respond (AC, -3dB)	≥10Hz (at input, AC coupling, -3dB)				
Sample Rate / Relay Time Accuracy	±100ppm				
Interpolation	sin(x)/x				
Interval (ΔT) Accuracy (full bandwidth)	Single : ±(1 interval time + 100ppm × reading + 0.6ns), Average > 16 : ±(1 interval time + 100ppm × reading + 0.4ns)				
Input Coupling	DC, AC, and GND				
Vertical Resolution (A/D)	8 bits (2 channels simultaneously)				
Vertical Sensitivity	5mV/div - 10V/div (at input)	2mV/div - 10V/div (at input)			

Model	SDS5032E	SDS6062E	SDS7072E	SDS7102E	SDS7122E
Trigger Type	Edge, Pulse, Video, Slope, and Alternate				
Trigger Mode	Auto, Normal, and Single				
Trigger Level	±6 divisions from screen center				
Line / Field Frequency (video)	NTSC, PAL, and SECAM standard				
Cursor Measurement	ΔV, and ΔT between cursors				
Automatic Measurement	Vpp, Vavg, Vrms, Freq, Period, Vmax, Vmin, Vtop, Vbase, Vamp, Overshoot, Preshoot, Rise Time, Fall Time, Delay A→B, Delay A→B, +Width, -Width, +Duty, -Duty				
Waveform Math	+, -, ×, ÷, invert, FFT				
Waveform Storage	15 waveforms				
Lissajous Figure	Bandwidth	full bandwidth			
	Phase Difference	±3 degrees			
Communication Interface	USB host, USB device, Pass / Fail, LAN, and VGA (optional)				
Frequency Counter	available				
Power Supply	100V - 240V AC, 50/60Hz, CAT II				
Power Consumption	<18W				
Fuse	2A, T class, 250V				
Battery	not supported				
Dimension (W x H x D)	348 x 170 x 78 (mm)				
Weight (without package)	1.50 kg				

Specifications subject to change without prior notice.

+ Application

electronic circuit debugging circuit testing design and manufacture
education and training automobile maintenance and testing

+ Accessories

The accessories subject to final delivery.

