

April 2010

# MID400 AC Line Monitor Logic-Out Device

#### **Features**

- Direct operation from any line voltage with the use of an external resistor.
- Externally adjustable time delay
- Externally adjustable AC voltage sensing level
- High voltage isolation between input and output
- Compact plastic DIP package
- Logic level compatibility
- UL recognized (File #E90700)
- VDE recognized (file #102915), add option V (e.g., MID400V)

### **Applications**

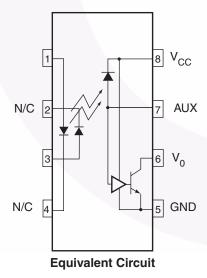
- Monitoring of the AC/DC "line-down" condition
- "Closed-loop" interface between electromechanical elements such as solenoids, relay contacts, small motors, and microprocessors
- Time delay isolation switch

### **Description**

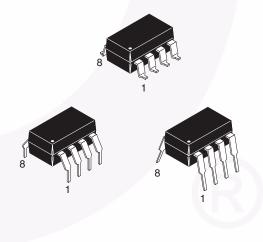
The MID400 is an optically isolated AC line-to-logic interface device. It is packaged in an 8-lead plastic DIP. The AC line voltage is monitored by two back-to-back GaAs LED diodes in series with an external resistor. A high gain detector circuit senses the LED current and drives the output gate to a logic low condition.

The MID400 has been designed solely for the use as an **AC line monitor**. It is recommended for use in any AC-to-DC control application where excellent optical isolation, solid state reliability, TTL compatibility, small size, low power, and low frequency operations are required.

#### **Schematic**



# **Package Outlines**



## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Value	Unit
TOTAL DEVIC	E		1
T <sub>STG</sub>	Storage Temperature	-55 to +125	°C
T <sub>OPR</sub>	Operating Temperature	-40 to +85	°C
T <sub>SOL</sub>	Lead Solder Temperature	260 for 10 sec	°C
P <sub>D</sub>	Total Device Power Dissipation @ T <sub>A</sub> = 25°C	115	mW
	Derate above 70°C	4.0	mW/°C
	Steady State Isolation	2500	VRMS
EMITTER			
	RMS Current	25	mA
	DC Current	±30	mA
$P_{D}$	LED Power Dissipation @ T <sub>A</sub> = 25°C	45	mW
	Derate above 70°C	2.0	mW/°C
DETECTOR			
l <sub>OL</sub>	Low Level Output Current	20	mA
V <sub>OH</sub>	High Level Output Voltage	7.0	V
V <sub>CC</sub>	Supply Voltage	7.0	V
P <sub>D</sub>	Detector Power Dissipation @ T <sub>A</sub> = 25°C	70	mW
	Derate above 70°C	2.0	mW/°C

#### **Electrical Characteristics**

(0°C to 70°C Free Air Temperature unless otherwise specified-All typical values are at 25°C)

#### **Individual Component Characteristics**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
<b>EMITTER</b>						
V <sub>F</sub>	Input Forward Voltage	$I_F = \pm 30$ mA, DC			1.5	V
DETECTOR						
I <sub>CCL</sub>	Logic Low Output Supply Current	$I_{IN}$ = 4.0 mA RMS, $V_{O}$ = Open, $V_{CC}$ = 5.5V, 24V $V_{I (ON)}$ , RMS $\leq$ 240V			3.0	mA
I <sub>CCH</sub>	Logic High Output Supply Current	$I_{IN}$ = 0.15mA RMS, $V_{CC}$ = 5.5V, $V_{I (OFF)}$ , RMS $\geq$ 5.5V			0.80	mA

#### **Transfer Characteristics**

Symbol	DC Characteristics	Test Conditions	Min.	Тур.	Max.	Units
V <sub>OL</sub>	Logic Low Output Current	$I_{IN} = I_{I (ON)}$ RMS, $I_{O} = 16$ mA, $V_{CC} = 4.5$ V, $24$ V $\leq$ V $_{I (ON)}$ , RMS $\leq$ 240V		0.18	0.40	V
ГОН	Logic High Output Current	$\begin{split} I_{IN} &= 0.15 \text{mA RMS}, \ V_O = V_{CC} = 5.5 \text{V}, \\ V_{I \ (OFF)}, \ RMS \geq \ 5.5 \text{V} \end{split}$		0.02	100	μA
V <sub>I (ON)</sub> RMS	On-state RMS Input Voltage	$V_O$ = 0.4V, $I_O$ = 16mA, $V_{CC}$ = 4.5V, $R_{IN}$ = 22k $\Omega$	90			V
V <sub>I (OFF)</sub> RMS	Off-state RMS Input Voltage	$V_O = V_{CC} = 5.5 \text{ V}, I_O \le 100 \mu\text{A},$ $R_{IN} = 22 k\Omega$			5.5	V
I <sub>I (ON)</sub> RMS	On-state RMS Input Current	$V_O = 0.4V$ , $I_O = 16mA$ , $V_{CC} = 4.5V$ , $24V \le V_{I (ON)}$ , RMS $\le 240V$	4.0			mA
I <sub>I (OFF)</sub> RMS	Off-state RMS Input Current	$V_O = V_{CC} = 5.5V$ , $I_O \le 100 \mu A$ , $V_{I~(OFF)}$ , RMS $\ge 5.5V$			0.15	mA

#### **Transfer Characteristics**

Symbol	Characteristics	Test Conditions	Min.	Тур.	Max.	Units	
SWITCHING	<b>SWITCHING TIME</b> $(T_A = 25^{\circ}C)$						
t <sub>ON</sub>	Turn-On Time	$I_{IN}$ = 4.0mA RMS, $I_{O}$ = 16mA, $V_{CC}$ = 4.5V, $R_{IN}$ = 22k $\Omega$ (See Test Circuit 2)		1.0		ms	
t <sub>OFF</sub>	Turn-Off Time	$I_{IN}$ = 4.0mA RMS, $I_{O}$ = 16mA, $V_{CC}$ = 4.5V, $R_{IN}$ = 22k $\Omega$ (See Test Circuit 2)		1.0		ms	

(RMS = True RMS Voltage at 60 Hz, THD  $\leq$  1%)

# **Isolation Characteristics** $(T_A = 25^{\circ}C)$

Symbol	Characteristics	Test Conditions	Min.	Тур.	Max.	Units
V <sub>ISO</sub>	Steady State Isolation Voltage	Relative Humidity $\leq$ 50%, I <sub>I-O</sub> $\leq$ 10 $\mu$ A, 1 Minute, 60Hz	2500			VRMS
R <sub>ISO</sub>	Isolation Resistance	V <sub>I-O</sub> = 500VDC	10 <sup>11</sup>			Ω
C <sub>ISO</sub>	Isolation Capacitance	f = 1MHz			2	pF

### **Description/Applications**

The input of the MID400 consists of two back-to-back LED diodes which will accept and convert alternating currents into light energy. An integrated photo diodedetector amplifier forms the output network. Optical coupling between input and output provides 2500 VRMS voltage isolation. A very high current transfer ratio (defined as the ratio of the DC output current and the DC input current) is achieved through the use of high gain amplifier. The detector amplifier circuitry operates from a 5V DC supply and drives an open collector transistor output. The switching times are intentionally designed to be slow in order to enable the MID400, when used as an AC line monitor, to respond only to changes in input voltage exceeding many milliseconds. The short period of time during zero-crossing which occurs once every half cycle of the power line is completely ignored. To operate the MID400, always add a resistor, R<sub>IN</sub>, in series with the input (as shown in test circuit 1) to limit the current to the required value. The value of the resistor can be determined by the following equation:

$$R_{IN} = \frac{V_{IN} - V_F}{I_{IN}}$$

Mhara

V<sub>IN</sub> (RMS) is the input voltage.

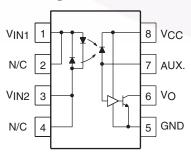
V<sub>F</sub> is the forward voltage drop across the LED.

 $I_{\rm IN}$  (RMS) is the desired input current required to sustain a logic "O" on the output.

## **Pin Description**

	•	
Pin Number	Pin Name	Function
1, 3	$V_{IN1}, V_{IN2}$	Input terminals
2, 4	N/C	No Connect
8	V <sub>CC</sub>	Supply voltage, output circuit.
7	AUX	Auxiliary terminal. Programmable capacitor input to adjust AC voltage sensing level and time delay.
6	V <sub>O</sub>	Output terminal; open collector.
5	GND	Circuit ground potential.

# **Schematic Diagram**



### Glossary

#### **VOLTAGES**

### V<sub>I (ON)</sub> RMS On-State RMS Input Voltage

The RMS voltage at an input terminal for a specified input current with output conditions applied that according to the product specification will cause the output switching element to be sustained in the on-state within one full cycle.

#### V<sub>I (OFF)</sub> RMS Off-State RMS Input Voltage

The RMS voltage at an input terminal for a specified input current with output conditions applied that according to the product specification will cause the output switching element to be sustained in the off-state within one full cycle.

### V<sub>OL</sub> Low-Level Output Voltage

The voltage at an output terminal for a specific output current  $I_{OL}$ , with input conditions applied that according to the product specification will establish a low-level at the output.

#### V<sub>OH</sub> High-Level Output Voltage

The voltage at an output terminal for a specific output current  $I_{OH}$ , with input conditions applied that according to the product specification will establish a high-level at the output.

#### V<sub>F</sub> LED Forward Voltage

The voltage developed across the LED when input current  $I_{\text{F}}$  is applied to the anode of the LED.

#### **CURRENTS**

#### I<sub>I (ON)</sub> RMS On-State RMS Input Current

The RMS current flowing into an input with output conditions applied that according to the product specification will cause the output switching element to be sustained in the on-state within one full cycle.

### I<sub>I (OFF)</sub> RMS Off-state RMS Input Current

The RMS current flowing into an input with output conditions applied that according to the product specification will cause the output switching element to be sustained in the off-state within one full cycle.

### I<sub>OH</sub> High-Level Output Current

The current flowing into \* an output with input conditions applied that according to the product specification will establish a high-level at the output.

\*Current flowing out of a terminal is a negative value.

#### I<sub>OL</sub> Low-Level Output Current

The current flowing into \* an output with input conditions applied that according to the product specification will establish a low-level at the output.

### I<sub>CCL</sub> Supply Current, Output LOW

The current flowing into \* the V<sub>CC</sub> supply terminal of a circuit when the output is at a low-level voltage.

### I<sub>CCH</sub> Supply Current, Output HIGH

The current flowing into  $^{\star}$  the  $V_{CC}$  supply terminal of a circuit when the output is at a high-level voltage.

\* Current flowing out of a terminal is a negative value.

#### **DYNAMIC CHARACTERISTICS**

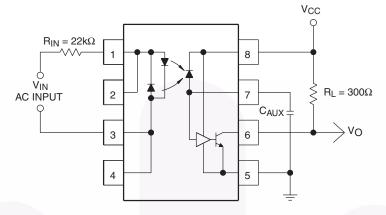
#### t<sub>ON</sub> Turn-On Time

The time between the specified reference points on the input and the output voltage waveforms with the output changing from the defined high-level to the defined low-level.

### t<sub>OFF</sub> Turn-Off time

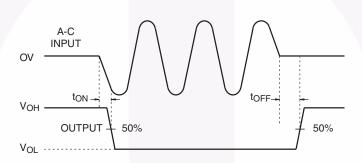
The time between the specified reference points on the input and the output voltage waveforms with the output changing from the defined low-level to the defined high-level.

# **Operating Schematics**

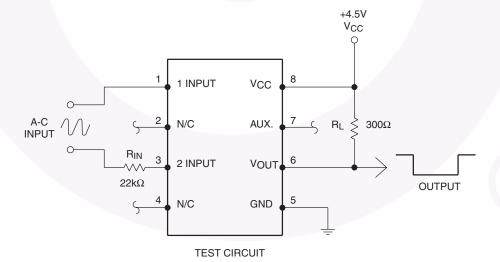


INPUT CURRENT VS. CAPACITANCE, CAUX CIRCUIT

#### **TEST CIRCUIT 1**



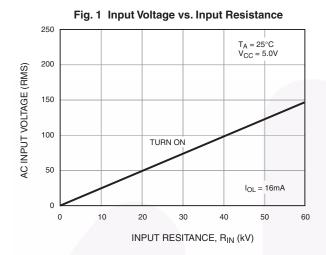
\* INPUT TURNS ON AND OFF AT ZERO CROSSING

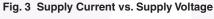


TEST CIRCUIT 2 MID400 Switching Time

60

# **Typical Performance Curves**





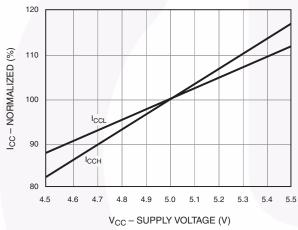


Fig. 4 Input Current vs. Capacitance

INPUT RESITANCE,  $R_{\text{IN}}$  ( $k\Omega$ )

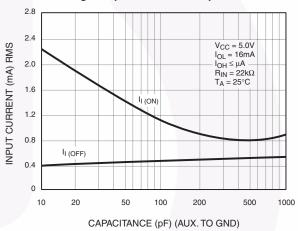
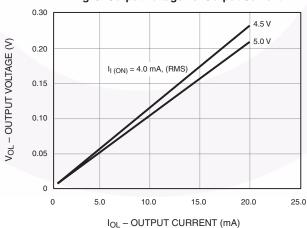


Fig. 5 Output Voltage vs. Output Current

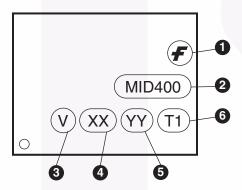
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# **Ordering Information**

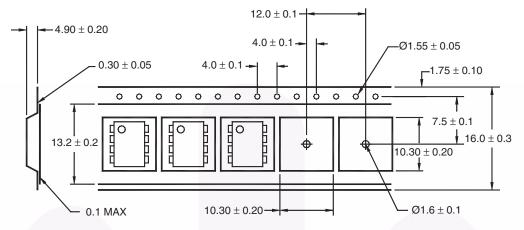
Option	Example Part Number	Description		
No Option	MID400	Standard Through Hole		
S	MID400S	Surface Mount Lead Bend		
SD	MID400SD	Surface Mount; Tape and reel		
V	MID400V	VDE0884		
WV	MID400WV	VDE0884; 0.4" Lead Spacing		
SV MID400SV VDE0884; Surface Mor		VDE0884; Surface Mount		
SDV MID400SDV		VDE0884; Surface Mount; Tape and Reel		

# **Marking Information**



Definitions				
1	Fairchild logo			
2	Device number			
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)			
4	Two digit year code, e.g., '03'			
5	Two digit work week ranging from '01' to '53'			
6	Assembly package code			

# **Carrier Tape Specifications ("D" Taping Orientation)**

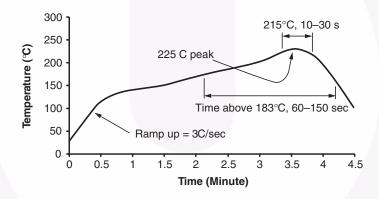


User Direction of Feed -

#### Note:

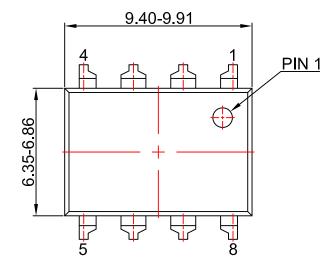
All dimensions are in inches (millimeters)

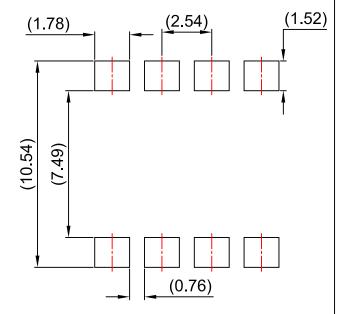
#### **Reflow Profile**

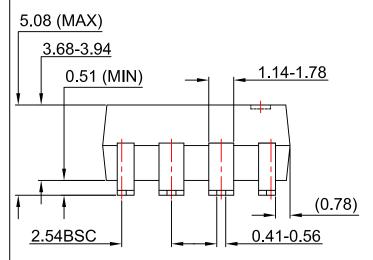


- Peak reflow temperature: 225°C (package surface temperature)
   Time of temperature higher than 183°C for 60–150 seconds
   One time soldering reflow is recommended

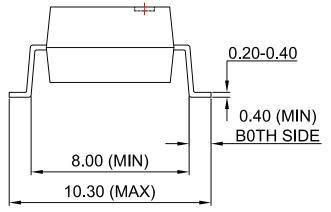








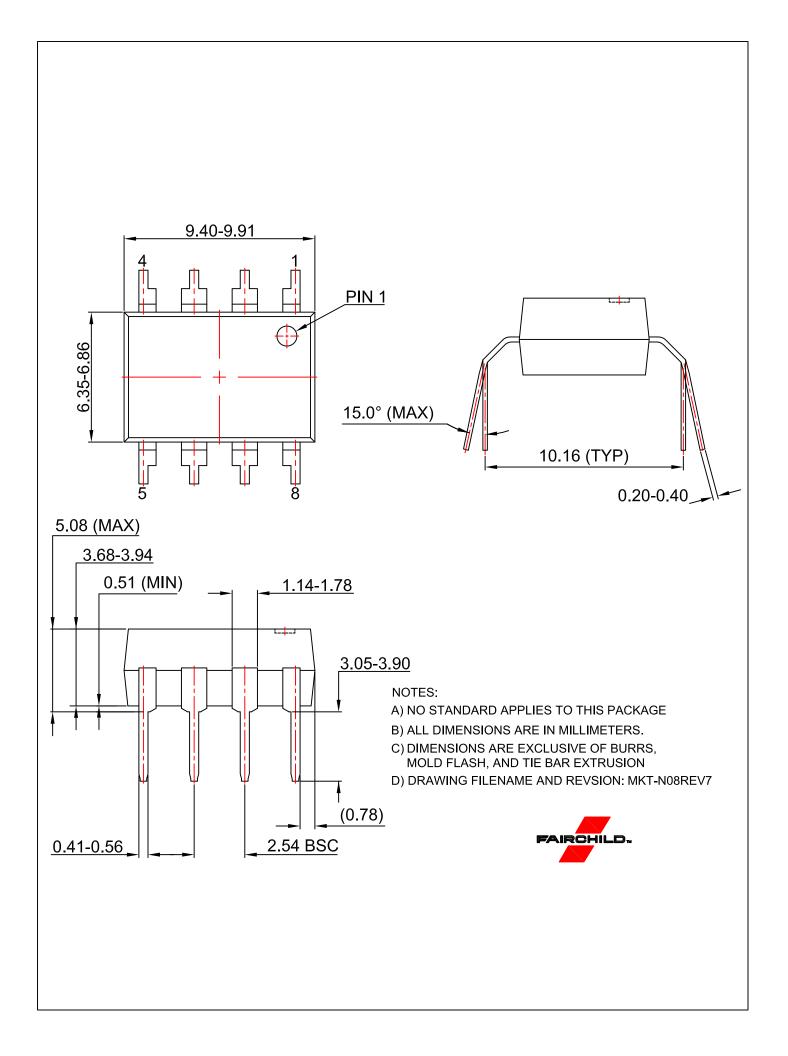




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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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