

March 2015

FGH40N60UFD 600 V, 40 A Field Stop IGBT

Features

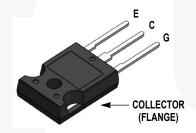
- · High Current Capability
- Low Saturation Voltage: V_{CE(sat)} = 1.8 V @ I_C = 40 A
- · High Input Impedance
- · Fast Switching
- RoHS Compliant

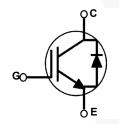
Applications

· Solar Inverter, UPS, Welder, PFC, Microwave Oven, Telecom, ESS

General Description

Using novel field stop IGBT technology, Fairchild's field stop IGBTs offer the optimum performance for solar inverter, UPS, welder, microwave oven, telecom, ESS and PFC applications where low conduction and switching losses are essential.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit
V _{CES}	Collector to Emitter Voltage		600	V
V _{GES}	Gate to Emitter Voltage		± 20	V
*GES	Transient Gate-to-Emitter Voltage		± 30	V
I _C	Collector Current	@ T _C = 25°C	80	Α
10	Collector Current	@ T _C = 100°C	40	Α
I _{CM (1)}	Pulsed Collector Current	@ T _C = 25°C	120	Α
P _D	Maximum Power Dissipation	@ T _C = 25°C	290	W
	Maximum Power Dissipation	@ T _C = 100°C	116	W
T _J	Operating Junction Temperature		-55 to +150	°C
T _{stg}	Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes:1: Repetitive rating: Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	-	0.43	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	-	1.45	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGH40N60UFDTU	FGH40N60UFD	TO-247	Tube	N/A	N/A	30

Electrical Characteristics of the IGBT $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV _{CES}	Collector to Emitter Breakdown Voltage	V _{GE} = 0 V, I _C = 250 μA	600	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_{J}}$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	-	0.6	-	V/°C
I _{CES}	Collector Cut-Off Current	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μА
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	±400	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	I _C = 250 μA, V _{CE} = V _{GE}	4.0	5.0	6.5	V
		I _C = 40 A, V _{GE} = 15 V	-	1.8	2.4	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	I _C = 40 A, V _{GE} = 15 V, T _C = 125°C	-	2.0	-	٧
Dynamic C	haracteristics					
C _{ies}	Input Capacitance		-	2110	-	pF
C _{oes}	Output Capacitance	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V},$ f = 1 MHz	-	200	-	pF
C _{res}	Reverse Transfer Capacitance	1 - 1 WILIZ	-	60	-	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time		-	24	-	ns
t _r	Rise Time		- /	44	-	ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 400 \text{ V}, I_{C} = 40 \text{ A},$		112	- ,	ns
t _f	Fall Time	$R_G = 10 \Omega, V_{GE} = 15 V,$	-	30	60	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 25°C	-	1.19	-	mJ
E _{off}	Turn-Off Switching Loss		-	0.46	-	mJ
E _{ts}	Total Switching Loss		-	1.65	- /	mJ
t _{d(on)}	Turn-On Delay Time		-	24	-	ns
t _r	Rise Time		-	45	- \	ns
t _{d(off)}	Turn-Off Delay Time	$V_{CC} = 400 \text{ V}, I_{C} = 40 \text{ A},$	-	120	-	ns
t _f	Fall Time	$R_G = 10 \Omega, V_{GE} = 15 V,$	-	40	-	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 125°C	-	1.2	-	mJ
E _{off}	Turn-Off Switching Loss		-	0.69	-	mJ
E _{ts}	Total Switching Loss		-	1.89	-	mJ
Qg	Total Gate Charge		-	120	-	nC
Q _{ge}	Gate to Emitter Charge	$V_{CE} = 400 \text{ V}, I_{C} = 40 \text{ A},$ $V_{GF} = 15 \text{ V}$	-	14	-	nC
Q _{gc}	Gate to Collector Charge] *GE = 10 *	-	58	-	nC

Electrical Characteristics of the Diode $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min.	Тур.	Max	Unit	
V _{FM}	Diode Forward Voltage	I _E = 2	n A	T _C = 25°C	-	1.95	2.6	V
	Slode Forward Voltage	14 2071	T _C = 125°C	-	1.85	-]	
t _{rr} Di	Diode Reverse Recovery Time) A, di _F /dt = 200 A/μs	T _C = 25°C	-	45	-	ns
41	Disas reverse reservery rime			T _C = 125°C	-	140	-	
Q _{rr}	Diode Reverse Recovery Charge	'F C	71, dip/dt 20070µ0	T _C = 25°C	-	75	-	nC
				T _C = 125°C	-	375	-	

Figure 1. Typical Output Characteristics

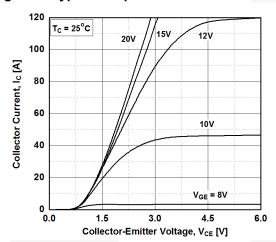


Figure 3. Typical Saturation Voltage Characteristics

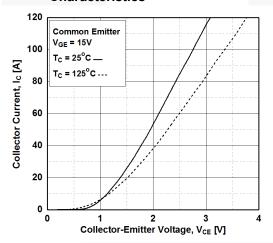


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level

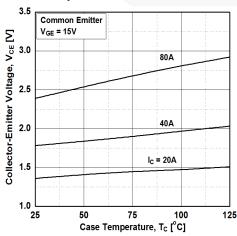


Figure 2. Typical Output Characteristics

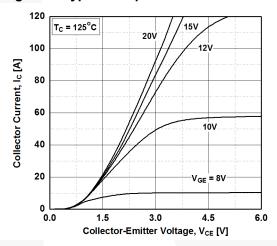


Figure 4. Transfer Characteristics

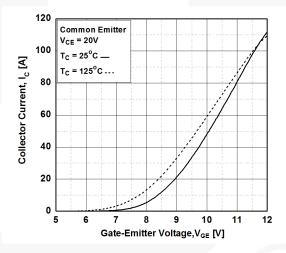


Figure 6. Saturation Voltage vs. V_{GE}

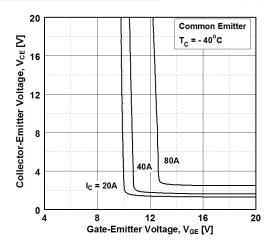


Figure 7. Saturation Voltage vs. V_{GE}

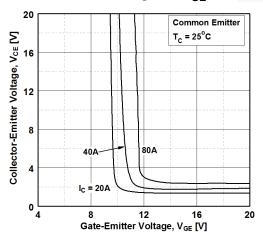


Figure 9. Capacitance Characteristics

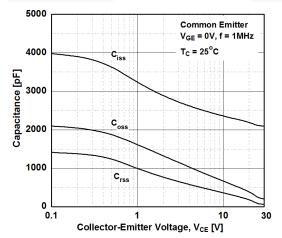


Figure 11. SOA Characteristics

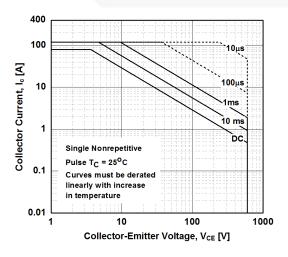


Figure 8. Saturation Voltage vs. V_{GE}

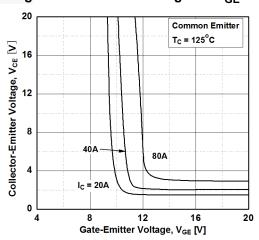


Figure 10. Gate charge Characteristics

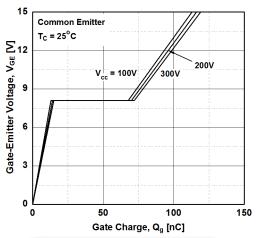


Figure 12. Turn-on Characteristics vs. Gate Resistance

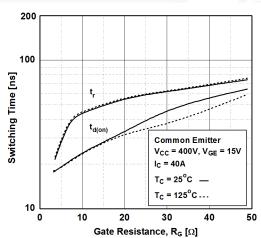


Figure 13. Turn-off Characteristics vs.
Gate Resistance

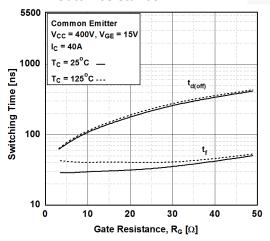


Figure 15. Turn-off Characteristics vs. Collector Current

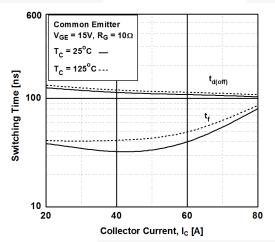


Figure 17. Switching Loss vs. Collector Current

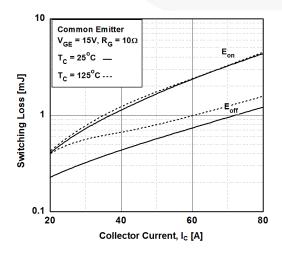


Figure 14. Turn-on Characteristics vs.
Collector Current

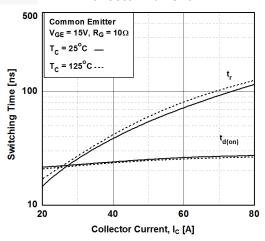


Figure 16. Switching Loss vs. Gate Resistance

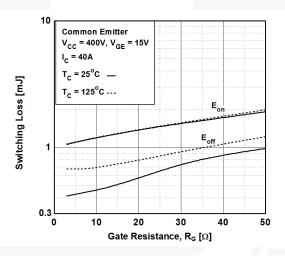


Figure 18. Turn off Switching SOA Characteristics

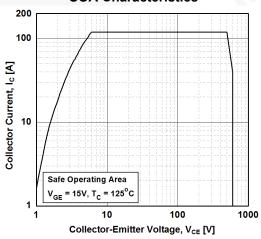


Figure 19. Forward Characteristics

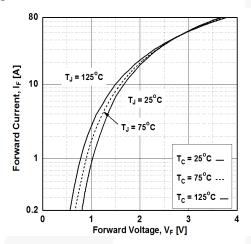


Figure 21. Stored Charge

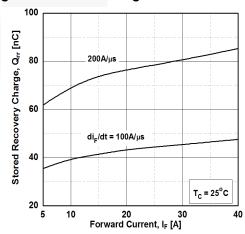


Figure 20. Reverse Current

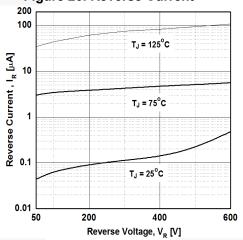


Figure 22. Reverse Recovery Time

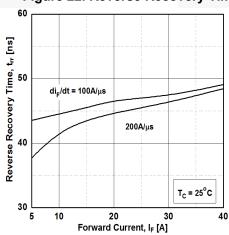
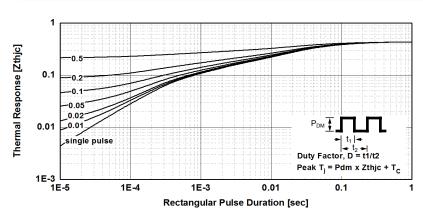
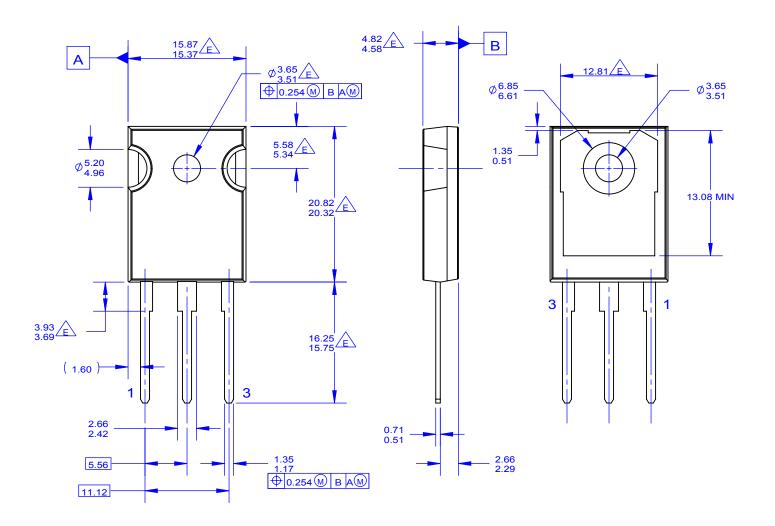


Figure 23. Transient Thermal Impedance of IGBT





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Definition of Terms

Definition of Terms							
Datasheet Identification	Product Status	Definition					
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.					
Preliminary First Production		Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.					
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