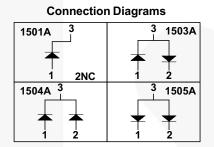


November 2014

# MMBD1501A / MMBD1503A / MMBD1504A / MMBD1505A Small Signal Diodes





# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MMBD1501A	A11	SOT-23 3L	Tape and Reel
MMBD1503A	A13	SOT-23 3L	Tape and Reel
MMBD1503A_D87Z	A13	SOT-23 3L	Tape and Reel
MMBD1504A	A14	SOT-23 3L	Tape and Reel
MMBD1505A	A15	SOT-23 3L	Tape and Reel

# Absolute Maximum Ratings(1), (2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter		Value	Unit
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage		200	V
I <sub>F(AV)</sub>	Average Rectified Forward Current		200	mA
1=014	Non-Repetitive Peak Forward Surge Current	Pulse Width = 1.0 second	1.0	A
		Pulse Width = 1.0 microsecond	2.0	
T <sub>STG</sub>	Storage Temperature Range		-55 to +150	°C
T <sub>J</sub>	Operating Junction Temperature		150	°C

# Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

# **Thermal Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
P <sub>D</sub>	Power Dissipation	350	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	357	°C/W

# **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>R</sub>	Breakdown Voltage	I <sub>R</sub> = 5.0 μA	200		V
	Forward Voltage	I <sub>F</sub> = 1.0 mA	620	720	mV
		I <sub>F</sub> = 10 mA	720	830	mV
V-		I <sub>F</sub> = 50 mA	800	890	mV
V <sub>F</sub>		I <sub>F</sub> = 100 mA	830	930	mV
		$I_F = 200 \text{ mA}$	0.87	1.10	V
		$I_F = 300 \text{ mA}$	0.90	1.15	V
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 125 V		1.0	nA
		$V_R = 125 \text{ V}, T_A = 150^{\circ}\text{C}$		3.0	μΑ
		V <sub>R</sub> = 180 V	\ \	10.0	nA
		$V_R = 180 \text{ V}, T_A = 150^{\circ}\text{C}$		5.0	μΑ
C <sub>T</sub>	Total Capacitance	$V_R = 0$ , $f = 1.0 \text{ MHz}$		4.0	pF

# **Typical Performance Characteristics**

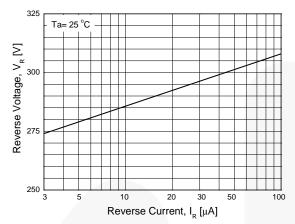


Figure 1. Reverse Voltage vs. Reverse Current BV - 3.0 to 100  $\mu A$ 

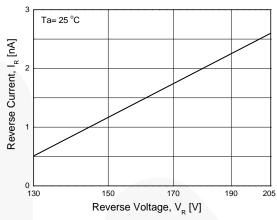


Figure 2. Reverse Current vs. Reverse Voltage IR - 130 to 205 V

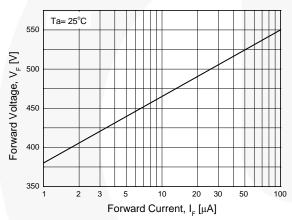


Figure 3. Forward Voltage vs. Forward Current VF - 1 to 100  $\mu A$ 

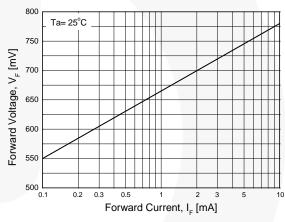


Figure 4. Forward Voltage vs. Forward Current VF - 0.1 to 10 mA

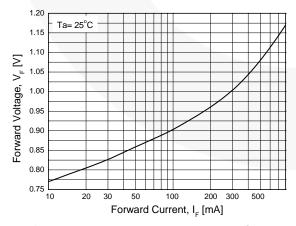


Figure 5. Forward Voltage vs. Forward Current VF - 10 to 800 mA

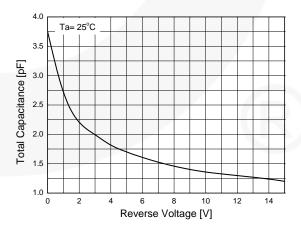


Figure 6. Total Capacitance vs. Reverse Voltage VR - 0 to 15 V

# **Typical Performance Characteristics** (Continued)

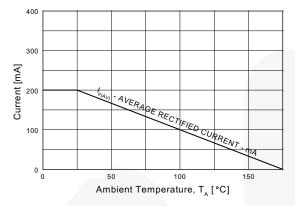


Figure 7. Average Rectified Current ( $I_{F(AV)}$ ) vs. Ambient Temperature ( $T_A$ )

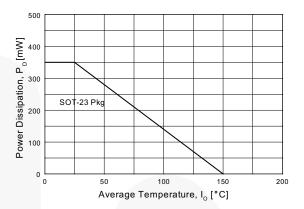
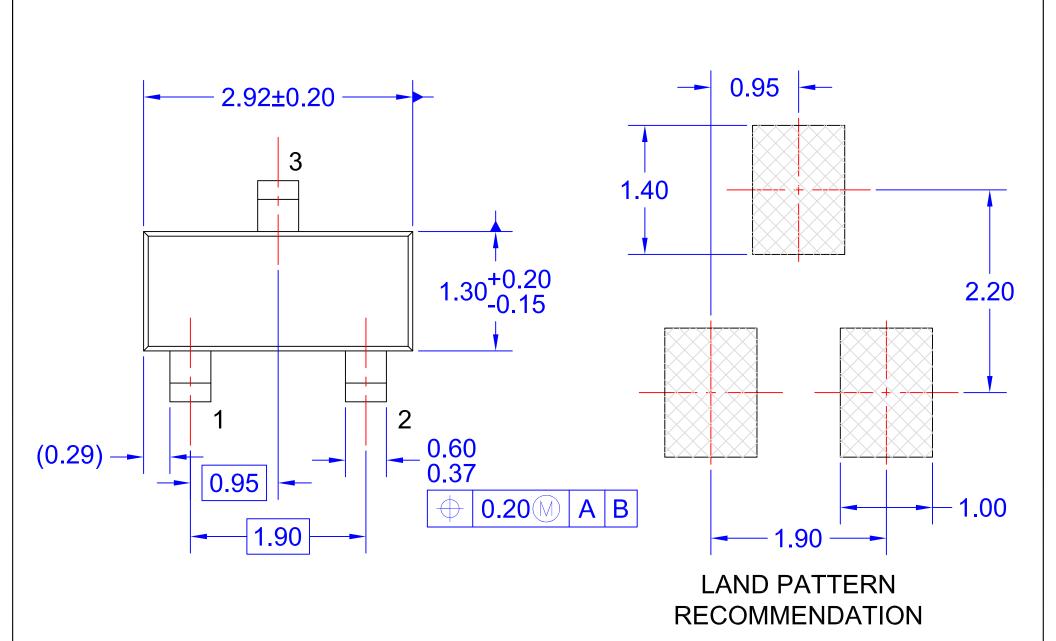
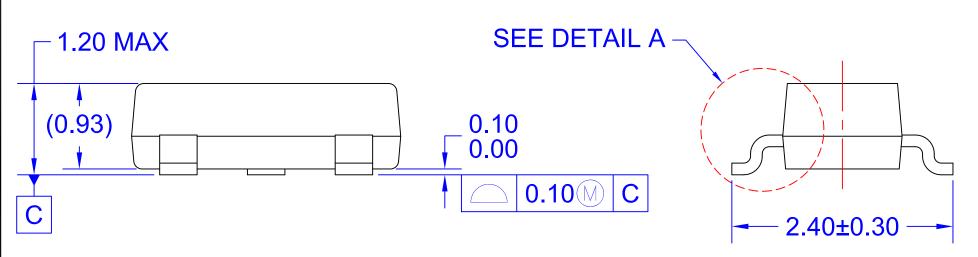
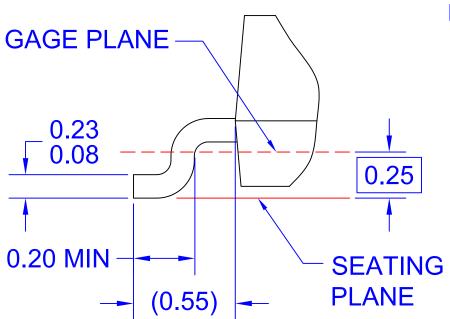


Figure 8. Power Derating Curve







DETAIL A
SCALE: 2X

# **NOTES: UNLESS OTHERWISE SPECIFIED**

- A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE INCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M 2009.
- E) DRAWING FILE NAME: MA03DREV11







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# Definition of Terms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.	
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.	
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