# VSMY1940ITX01



# High Speed Infrared Emitting Diodes, 940 nm, Surface Emitter Technology



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### DESCRIPTION

As part of the <u>SurfLight<sup>TM</sup></u> portfolio, the VSMY1940ITX01 is an infrared, 940 nm emitting diode based on GaAlAs surface emitter chip technology with high radiant intensity, high optical power and high speed, molded in clear, untinted 0805 plastic package for surface mounting (SMD).

### FEATURES

- Package type: surface mount
- Package form: 0805
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.85
- AEC-Q101 qualified
- Enhanced operating temperature range: -40 °C to +105 °C
- Peak wavelength:  $\lambda_p = 940 \text{ nm}$
- High reliability
- High radiant power
- High radiant intensity
- High speed
- Angle of half sensitivity:  $\phi = \pm 60^{\circ}$
- Suitable for high pulse current operation
- 0805 standard surface-mountable package
- Floor life: 72 h, MSL 4, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- Miniature light barrier
- Photointerrupters
- Optical switch
- · Emitter source for proximity sensors
- Emitter for automotive applications (e.g. rain sensor)
- IR flash
- IR illumination

PRODUCT SUMMARY					
COMPONENT	l <sub>e</sub> (mW/sr)	φ (deg)	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)	
VSMY1940ITX01	10	± 60	940	10	

#### Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VSMY1940ITX01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805

#### Note

• MOQ: minimum order quantity









# **VSMY1940ITX01**



## **Vishay Semiconductors**

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	5	V
Forward current		I <sub>F</sub>	100	mA
Peak forward current	$t_p/T = 0.1, t_p = 100 \ \mu s$	I <sub>FM</sub>	200	mA
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	1	А
Power dissipation		Pv	180	mW
Junction temperature		Tj	110	°C
Operating temperature range		T <sub>amb</sub>	-40 to +105	°C
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C
Soldering temperature	acc. figure 10, J-STD-020	T <sub>sd</sub>	260	°C
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	R <sub>thJA</sub>	250	K/W

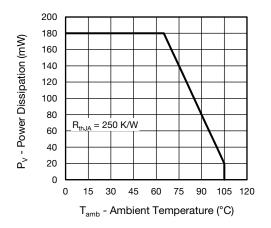


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

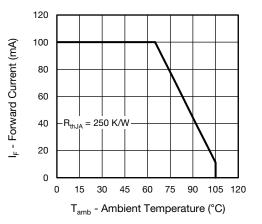


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	V <sub>F</sub>		1.45	1.8	V
	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	V <sub>F</sub>		2.2		V
Temperature coefficient of $V_F$	I <sub>F</sub> = 100 mA	TK <sub>VF</sub>		-2		mV/K
Reverse current		I <sub>R</sub>	not design	ed for revers	e operation	μA
Junction capacitance	$V_R = 0 V$ , f = 1 MHz, E = 0 mW/cm <sup>2</sup>	CJ		125		pF
Dedient intereit.	I <sub>F</sub> = 100 mA, t <sub>p</sub> = 20 ms	l <sub>e</sub>	5	10	15	mW/sr
Radiant intensity	I <sub>F</sub> = 1 A, t <sub>p</sub> = 100 μs	l <sub>e</sub>		75		mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фе		50		mW
Temperature coefficient of radiant power	I <sub>F</sub> = 100 mA	ΤKφ <sub>e</sub>		-0.2		%/K
Angle of half intensity		φ		± 60		deg
Peak wavelength	I <sub>F</sub> = 100 mA	λρ	920	940	960	nm
Spectral bandwidth	I <sub>F</sub> = 100 mA	Δλ		40		nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 30 mA	ΤΚ <sub>λρ</sub>		0.25		nm
Rise time	$I_F$ = 100 mA, 20 % to 80 %	t <sub>r</sub>		10		ns
Fall time	$I_F$ = 100 mA, 20 % to 80 %	t <sub>f</sub>		10		ns

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### BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

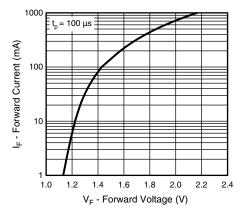


Fig. 3 - Forward Current vs. Forward Voltage

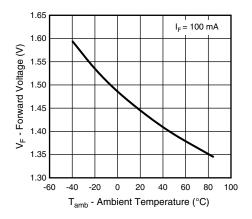


Fig. 4 - Forward Voltage vs. Ambient Temperature

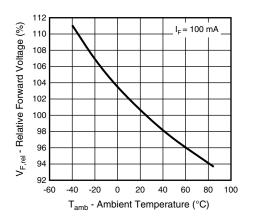


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

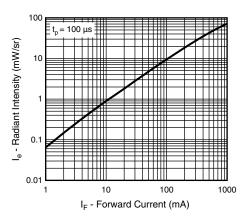


Fig. 6 - Radiant Intensity vs. Forward Current

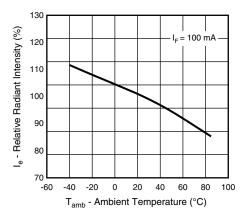


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

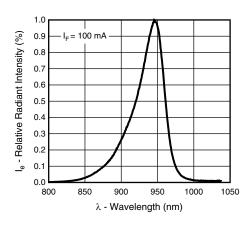


Fig. 8 - Relative Radiant Intensity vs. Wavelength

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## **Vishay Semiconductors**

### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### **FLOOR LIFE**

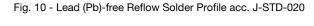
Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020: Moisture sensitivity: level 4 Floor life: 72 h Conditions:  $T_{amb} < 30\ ^\circ C,\ RH < 60\ \%$ 

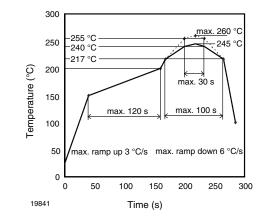
### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

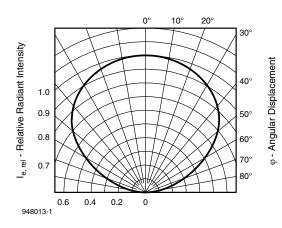


### **REFLOW SOLDER PROFILE**







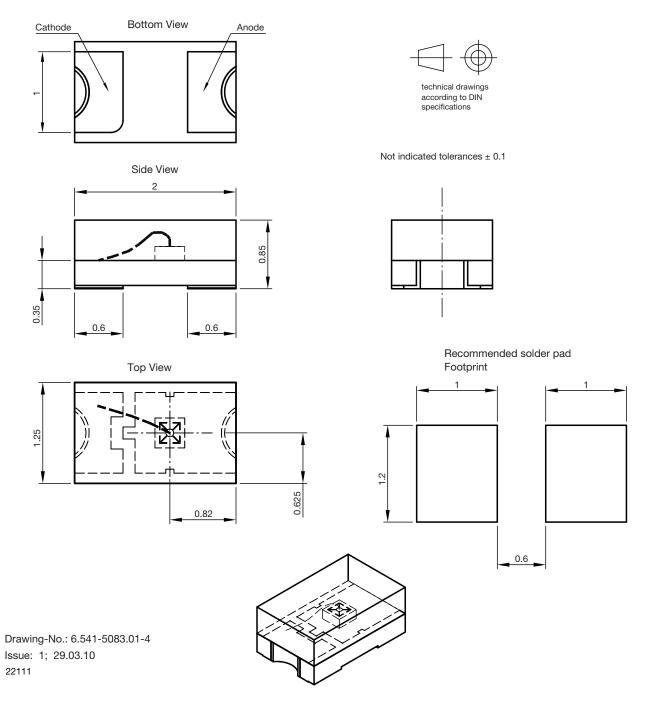


# Vishay Semiconductors



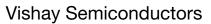
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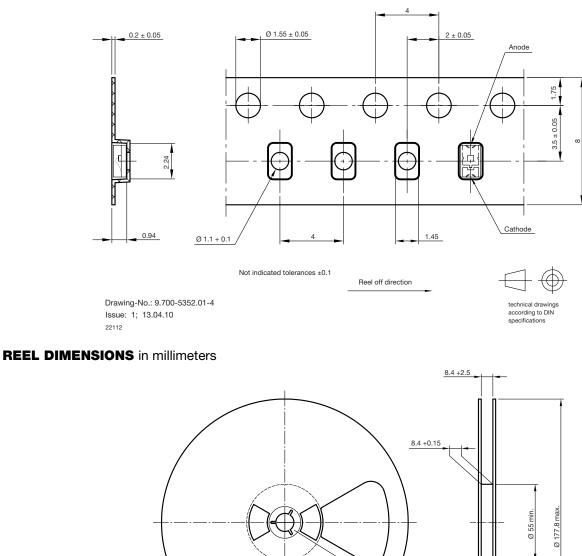
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Form of the leave open of the wheel is supplier specific.

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14.4 max.

technical drawings according to DIN specifications



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