Meets the resistance required range for packaging of EN 61340-5-1 and Packaging standard IEC 61340-5-3 per IEC 61340-2-3.

A. Vermason PCB racks are moulded in black conductive polypropylene.
B. These racks are a good investment to protect CMOS loaded boards, the design enables the storing of boards approximately 25% longer than their base depth.
C. It is recommended that they are used in conjunction with complete static protected work stations.
D. Electrical resistance $R_V$: $1 \times 10^3$ to $< 1 \times 10^5$ ohms (test method per IEC 61340-2-3).
E. Recommended maximum operating temperature is 75°C

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>237195</td>
<td>PCB Rack, 208mm x 272mm x 93mm, slots: 25 x 2mm</td>
</tr>
<tr>
<td>237200</td>
<td>PCB Rack, 355mm x 266mm x 128mm, slots: 25 x 3mm</td>
</tr>
</tbody>
</table>

“Risks of damage to semiconductor devices and some other electronic components arise in two main ways from static electricity:
Discharges of static electricity from conductors or charged insulators causing melting and evaporation of fine tracks on integrated circuit chips;
Electric fields from charged conductors and insulators causing electrical breakdown on insulation between features on integrated circuits.” (EN 61340-5-2 Introduction)
“A static audit with an electrostatic field meter should be carried out to determine the levels of static potential present.” (EN 61340-5-2 section 5.2.9.2)