

V _{CES}	650V
I _{C(100°C)}	25A
V _{CE(sat) (Typ.)}	1.6V
P _D	174W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

Applications

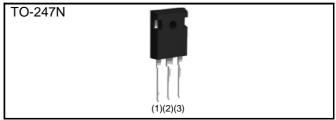
PFC

UPS

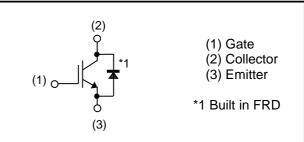
Power Conditioner

IH

Outline



Inner Circuit



Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Type	Tape Width (mm)	-
Туре	Basic Ordering Unit (pcs)	450
	Taping Code	C11
	Marking	RGTH50TS65D

•Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

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Parameter		Symbol	Value	Unit	
Collector - Emitter Voltage		V _{CES}	650	V	
Gate - Emitter Voltage		V _{GES}	±30	V	
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	Ι _C	50	А	
Collector Current	$T_{\rm C} = 100^{\circ}{\rm C}$	Ι _C	25	А	
Pulsed Collector Current		I _{CP} *1	100	А	
Diode Forward Current	$T_{\rm C} = 25^{\circ}{\rm C}$	I _F	35	А	
Dioue Forward Current	$T_{\rm C} = 100^{\circ}{\rm C}$	I _F	20	А	
Diode Pulsed Forward Current		I _{FP} ^{*1}	100	А	
Power Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$	P _D	174	W	
Power Dissipation	T _C = 100°C	P _D	87	W	
Operating Junction Temperatu	ire	Tj	-40 to +175	°C	
Storage Temperature		T _{stg}	-55 to +175	°C	
*1 Pulse width limited by T		•			

*1 Pulse width limited by T_{jmax.}

Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.86	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	2.28	°C/W

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
Faranieler	Symbol	Conditions	Min.	Тур.	Max.		
Collector - Emitter Breakdown Voltage	BV _{CES}	I _C = 10μΑ, V _{GE} = 0V	650	-	-	V	
Collector Cut - off Current	I _{CES}	V _{CE} = 650V, V _{GE} = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I _{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA	
Gate - Emitter Threshold Voltage	V _{GE(th)}	V _{CE} = 5V, I _C = 17.5mA	4.5	5.5	6.5	V	
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 25A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.6 2.1	2.1	V	

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

	0 1 1		Values			L lus it
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	V _{CE} = 30V	-	1410	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	57	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	22	-	
Total Gate Charge	Qg	V _{CE} = 300V	-	49	-	
Gate - Emitter Charge	Q _{ge}	I _C = 25A	-	15	-	nC
Gate - Collector Charge	Q _{gc}	V _{GE} = 15V	-	19	-	
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 25 {\rm A}, \ V_{\rm CC} = 400 {\rm V}$	-	27	-	
Rise Time	t _r	V_{GE} = 15V, R_G = 10 Ω	-	38	-	
Turn - off Delay Time	t _{d(off)}	$T_j = 25^{\circ}C$	-	94	-	ns
Fall Time	t _f	Inductive Load	-	50	-	
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 25$ A, $V_{\rm CC} = 400$ V	-	27	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	38	-	
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	107	-	ns
Fall Time	t _f	Inductive Load	-	65	-	
		$I_{\rm C} = 100$ A, $V_{\rm CC} = 520$ V			-	
Reverse Bias Safe Operating Area	RBSOA	$V_{P} = 650V, V_{GE} = 15V$	FU	LL SQUA	RE	-
		$R_{G} = 60\Omega, T_{j} = 175^{\circ}C$				

•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Symbol	Conditions	Values			Linit	
Parameter Symbol C		Conditions	Min.	Тур.	Max.	Unit	
Diode Forward Voltage	V _F	$I_F = 20A$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.45 1.25	1.9 -	V	
Diode Reverse Recovery Time	t _{rr}	I _F = 20A	-	58	-	ns	
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs	-	6.3	-	А	
Diode Reverse Recovery Charge	Q _{rr}	T _j = 25°C	-	0.20	-	μC	
Diode Reverse Recovery Time	t _{rr}	I _F = 20A	-	256	-	ns	
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs	-	10.4	-	А	
Diode Reverse Recovery Charge	Q _{rr}	T _j = 175°C	-	1.35	-	μC	

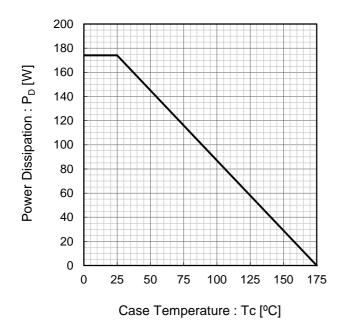


Fig.1 Power Dissipation vs. Case Temperature

Fig.2 Collector Current vs. Case Temperature

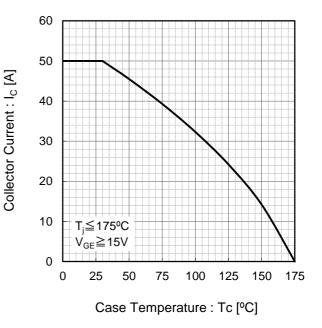
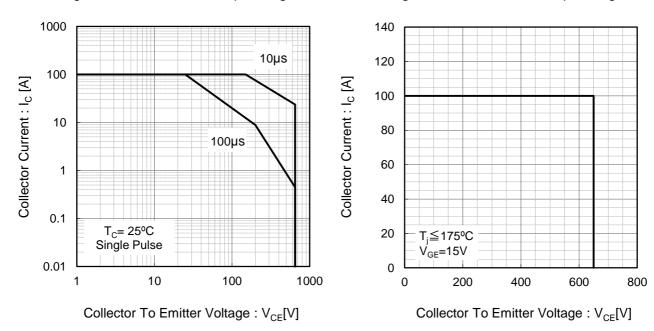


Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area



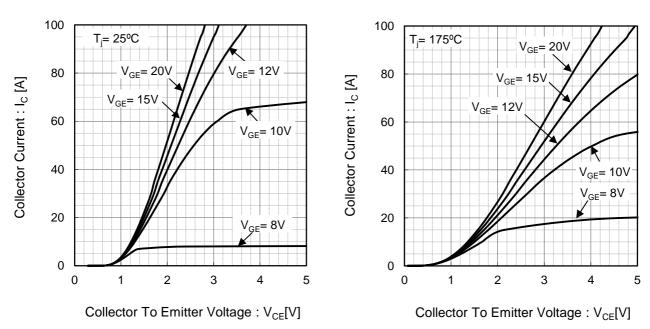
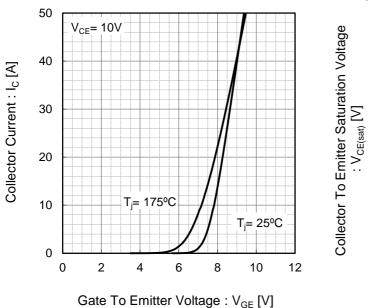


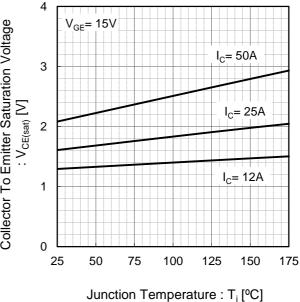
Fig.5 Typical Output Characteristics

Fig.6 Typical Output Characteristics

Fig.7 Typical Transfer Characteristics

Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature





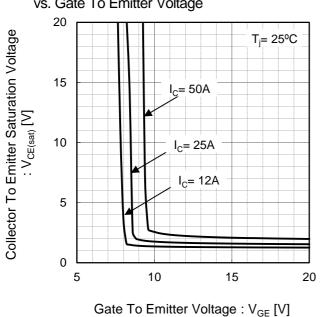


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

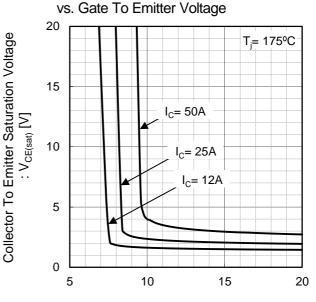


Fig.10 Typical Collector To Emitter Saturation Voltage

Gate To Emitter Voltage : V_{GE} [V]

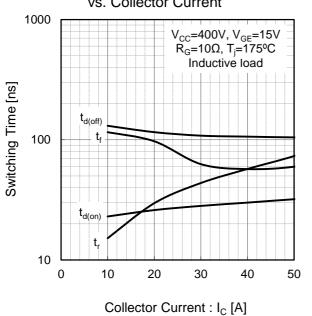
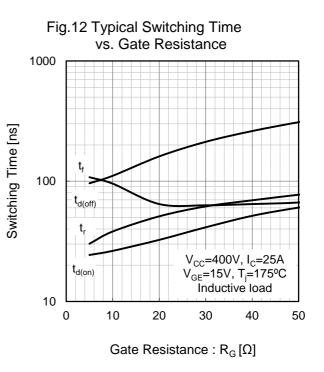
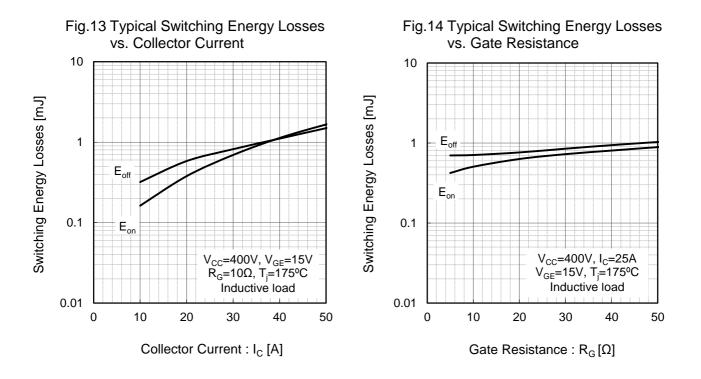
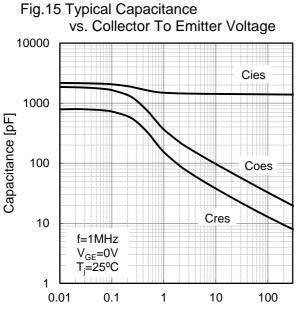


Fig.11 Typical Switching Time vs. Collector Current

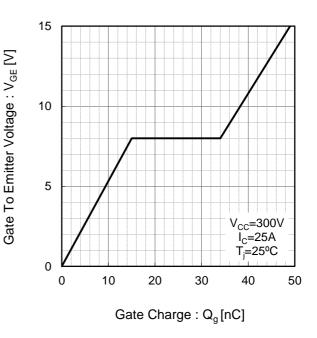






Collector To Emitter Voltage : $V_{CE}[V]$

Fig.16 Typical Gate Charge



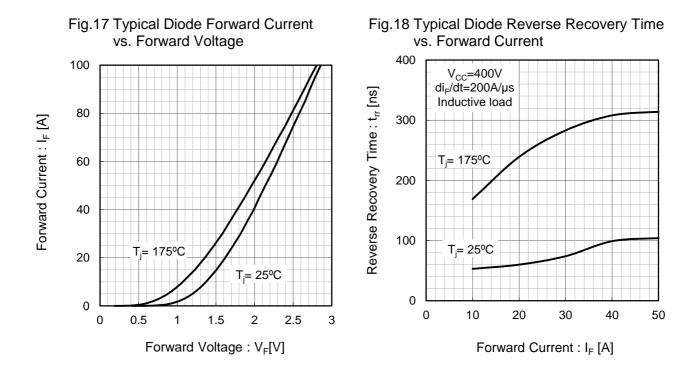


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

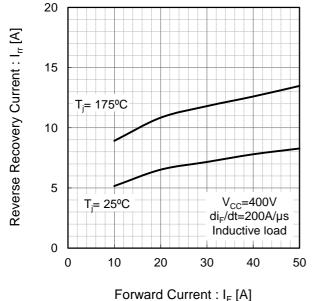
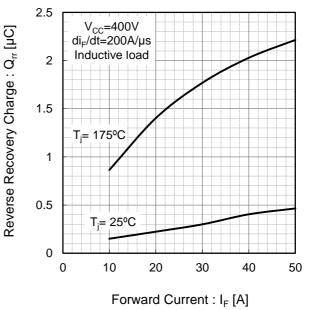


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



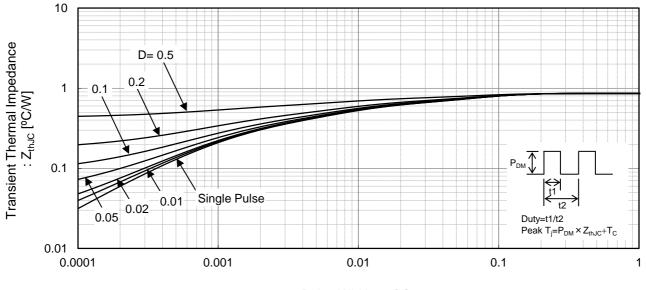


Fig.21 IGBT Transient Thermal Impedance



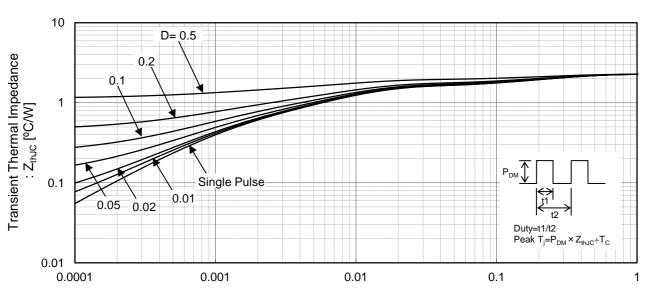


Fig.22 Diode Transient Thermal Impedance

Pulse Width : t1[s]

●Inductive Load Switching Circuit and Waveform

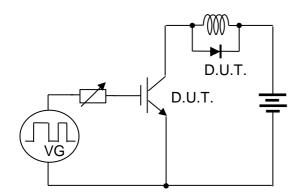


Fig.23 Inductive Load Circuit

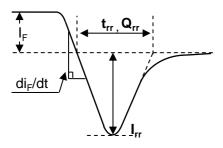


Fig.25 Diode Reverce Recovery Waveform

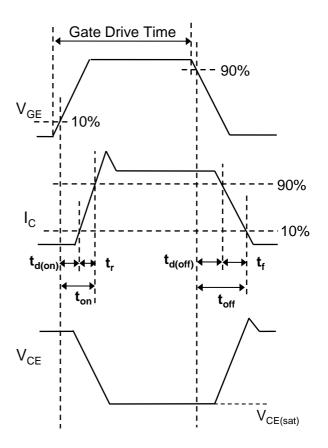


Fig.24 Inductive Load Waveform

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