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| Specification | |
|---|---|
| Part Number: | MCCOG21608A6W-FPTLWI |
| Version: | 1 |
| Date: | 20/03/2015 |
| **Revision** | |
| | |

# Contents

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# 1.General Specification

- Number of Characters: 16 characters x 2 Lines
- Module dimension: 85.0*30.0*6.2 mm
- View area: 76.0*18.0 mm
- Active area: 73.81*15.88 mm
- Dot size: 0.75*0.9 mm
- Dot pitch: 0.79*0.97 mm
- Character size: 3.91*7.69 mm
- Character pitch: 4.66*8.19 mm
- LCD type: FSTN Positive Transflective,
- Duty: 1/16 , 1/5 Bias
- View direction: 6 o’clock
- Backlight Type: LED, White
- IC: ST7032i

# Midas LCD Part Number System

| MC | COG | 132033 | A | * | 6 | W | * | * | - | S | N | T | L | W | * | * |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | - | 10 | 11 | 12 | 13 | 14 | 15 | 16 |

1 = **MC:** Midas Components

2 = **Blank:** COB (chip on board) **COG**: chip on glass

3 = **No of dots** (e.g. 240064 = 240 x 64 dots) (e.g. 21605 = 2 x 16 5mm C.H.)

4 = **Series**

5 = **Series Variant:** A to Z – see addendum

6 = **3:** 3 o'clock **6:** 6 o'clock **9**: 9 o'clock **12**: 12 o'clock

7 = **S**: Normal (0 to + 50 deg C) **W:** Wide temp. (-20 to + 70 deg C) **X:** Extended temp (-30 + 80 Deg C)

8 = **Character Set**

**Blank:** Standard (English/Japanese)
**C:** Chinese Simplified (Graphic Displays only)
**CB:** Chinese Big 5 (Graphic Displays only)
**H:** Hebrew
**K:** European (std) (English/German/French/Greek)
**L:** English/Japanese (special)
**M:** European (English/Scandinavian)
**R:** Cyrillic
**W:** European (English/Greek)
**U:** European (English/Scandinavian/Icelandic)
**J**: Asian/Arabic

9 = **Bezel Height** (where applicable / available)

| | Top of Bezel to Top of PCB | Common (via pins 1 and 2) | Array or Edge Lit |
|---|---|---|---|
| **Blank** | 9.5mm / not applicable | Common | Array |
| **2** | 8.9 mm | Common | Array |
| **3** | 7.8 mm | Separate | Array |
| **4** | 7.8 mm | Common | Array |
| **5** | 9.5 mm | Separate | Array |
| **6** | 7 mm | Common | Array |
| **7** | 7 mm | Separate | Array |
| **8** | 6.4 mm | Common | Edge |
| **9** | 6.4 mm | Separate | Edge |
| **A** | 5.5 mm | Common | Edge |
| **B** | 5.5 mm | Separate | Edge |
| **D** | 6.0mm | Separate | Edge |
| **E** | 5.0mm | Separate | Edge |
| **F** | 4.7mm | Common | Edge |
| **G** | 3.7mm | Separate | EL |

10 = **T:** TN **S**: STN **B:** STN Blue **G:** STN Grey **F:** FSTN **F2:** FFSTN **V:** VATN **Z:** Zero Power (Bi-Stable)

11 = **P:** Positive **N**: Negative

12 = **R:** Reflective **M**: Transmissive **T:** Transflective

13 = **Backlight: Blank:** Reflective **L:** LED

14 = **Backlight Colour: Y:** Yellow-Green **W:** White **B:** Blue **R:** Red **A:** Amber **O:** Orange **G:** Green **RGB:** R.G.B.

**If Z (Zero Power): WB:** White on blue **GB:** Green on black **YB:** Yellow on black **YPB:** Yellow on pink and/or blue

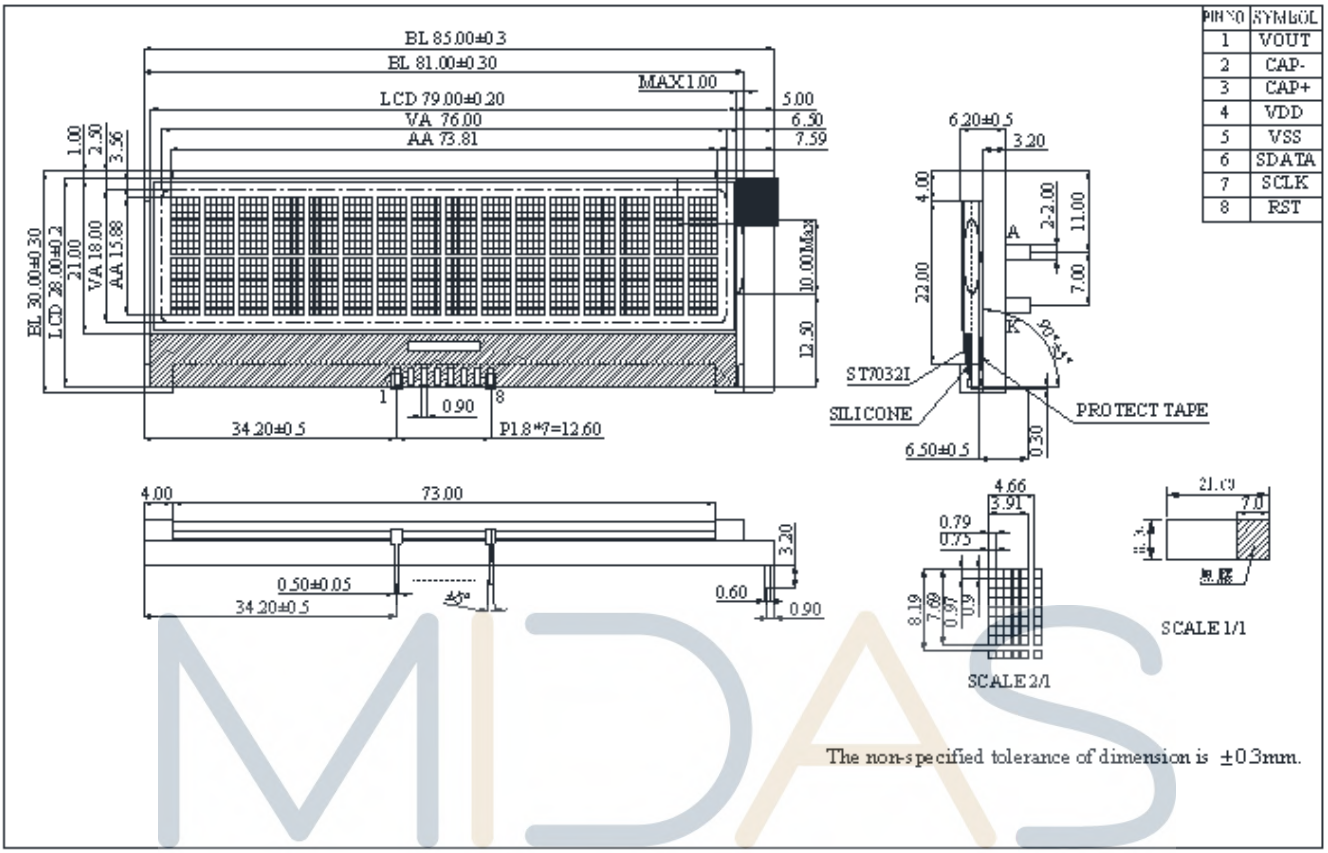
15 = **Driver Chip: Blank:** Standard **I:** I²C **T:** Toshiba T6963C **A:** Avant SAP1024B **R:** Raio RA8835

16 = **Voltage Variant**: e.g. **3** = 3v

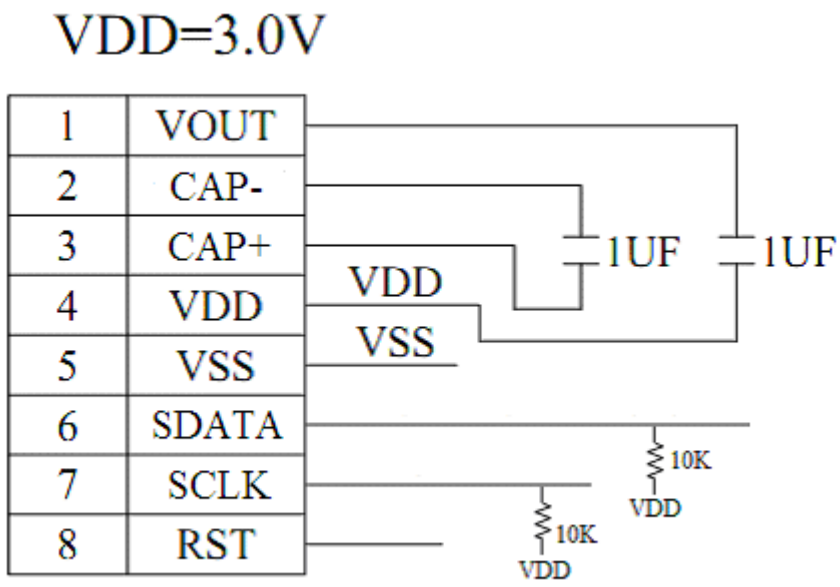
# 3.Interface Pin Function

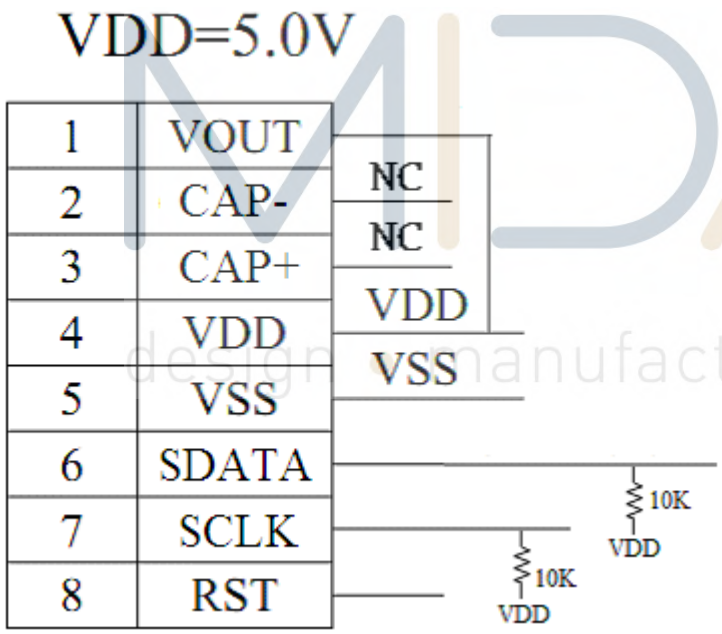
| Pin No. | Symbol | Level | Description |
|---|---|---|---|
| 1 | VOUT | | DC/DC voltage converter. Connect a capacitor between this terminal and VIN when the built-in booster is used. |
| 2 | CAP- | | For voltage booster circuit(VDD-VSS)<br>External capacitor about 0.1u~4.7uf |
| 3 | CAP+ | | |
| 4 | VDD | 3.0/5.0V | Power supply |
| 5 | VSS | | GND |
| 6 | SDATA | | (In I2C interface DB7 (SDATA) is input data.<br>SDATA and SCLK must connect to I2C bus (I2C bus is to connect a resister between SDATA/ SCLK and the power of I2C bus ). |
| 7 | SCLK | | (In I2C interface DB6 (SCLK) is clock input.<br>SDA and SCLK must connect to I2C bus (I2C bus is to connect a resister between SDATA/SCLK and the power of I2C bus ). |
| 8 | RST | | RESET (Low active) |

# 4.Contour Drawing



## Application schematic



**INITIALIZE: (3V)**

```
MOV     I2C_CONTROL,#00H ;WRITE COMMAND
MOV     I2C_DATA,#38H    ;Function Set
LCALL   WRITE_CODE
MOV     I2C_CONTROL,#00H ;WRITE COMMAND
MOV     I2C_DATA,#39H    ;Function Set
LCALL   WRITE_CODE
MOV     I2C_DATA,#14H    ;Internal OSC frequency
LCALL   WRITE_CODE
MOV     I2C_DATA,#74H    ;Contrast set
LCALL   WRITE_CODE
MOV     I2C_DATA,#54H        ;Power/ICON control/Contrast set
LCALL   WRITE_CODE
MOV     I2C_DATA,#6FH        ;Follower control
LCALL   WRITE_CODE
MOV     I2C_DATA,#0CH    ;Display ON/OFF
LCALL   WRITE_CODE
MOV     I2C_DATA,#01H    ;Clear Display
LCALL   WRITE_CODE
```

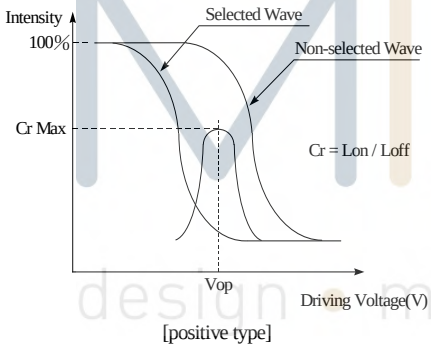
**INITIALIZE: (5V)**

```
MOV     I2C_CONTROL,#00H;WRITE COMMAND
MOV     I2C_DATA,#38H    ;Function Set
LCALL   WRITE_CODE
MOV     I2C_CONTROL,#00H;WRITE COMMAND
MOV     I2C_DATA,#39H    ;Function Set
LCALL   WRITE_CODE
MOV     I2C_DATA,#14H    ;Internal OSC frequency
LCALL   WRITE_CODE
MOV     I2C_DATA,#79H    ;Contrast set
LCALL   WRITE_CODE
MOV     I2C_DATA,#50H        ;Power/ICON control/Contrast set
LCALL   WRITE_CODE
MOV     I2C_DATA,#6CH        ;Follower control
LCALL   WRITE_CODE
MOV     I2C_DATA,#0CH    ;Display ON/OFF
LCALL   WRITE_CODE
MOV     I2C_DATA,#01H    ;Clear Display
LCALL   WRITE_CODE
```

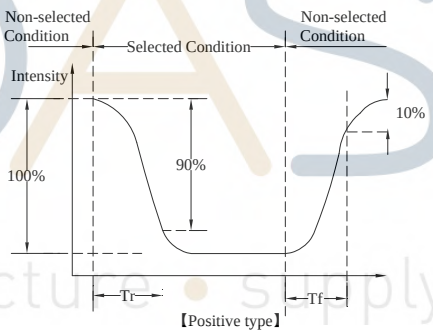
# 5.Optical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| View Angle | θ | CR≧2 | 0 | — | 30 | ψ= 180° |
| | θ | CR≧2 | 0 | — | 60 | ψ= 0° |
| | θ | CR≧2 | 0 | — | 45 | ψ= 90° |
| | θ | CR≧2 | 0 | — | 45 | ψ= 270° |
| Contrast Ratio | CR | — | — | 5 | — | — |
| Response Time | T rise | — | — | 150 | 200 | ms |
| | T fall | — | — | 150 | 200 | ms |

**Definition of Operation Voltage (Vop)**




**Definition of Response Time ( Tr , Tf )**

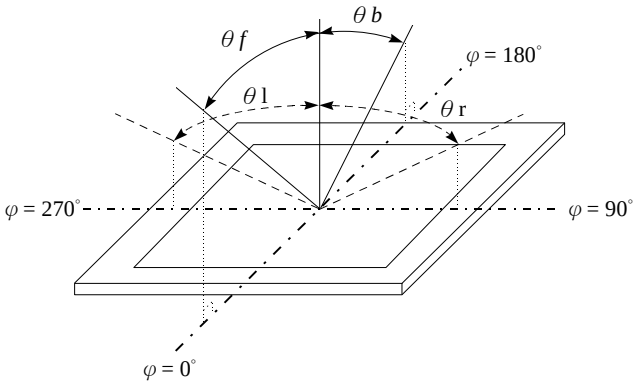



**Conditions :**

Operating Voltage : Vop  Viewing Angle(θ，φ) : 0°， 0°

Frame Frequency : 64 HZ Driving Waveform : 1/N duty , 1/a bias

**Definition of viewing angle(CR≧2)**



# 6.Absolute Maximum Ratings

| Item | Symbol | Min | Typ | Max | Unit |
|---|---|---|---|---|---|
| Operating Temperature | $T_{OP}$ | -20 | — | +70 | |
| Storage Temperature | $T_{ST}$ | -30 | — | +80 | |
| Input Voltage | $V_{IN}$ | -0.3 | — | $V_{DD}$+0.3 | V |
| Power Supply Voltage | $V_{DD}$-$V_{SS}$ | -0.3 | — | +6.0 | V |
| LCD Driver Voltage | $V_{LCD}$ | 2.7 | — | 7.0 | V |

# 7.Electrical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|
| Supply Voltage For Logic | $V_{DD}$-$V_{SS}$ | — | 3 | 3.3 | 5<br>(bon=1<br>max=3.5V) | V |
| Supply Voltage For LCD | $V_{LCD}$ | Ta=-20<br>Ta=25<br>Ta=70 | —<br>4.3<br>— | —<br>4.5<br>— | —<br>4.7<br>— | V<br>V<br>V |
| Input High Volt. | $V_{IH}$ | — | 0.7 $V_{DD}$ | — | $V_{DD}$ | V |
| Input Low Volt. | $V_{IL}$ | — | — | — | 0.2 $V_{DD}$ | V |
| Output High Volt. | $V_{OH}$ | — | 0.8 $V_{DD}$ | — | $V_{DD}$ | V |
| Output Low Volt. | $V_{OL}$ | — | — | — | 0.2$V_{DD}$ | V |
| Supply Current(No include LED Backlight) | $I_{DD}$ | — | — | — | 2.0 | mA |

Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance.

# 8.Backlight Information

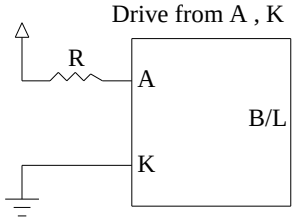
## Specification

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT | TEST CONDITION |
|---|---|---|---|---|---|---|
| Supply Current | ILED | — | 32 | 40 | mA | V=3.5V |
| Supply Voltage | V | 3.4 | 3.5 | 3.6 | V | |
| Reverse Voltage | VR | — | — | 5 | V | — |
| Color coordinate | X | 0.24 | 0.26 | 0.28 | | V=3.5V |
| | Y | 0.25 | 0.27 | 0.29 | | V=3.5V |
| Luminance (Without LCD) | IV | 536 | 670 | — | $CD/M^2$ | ILED=32mA |
| LED Life Time | — | — | 50000 | — | Hr. | $I_{LED} \leqq 32$ mA 25 ,50-60%RH Note1 |
| Color | White | | | | | |

**Note: The LED of B/L is drive by current only；driving voltage is only for reference To make driving current in safety area (waste current between minimum and maximum).**

**Note1 :50K hours is only an estimate for reference.**

LED B\L Drive Method
Drive from A , K



# 9.Reliability

**Content of Reliability Test (Wide temperature, -20 ~70 )**

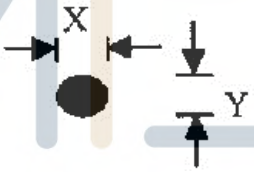
| Environmental Test | | | |
|---|---|---|---|
| **Test Item** | **Content of Test** | **Test Condition** | **Note** |
| High Temperature storage | Endurance test applying the high storage temperature for a long time. | 80<br>200hrs | 2 |
| Low Temperature storage | Endurance test applying the low storage temperature for a long time. | -30<br>200hrs | 1,2 |
| High Temperature Operation | Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time. | 70<br>200hrs | ——— |
| Low Temperature Operation | Endurance test applying the electric stress under low temperature for a long time. | -20<br>200hrs | 1 |
| High Temperature/ Humidity storage | The module should be allowed to stand at<br>60 ,90%RH max<br>For 96hrs under no-load condition excluding the polarizer,<br>Then taking it out and drying it at normal temperature. | 60 ,90%RH<br>96hrs | 1,2 |
| Thermal shock resistance | The sample should be allowed stand the following 10 cycles of operation<br>-20 25 70<br>30min 5min 30min | -20 /70<br>10 cycles | ——— |
| Vibration test | Endurance test applying the vibration during transportation and using. | Total fixed amplitude :<br>1.5mm<br>Vibration Frequency :<br>10~55Hz<br>One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes | 3 |
| Static electricity test | Endurance test applying the electric stress to the terminal. | VS=800V,RS=1.5kΩ<br>CS=100pF<br>1 time | ——— |

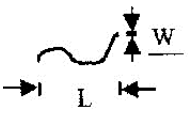
**Note1: No dew condensation to be observed.**

**Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.**

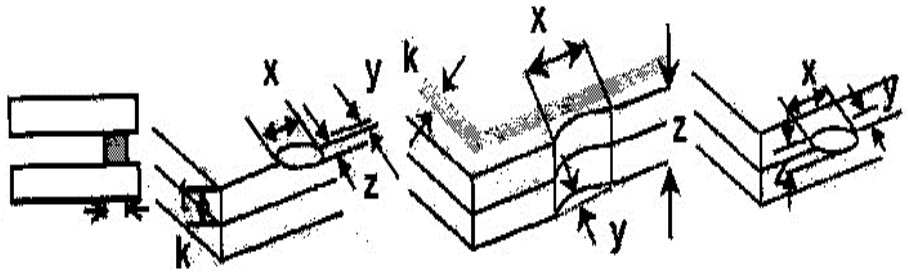
**Note3: The packing have to including into the vibration testing.**

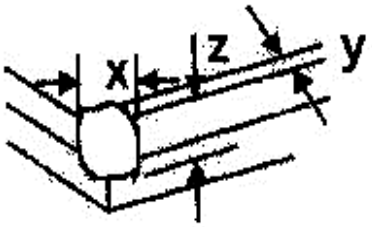
# 10.Inspection specification

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>01</td><td>Electrical Testing</td><td>1.1 Missing vertical, horizontal segment, segment contrast defect.<br>1.2 Missing character , dot or icon.<br>1.3 Display malfunction.<br>1.4 No function or no display.<br>1.5 Current consumption exceeds product specifications.<br>1.6 LCD viewing angle defect.<br>1.7 Mixed product types.<br>1.8 Contrast defect.</td><td>0.65</td></tr>
<tr><td>02</td><td>Black or white spots on LCD (display only)</td><td>2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present.<br>2.2 Densely spaced: No more than two spots or lines within 3mm</td><td>2.5</td></tr>
<tr><td rowspan="2">03</td><td rowspan="2">LCD black spots, white spots, contamination (non-display)</td><td>3.1 Round type : As following drawing<br>Φ=( x + y ) / 2<br> 

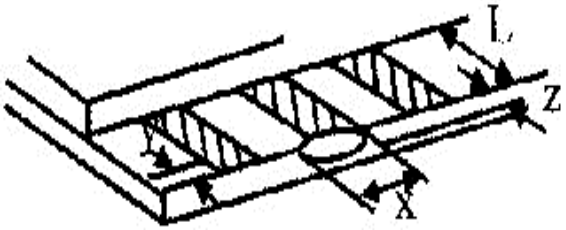
<table>
<tr><th>SIZE</th><th>Acceptable Q TY</th></tr>
<tr><td>Φ≦0.10</td><td>Accept no dense</td></tr>
<tr><td>0.10＜Φ≦0.20</td><td>2</td></tr>
<tr><td>0.20＜Φ≦0.25</td><td>1</td></tr>
<tr><td>0.25＜Φ</td><td>0</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>3.2 Line type : (As following drawing)<br> 

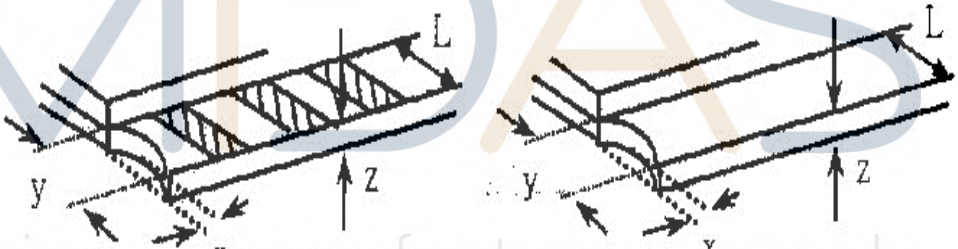
<table>
<tr><th>Length</th><th>Width</th><th>Acceptable Q TY</th></tr>
<tr><td>---</td><td>W≦0.02</td><td>Accept no dense</td></tr>
<tr><td>L≦3.0</td><td>0.02＜W≦0.03</td><td rowspan="2">2</td></tr>
<tr><td>L≦2.5</td><td>0.03＜W≦0.05</td></tr>
<tr><td>---</td><td>0.05＜W</td><td>As round type</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>04</td><td>Polarizer bubbles</td><td>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.
<table>
<tr><th>Size Φ</th><th>Acceptable Q TY</th></tr>
<tr><td>Φ≦0.20</td><td>Accept no dense</td></tr>
<tr><td>0.20＜Φ≦0.50</td><td>3</td></tr>
<tr><td>0.50＜Φ≦1.00</td><td>2</td></tr>
<tr><td>1.00＜Φ</td><td>0</td></tr>
<tr><td>Total Q TY</td><td>3</td></tr>
</table></td><td>2.5</td></tr>
</table>

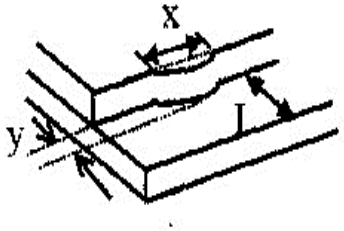
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>05</td><td>Scratches</td><td>Follow NO.3 LCD black spots, white spots, contamination</td><td></td></tr>
<tr><td>06</td><td>Chipped glass</td><td>
Symbols Define:<br>
x: Chip length y: Chip width z: Chip thickness<br>
k: Seal width t: Glass thickness a: LCD side length<br>
L: Electrode pad length:<br><br>
6.1 General glass chip :<br>
6.1.1 Chip on panel surface and crack between panels:<br>


<table>
<tr><th>z: Chip thickness</th><th>y: Chip width</th><th>x: Chip length</th></tr>
<tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr>
<tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr>
</table>
⊙If there are 2 or more chips, x is total length of each chip.<br><br>
6.1.2 Corner crack:<br>


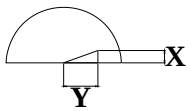
<table>
<tr><th>z: Chip thickness</th><th>y: Chip width</th><th>x: Chip length</th></tr>
<tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr>
<tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr>
</table>
⊙If there are 2 or more chips, x is the total length of each chip.
</td><td>2.5</td></tr>
</table>

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>06</td><td>Glass crack</td><td>
Symbols :<br>
x: Chip length y: Chip width z: Chip thickness<br>
k: Seal width t: Glass thickness a: LCD side length<br>
L: Electrode pad length<br>
6.2 Protrusion over terminal :<br>
6.2.1 Chip on electrode pad :<br>


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦0.5mm</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
6.2.2 Non-conductive portion:<br>


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦ L</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.<br>
⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.<br>
6.2.3 Substrate protuberance and internal crack.<br>


<table>
<tr><th>y: width</th><th>x: length</th></tr>
<tr><td>y≦1/3L</td><td>x ≦ a</td></tr>
</table>
</td><td>2.5</td></tr>
</table>

| NO | Item | Criterion | AQL |
|---|---|---|---|
| 07 | Cracked glass | The LCD with extensive crack is not acceptable. | 2.5 |
| 08 | Backlight elements | 8.1 Illumination source flickers when lit.<br>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.<br>8.3 Backlight doesn't light or color wrong. | 0.65<br>2.5<br>0.65 |
| 09 | Bezel | 9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.<br>9.2 Bezel must comply with job specifications. | 2.5<br>0.65 |
| 10 | PCB、COB | 10.1 COB seal may not have pinholes larger than 0.2mm or contamination.<br>10.2 COB seal surface may not have pinholes through to the IC.<br>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.<br>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.<br>10.5 No oxidation or contamination PCB terminals.<br>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.<br>10.7 The jumper on the PCB should conform to the product characteristic chart.<br>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.<br>10.9 The Scraping testing standard for Copper Coating of PCB<br>  <br><br>X * Y<=2mm2 | 2.5<br>2.5<br>0.65<br>2.5<br>2.5<br>0.65<br>0.65<br>2.5<br>2.5 |
| 11 | Soldering | 11.1 No un-melted solder paste may be present on the PCB.<br>11.2 No cold solder joints, missing solder connections, oxidation or icicle.<br>11.3 No residue or solder balls on PCB.<br>11.4 No short circuits in components on PCB. | 2.5<br>2.5<br>2.5<br>0.65 |

| NO | Item | Criterion | AQL |
|---|---|---|---|
| 12 | General appearance | 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP. | 2.5 |
| | | 12.2 No cracks on interface pin (OLB) of TCP. | 0.65 |
| | | 12.3 No contamination, solder residue or solder balls on product. | 2.5 |
| | | 12.4 The IC on the TCP may not be damaged, circuits. | 2.5 |
| | | 12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever. | 2.5 |
| | | 12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color. | 2.5 |
| | | 12.7 Sealant on top of the ITO circuit has not hardened. | 0.65 |
| | | 12.8 Pin type must match type in specification sheet. | 0.65 |
| | | 12.9 LCD pin loose or missing pins. | 0.65 |
| | | 12.10 Product packaging must the same as specified on packaging specification sheet. | 0.65 |
| | | 12.11 Product dimension and structure must conform to product specification sheet. | |
| | | 12.12 Visual defect outside of VA is not considered to be rejection. | |

# 11.Precautions in use of LCD Modules

(1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2)Don’t make extra holes on the printed circuit board, modify its shape or change the components of LCD module.

(3)Don’t disassemble the LCM.

(4)Don’t operate it above the absolute maximum rating.

(5)Don’t drop, bend or twist LCM.

(6)Soldering: only to the I/O terminals.

(7)Storage: please storage in anti-static electricity container and clean environment.

(8) Midas have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors,capacitors and other passive components will have different appearance and color caused by the different supplier.)

(9)Midas have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Midas have the right to modify the version.)

# 12.Material List of Components for RoHs

1. Midas hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

   Exhibit A：The Harmful Material List

| Material | (Cd) | (Pb) | (Hg) | (Cr6+) | PBBs | PBDEs |
|---|---|---|---|---|---|---|
| Limited Value | 100 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm | 1000 ppm |
| Above limited value is set up according to RoHS. | | | | | | |

2.Process for RoHS requirement：

(1) Use the Sn/Ag/Cu soldering surface；the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp.：

Reflow：250 ,30 seconds Max.；

Connector soldering wave or hand soldering：320 , 10 seconds max.

(3) Temp. curve of reflow, max. Temp.：235±5 ；

Recommended customer's soldering temp. of connector：280 , 3 seconds.

# 13.Recommendable Storage

1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module.