Datasheet

AEC-Q101 Qualified

Nch 100V 1A Power MOSFET

V_{DSS}	100V
$R_{DS(on)}(Max.)$	520m Ω
I _D	1.0A
P_D	1.0W

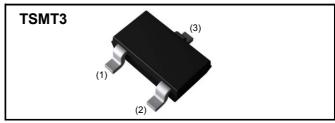
Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).
- 4) Pb-free lead plating; RoHS compliant

Application

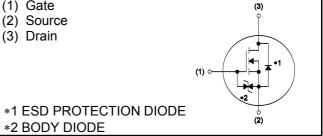
DC/DC converters

Outline



•Inner circuit

- (1) Gate



Packaging specifications

	Packaging	Taping
	Reel size (mm)	180
Typo	Tape width (mm)	8
Туре	Basic ordering unit (pcs)	3,000
	Taping code	TL
	Marking	ZJ

•Absolute maximum ratings($T_a = 25^{\circ}C$)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	100	V
Continuous drain current	I _D *1	±1	А
Pulsed drain current	I _{D,pulse} *2	±4	А
Gate - Source voltage	V_{GSS}	±20	V
Dower discination	P _D *3	1.0	W
Power dissipation	P _D *4	0.54	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	−55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farametei	Зуппоп	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *3	-	-	125	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	231	°C/W

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit	
r ai ai nietei	- /		Min.	Тур.	Max.	Ullit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	100	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	1	115	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μΑ	
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	1	1	±10	μΑ	
Gate threshold voltage	V _{GS (th)}	V_{DS} = 10V, I_D = 1mA	1.0	ı	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	ı	-3.7	-	mV/°C	
		V _{GS} =10V, I _D =1A	-	370	520		
Static drain - source	R _{DS(on)} *5	V _{GS} =4.5V, I _D =1A	-	400	560	m()	
on - state resistance		V _{GS} =4.0V, I _D =1A	1	410	580	mΩ	
		V _{GS} =10V, I _D =1A, T _j =125°C	ı	710	1000		
Gate input resistannce	R_{G}	f = 1MHz, open drain	-	10	-	Ω	
Transconductance	9 _{fs} *5	$V_{DS} = 10V, I_{D} = 1A$	1.0	3.1	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 $\mu s,~Duty~cycle \leq$ 1%

^{*3} Mounted on a ceramic board (30×30×0.8mm)

^{*4} Mounted on a FR4 (12×20×0.8mm)

^{*5} Pulsed

•Electrical characteristics($T_a = 25^{\circ}C$)

Parameter	Symbol	Conditions	Values			Unit
r ai ai ii etei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	140	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	20	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	12	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 50V$, $V_{GS} = 10V$	-	6	-	
Rise time	t _r *5	I _D = 0.5A	-	9	-	no
Turn - off delay time	t _{d(off)} 5	$R_L = 100\Omega$	-	22	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	15	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	${\sf Q_g}^{*5}$	$V_{DD} \simeq 50V$, $I_D = 1A$ $V_{GS} = 5V$	-	3.5	1	
Total gate charge	\mathcal{Q}_{g}	$V_{DD} \simeq 15V$, $I_D = 9A$ $V_{GS} = 10V$	1	7	14	nC
Gate - Source charge	Q _{gs} *5	$V_{DD} \simeq 50V$, $I_D = 1A$ $V_{GS} = 5V$	1	0.9	-	
Gate - Drain charge	Q _{gd} *5	$V_{GS} = 5V$	-	0.8	-	

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions -		Values			Unit
r ai ai ii etei	Syllibol	Conditions	Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	1	-	0.8	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V$, $I_s = 1A$	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

120

100

80

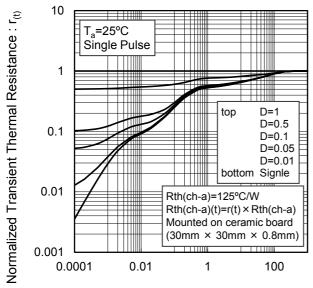
40

20

Junction Temperature : Tj [°C]

Fig.2 Maximum Safe Operating Area 10 P_W =100µs 1 Drain Current: -I_D [A] Operation in this area 0.1 is limited by $R_{DS}(on)$ ($V_{GS} = 10V$) P_W =10ms DC Operation 0.01 Single Pulse Mounted on a ceramic board. $(30mm \times 30mm \times 0.8mm)$ 0.001 0.1 10 100 1000

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width : P_W [s]

Fig.4 Single Pulse Maxmum Power dissipation

Drain - Source Voltage: V_{DS} [V]

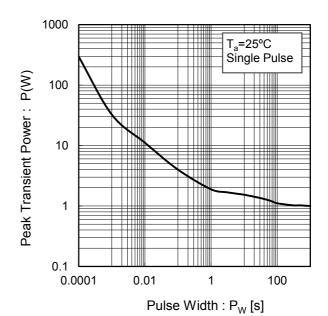
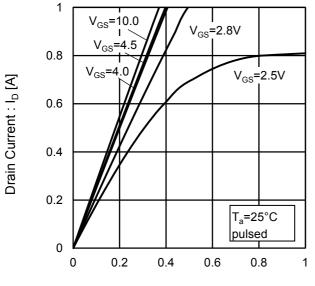
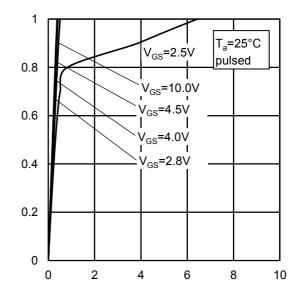


Fig.5 Typical Output Characteristics(I)



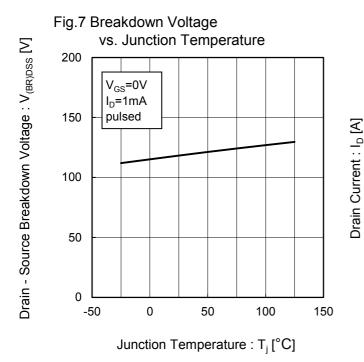
Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Drain Current : I_D [A]



10 | V_{DS}=10V | pulsed | 1 | T_a=125°C | T_a=75°C | T_a=25°C | T_a=-25°C | T_a

Fig.8 Typical Transfer Characteristics

Fig.9 Gate Threshold Voltage vs. Junction Temperature 3 V_{DS}=10V Gate Threshold Voltage: V_{GS(th)} [V] $I_D = 1 \text{mA}$ pulsed 2 1 0 0 -50 50 100 150 Junction Temperature : T_i [°C]

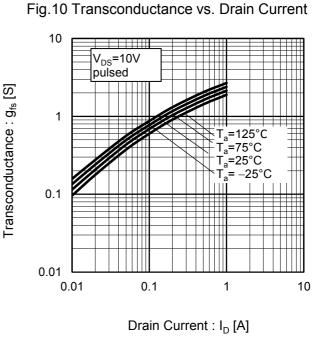


Fig.11 Drain CurrentDerating Curve

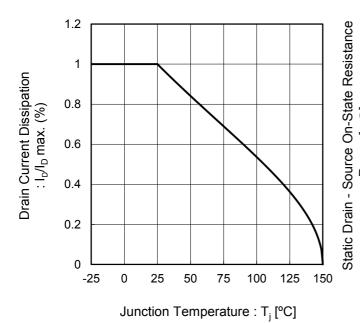
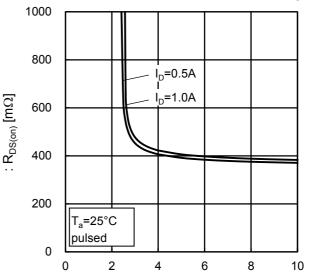


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : $V_{GS}[V]$

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

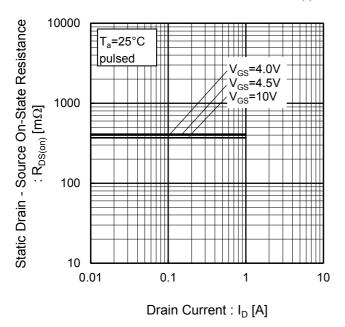
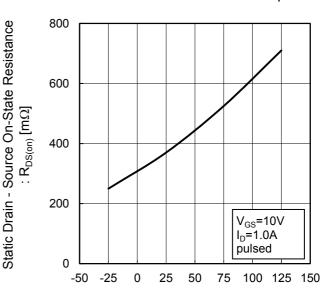


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_j [°C]

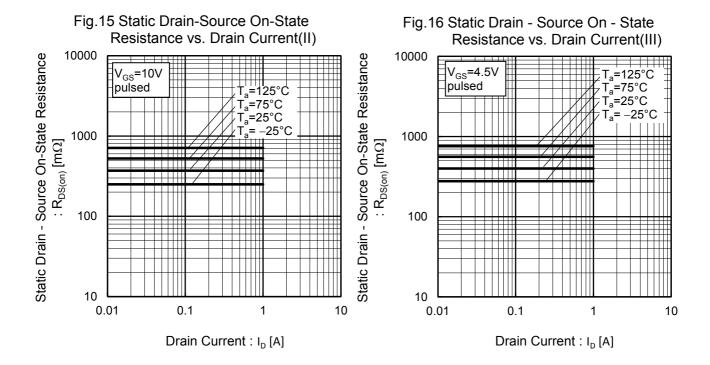
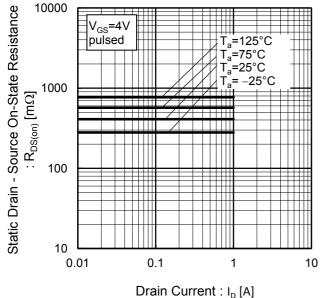
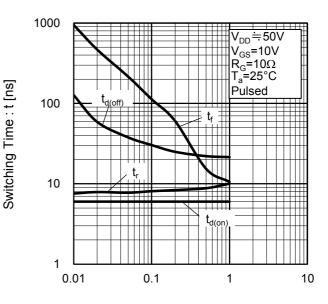


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)



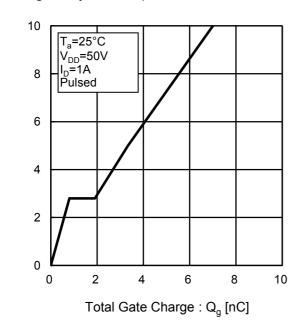
Drain - Source Voltage : V_{DS} [V]

Fig.19 Switching Characteristics



Drain Current : I_D [A]

Fig.20 Dynamic Input Characteristics



Source Current : I_S [A]

Fig.21 Source Current
vs. Source Drain Voltage

10

V_{GS}=0V
pulsed

1 T_a=125°C
T_a=75°C
T_a=25°C
T_a=-25°C
0.1

0.0

0.0

1 0.0

1 1.5
2.0

Source-Drain Voltage : V_{SD} [V]

Gate - Source Voltage : V_{GS} [V]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

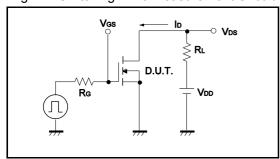


Fig.2-1 Gate Charge Measurement Circuit

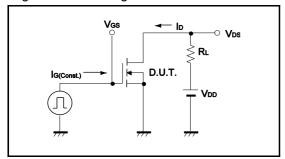


Fig.1-2 Switching Waveforms

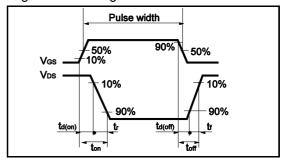
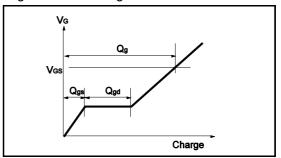
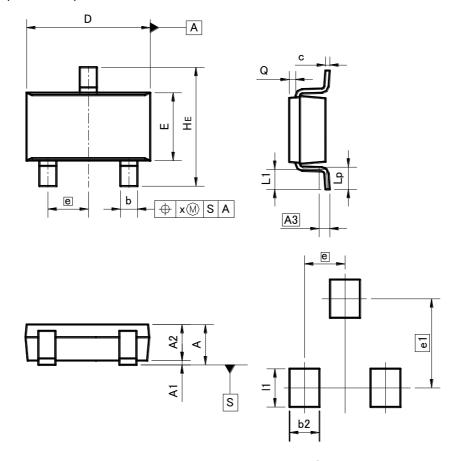


Fig.2-2 Gate Charge Waveform



●Dimensions (Unit: mm)

TSMT3



Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	ı	1.00	ı	0.039
A1	0.00	0.10	0	0.004
A2	0.75	0.95	0.03	0.037
A3	0.3	25	0.0	01
b	0.35	0.50	0.014	0.02
С	0.10	0.26	0.004	0.01
D	2.80	3.00	0.11	0.118
E	1.50	1.80	0.059	0.071
е	0.9	95	0.0	04
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.01
х		0.20		0.008

DIM	MILIM	ETERS	INC	HES
DIM	MIN MAX		MIN	MAX
e1	2.10		0.08	
b2		0.70	ı	0.028
11	_	0.90	_	0.035

Dimension in mm/inches

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Ì	JÁPAN	USA	EU	CHINA
Γ	CLASSⅢ	CL ACCIII	CLASS II b	CI VCCIII
Γ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [d] the Products are exposed to high Electrostatic
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