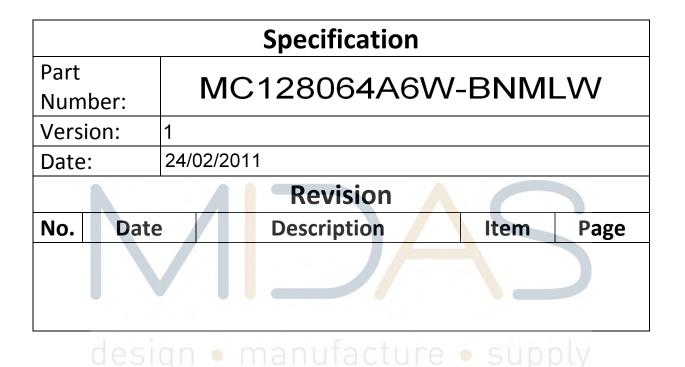


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# Midas LCD Part Number System

MC	COG	132033	Α	*	6	w	*	*	-	S	Ν	т	L	w	*	*
1	2	3	4	5	6	7	8	9	-	10	11	12	13	14	15	16
1	=	MC: Midas	Сотро	onents												
2	=	Blank: COE	B (chip	on boa	rd) CO	G: chip	on glas	s								
3	=	No of dots		(e.g. 2	40064	= 240 x	x 64 dot	s)	(e	.g. 216	05 = 2	x 16 5m	m C.H.	)		
4	=	Series														
5	=	Series Varia	nt:	A to Z	Z – see	addend	um									
6	=	<b>3:</b> 3 o'clock		<b>6:</b> 6 o'	clock	Ģ	<b>)</b> : 9 o'cl	ock	12	<b>2</b> : 12 o'	clock					
7	=	S: Normal (	0 to +	50 deg	C) <b>W</b> :	Wide t	emp. (-	20 to +	70 de	gC)X:	Exten	ded ten	np (-30 -	⊦ 80 De	gC)	
8	=	Character S	et													
		Blank: Stan C: Chinese S CB: Chinese H: Hebrew K: Europea L: English/ M: Europea R: Cyrillic W: Europea U: Europea	Simplif Big 5 n (std) Japane n (En an (En	fied (Gr (Graph ) (Englisese (spec glish/Sc glish/Gr	aphic ] iic Disj sh/Ger cial) andina reek)	Display plays or man/Fr wian)	ıly) ench/G									
9	=	Bezel Heigh	t (whe	ere appl	icable .	/availał	ole)									
		Blank         9.           2         8.           3         7.           4         7.           5         9.           6         7           7         7           8         6.           9         6.           A         5.           B         5.           D         6.           F         4.           G         3.			o Top	Com		5+ 16- non ate non ate non ate non ate ate ate ate ate ate ate ate ate		Array Edge I Array Array Array Array Array Array Array Edge Edge Edge Edge Edge Edge Edge Edge	<b>Ait</b>	•	54			
10	=	<b>T:</b> TN <b>S</b> : S <sup>*</sup>	ΓN B:	STN B	lue <b>G</b> :	STN G	rey F:	FSTN	F2: F	FSTN	V: VA	(Vertica	ally Alig	gned)		
11	=	P: Positive	N: Ne	gative												
12	=	R: Reflectiv	ие <b>М</b> :	Transm	issive	<b>T:</b> Trar	nsflectiv	ve								
13	=	Backlight:	Blank	Reflec	tive L	: LED										
14	=	Backlight C	olour:	Y: Ye	llow-G	reen W	White	e <b>B:</b> Bl	ue R:	Red A	: Ambe	er <b>0:</b> 01	ange G	Green	RGB: 1	R.G.B.
15	=	Driver Chip				dard l	[: I <sup>2</sup> C \$	S: SPI	T: Tos	hiba To	6963C	A: Ava	ant SAF	P1024B	<b>R:</b> R	aio RA6963
16	=	Voltage Var	r <b>iant</b> : e	e.g. <b>3</b> =	3v					D/D:		D			. ~	

#### BOOKBINDING AREA

DOC.

DATASHEET STATEMENT

- 1. The following icons are absolutely designed by Midas independently in 2007-SEP. They are not in common use in the LCD industry yet but just used for marking out Midasproducts' characteristics quickly and simply without any special meaning. Midas reserves the composing right and copyright. No one else is allowed to adopt these icons without Midas approval.
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- 4. As the difference in test standard and test conditions, also Midas insufficient familiarity with the actual LCD using environment, all the referred information in this DATASHEET (including the icons) only have two functions:
  4.1: providing quick reference when you are judging whether or not the product meets your requirements.
  4.2: listing out definitely the tolerance.

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- 5. The sequence of the icons is random and doesn't indicate the importance grade.
- 6. Icons explanation

Midas 2006 version logo.Midas is an integrated manufacturer of flat panel display (FPD). Midas supplies TN, HTN, STN, FSTN monochrome LCD panel; COB, COG, TAB LCD module; and all kinds of LED backlight.



#### FAST RESPONSE TIME

This icon on the cover indicates the product is with high response speed; Otherwise not.

C			
	Н	С	
Ľ			

#### HIGH CONTRAST

This icon on the cover indicates the product is with high contrast; Otherwise not.



#### WIDE VIEWING SCOPE

This icon on the cover indicates the product is with wide viewing scope; Otherwise not.



## RoHS COMPLIANCE

This icon on the cover indicates the product meets ROHS requirements; Otherwise not.



**3TIMEs 100% QC EXAMINATION** This icon on the cover indicates the product

has passed Midas thrice 100% QC. Otherwise not.



#### VIcm = 3.0V

This icon on the cover indicates the product can work at 3.0V exactly; otherwise not.



#### PROTECTION CIRCUIT

This icon on the cover indicates the product is with protection circuit; Otherwise not.



#### LONG LIFE VERSION

This icon on the cover indicates the product is long life version (over 9K hours guaranteed); Otherwise not.



#### Anti UV VERSION

This icon on the cover indicates the product is against UV line. Otherwise not.



#### OPERATION TEMPERATURE RANGE

This icon on the cover indicates the operating temperature range (X-Y).



#### TWICE SELECTION OF LED MATERIALS

This icon on the cover indicates the LED had passed Midas twice strict selection which promises the product's identical color and brightness; Otherwise not.



N SERIES TECHNOLOGY (2008 developed) New structure, new craft, new technology and new materials inside both LCD module and LCD panel to improve the "RainBow"

BOOKBINDING AREA			
STANDARD DOC.	CONTENTS	PAGE	<b>2</b> /1 <b>6</b>

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2.	MECHANICAL SPECIFICATIONS Page	3
3.	ABSOLUTE MAXIMUM RATINGS Page	3
4.	ELECTRONICAL CHARACTERISTIC Page	3
5.	OPTICAL CHARACTERISTICSPage	4
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BOOKE	BINDING AREA			
	PRODUCT SPEC.	MODE NO. MC128064A6W-BNMLW	PAGE	3/16

#### 1. GENERAL SPECIFICATIONS

ITEM	NOMINAL DIMENSIONS / AVAILABLE OPTIONS
DISPLAY FORMAT	128 X 64 DOT MATRIX
LCD PANEL OPTIONS	STN (Blue color)
POLARIZER OPTIONS	Negative, Transmissive
BACKLIGHT OPTIONS	Edge type LED backlight (White color)
VIEWING ANGLE OPTIONS	6:00 ( Bottom)
TEMPERATURE RANGE OPTIONS	Wide temperature range (-20°C ~ 70°C)
CONTROLLER IC	NT7107C+NT7108C
NEGATIVE IC	Built in
DISPLAY DUTY	1/64
DRIVING BIAS	1/9

#### 2. MECHANICAL SPECIFICATIONS

OVERALL SIZE	LED backlight	versio	on: 93.0 x 70.0 x	max 13.0	mm
VIEWING AREA	72.0W x <mark>4</mark> 0.0H	mm	HOLE-HOLE	88.0W x 64.0H	mm
DOT SIZE	0.48W x <mark>0</mark> .48H	mm	DOT PITCH	0.04W x 0.04H	mm
WEIGHT (EL BKL)	60.0	g	WEIGHT (LED BKL)	83.0	g

# 3. ABSOLUTE MAXIMUM RATINGS

		CONDITION	NALNI -		
ITEM	SYMBOL	CONDITION	MIN	MAX	UNIT
POWER SUPPLY (LOGIC)	Vdd	25°C	-0.3	7.0	V
POWER SUPPLY (LCD)	V0	25°C	Vdd -19.0	Vdd +0.3	V
INPUT VOLTAGE	Vin C	C 25°C E	-0.3	Vdd +0.3	V
OPERATING TEMPERATURE	Vopr		-20	70	°C
STORAGE TEMPERATURE	Vstg		-30	80	°C

## 4. ELECTRONICAL CHARACTERISTIC\*

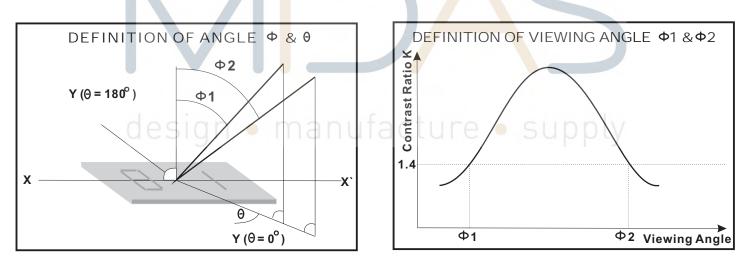
ITEM	SYMBOL	CONDITION	ST	UNIT		
I I E IVI	STINBUL CONDITION		MIN	ΤΥΡ	MAX	UNTI
Input voltage	Vdd	+5V	2.7	5.0	5.5	V
Supply current	ldd	Vdd=5V		2.1		mA
		-20 <sup>°</sup> C	8.40		9.00	
Recommended LCD driving		0°C	8.20		8.80	
voltage for normal temp.	Vdd - V0	25 <sup>°</sup> C	8.10	8.40	8.70	v
Version module		50 <sup>°</sup> C	8.00		8.60	
		70 <sup>°</sup> C	7.85		8.45	
LED forward voltage	Vf	25 <sup>°</sup> C	2.9		3.4	V
LED forward current	lf	25 <sup>°</sup> C		30	40	mA
LED reverse Current	Ir	25°C		20		μA
LED color range	X coordinate	25 <sup>°</sup> C If = 30mA	0.25		0.28	
	Y coordinate	25 <sup>°</sup> C If = 30mA	0.26		0.29	
LED illuminance (Without LCD)	Lv	25 <sup>°</sup> C If = 30mA	120		190	cd/m²
LED life time		25 <sup>°</sup> C If = 30mA	50K**			Hours

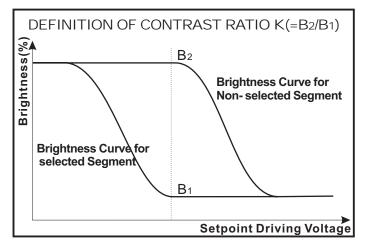
воокв	INDING AREA			
	PRODUCT SPEC.	MODE NO. MC128064A6W-BNMLW	PAGE	<b>4</b> /1 <b>6</b>

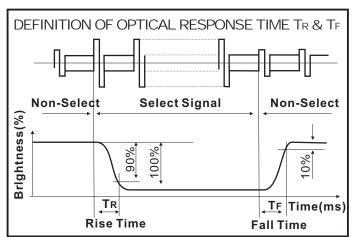
#### 5. OPTICAL CHARACTERISTIC

FOR TN TYPE LCD MODULE (TA=25 °C, Vdd=5.0V ± 0.25V)										
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT				
	Ф <b>2</b> -Ф 1	K=4	30			dog				
VIEWING ANGLE	Θ	<b>N=4</b>	25			deg				
CONTRAST RATIO	К			2						
RESPONSE TIME(RISE)	TR			120	150	ms				
RESPONSE TIME(FALL)	TF			120	150	ms				

FOR STN TYPE LCD MODULE (TA=25 °C, Vdd=5.0V ±0.25V)										
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT				
	Φ2-Φ 1	K=4	40			deg				
VIEWING ANGLE	Θ	<b>N</b> =4	60			ueg				
CONTRAST RATIO	К			6						
RESPONSE TIME(RISE)	TR			150	250	ms				
RESPONSE TIME(FALL)	TF			150	250	ms				







BOOKBINI	DING AREA			
	PRODUCT	MODE NO.	PAGE	<b>E</b> /1 <b>G</b>
	SPEC.	MC128064A6W-BNMLW	FAGE	<b>5</b> /1 <b>6</b>

# 6. DC CHARACTERISTIC

(Unless otherwise stated, VDD= +5V ± 10%, VSS=0V, Ta=25°C)

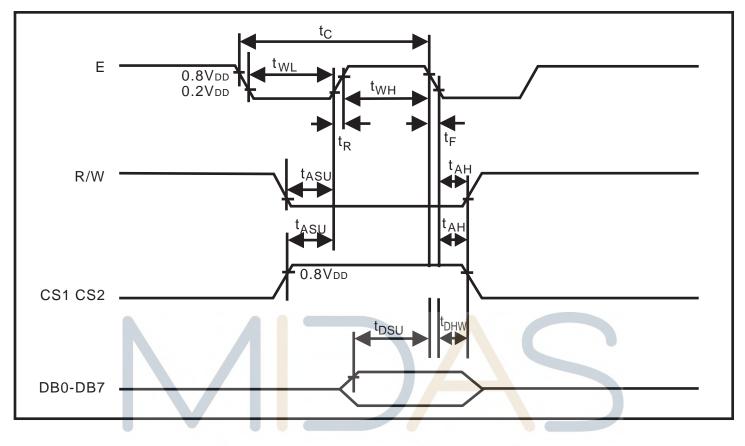
Symbol	Condition	Min.	Тур.	Max.	Unit
VIH1		0.7VDD		VDD	V
High Level Input Voltage VIII2		0.7VDD		VDD	V
VIL1		0		0.3VDD	V
VIL2		0		0.8	V
VOH	IOH = - 200µA	2.4			V
VOL	IOL = 1.6 mA			0.4	V
ILKG	VIN = VDD to VSS	-1.0		1.0	μ <b>Α</b>
ITSL	VIN = VDD to VSS	-5.0		5.0	μ <b>Α</b>
IDD1	During Display	(	-	100	μ <b>Α</b>
IDD2	During Access, Access Cycle = 1MHz			500	μ <b>Α</b>
	VIH1 VIH2 VIL1 VIL2 VIL2 VOH VOL ILKG ITSL IDD1	VIH1            VIH2            VIL1            VIL2            VIL2            VIL2            VOH         IOH = - 200 µA           VOL         IOL = 1.6 mA           ILKG         VIN = VDD to VSS           ITSL         VIN = VDD to VSS           IDD1         During Display           During Access,         During Access,	VIH1          0.7VDD           VIH2          0.7VDD           VIL1          0           VIL2          0           VIL2          0           VOH         IOH = - 200 µA         2.4           VOL         IOL = 1.6 mA            ILKG         VIN = VDD to VSS         -1.0           ITSL         VIN = VDD to VSS         -5.0           IDD1         During Display	VIH1          0.7VDD            VIH2          0.7VDD            VIL1          0            VIL2          0            VIL2          0            VIL2          0            VOH         IOH = - 200 µA         2.4            VOL         IOL = 1.6 mA             VOL         IOL = 1.6 mA             ILKG         VIN = VDD to VSS         -1.0            IDD1         During Display             During Access,	VIH1         ····         0.7VDD         ····         VDD           VIH2         ····         0.7VDD         ····         VDD           VIL1         ····         0         ····         VDD           VIL2         ····         0         ····         0.3VDD           VIL2         ····         0         ····         0.3VDD           VIL2         ····         0         ····         0.8           VOH         IOH = - 200μA         2.4         ····         ····           VOL         IOL = 1.6 mA         ····         0.4           ILKG         VIN = VDD to VSS         ·1.0         ····         1.0           ITSL         VIN = VDD to VSS         ·5.0         ····         5.0           IDD1         During Display         ····         ····         100

## 7. AC CHARACTERISTIC

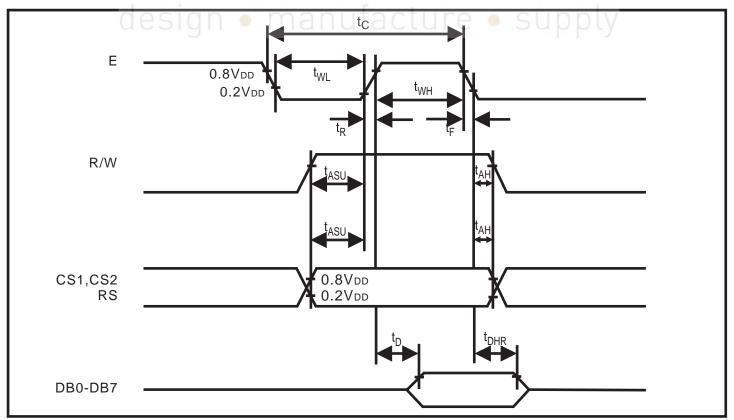
Characteristic	Symbol	an Minac	Тур	SUMax	Unit
E Cycle	tc	1000			ns
E High Level Width	t wn	450	_	—	ns
E Low Level Width	t w∟	450		—	ns
E Rise Time	t <sub>R</sub>	_	_	25	ns
E Fall Time	tf	_	_	25	ns
Address Setup Time	t asu	140	—	—	ns
Address Hold Time	t ah	10	_	—	ns
Data Setup Time	t dsu	200	_	—	ns
Data Delay Time	tD	_	—	320	ns
Data Hold Time (Write)	t dhw	10	_	—	ns
Data Hold Time (Read)	t <sub>DHR</sub>	20			ns

BOOKBINI	DING AREA			
	PRODUCT	MODE NO.	DACE	6/16
	SPEC.	MC128064A6W-BNMLW	PAGE	0/10

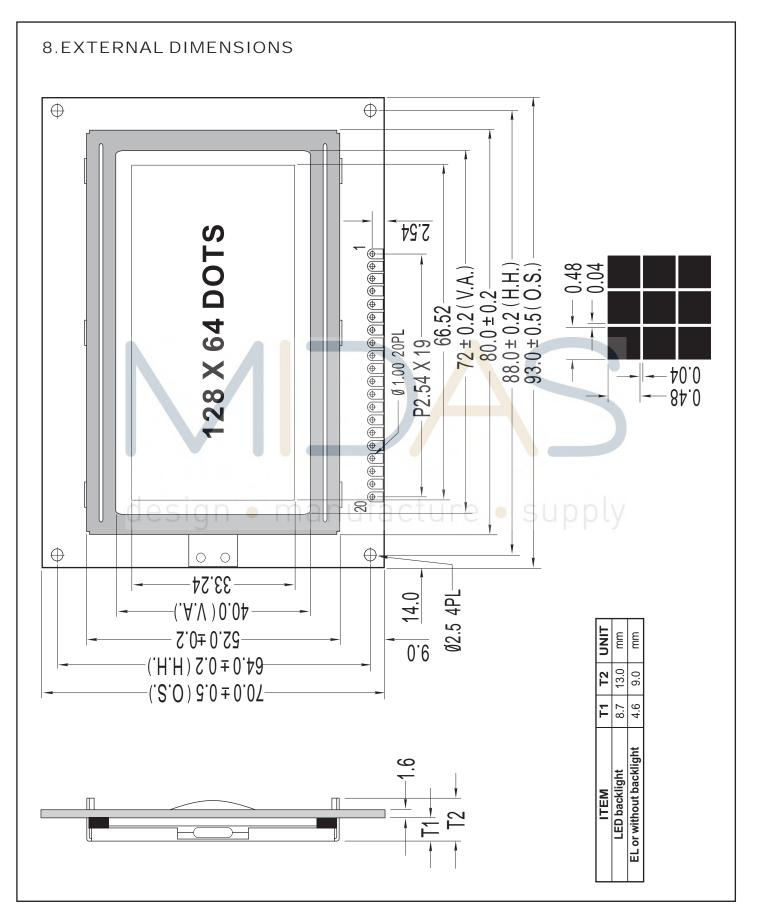
### 7.1 WRITE MODE TIMING DIAGRAM



## 7.2 READ MODE TIMING DIAGRAM



PRODUCT MODE NO. SPEC. MC128064A6W-BNMLW PAGE 7/16	BOOKBINI	DING AREA			
		PRODUCT	MODE NO.	DACE	7/16
		SPEC.	MC128064A6W-BNMLW	PAGE	1/10

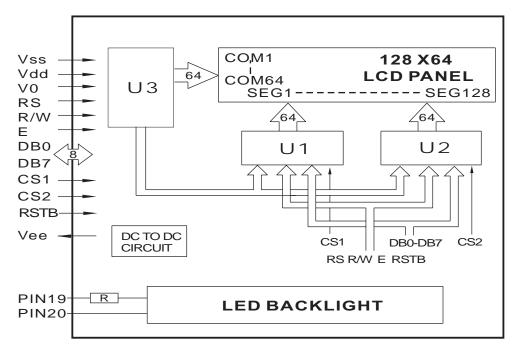


BOOKBINDING AREA			
PRODUCT SPEC. MC	MODE NO. C128064A6W-BNMLW	PAGE	<b>8</b> /1 <b>6</b>

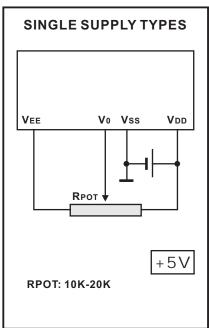
#### 9. PIN ASSIGNMENT

PIN NO.	SYMBOL	FUN	ICTION	REMARK		
1	Vss		0V			
2	Vdd	Power Supply	+5V			
3	V0		Contrast Adjust			
4	RS	Rigister	Select signal			
5	R/W	Read / Write				
6	E	Chip Enable signal				
7	DB0	Data Bit 0				
8	DB1	Data Bit 1				
9	DB2	Data Bit 2				
10	DB3	Data Bit 3				
11	DB4	Data Bit 4				
12	DB5	Data Bit 5		Data Bit 5		
13	DB6	Data Bit 6				
14	DB7	Data Bit 7				
15	CS1	When CS1=H,	CS2=L <mark>, s</mark> ele <mark>ct</mark> U1			
16	CS2	When CS1=L,CS2=H, select U2				
17	RSTB	Reset signal				
18	Vee	Negative v	oltage output			
19	LED+	Anode o	of LED Unit	5.0V		
20	LED-	Cathode	of LED Unit	0V		

10. BLOCK DIAGRAM



## 11. POWER SUPPLY



BOOKBIN	DING AREA			
	PRODUCT	MODE NO.	PAGE	<b>9</b> /16
	SPEC.	MC128064A6W-BNMLW	TAGE	5/10

### 12. FUNCTIONAL DESCRIPTION

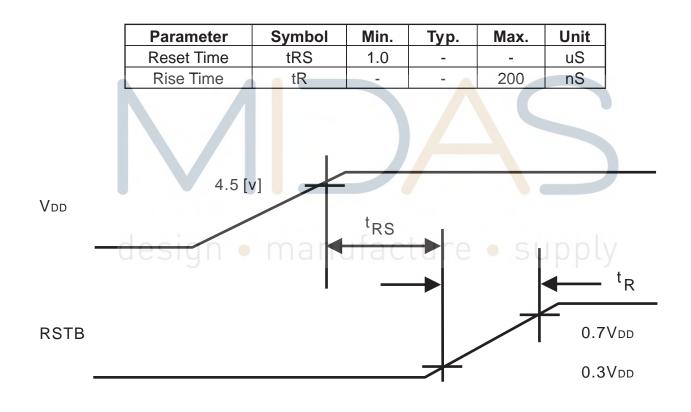
## **12.1 RESET**

The system can be initialized by setting the RSTB to LOW when turning the power ON or by instruction from the MPU. When the RSTB is set to LOW, the following condition occurs:

- 1. The Display is turned OFF.
- 2. The Display Start Line register is set to 0 (Z-Address 0).

No instructions except the status read can be executed when the RSTB is LOW. This means that in order to execute other instructions, the RSTB must be cleared by setting DB4 to 0 and the DB7 set to 0 by status read instruction.

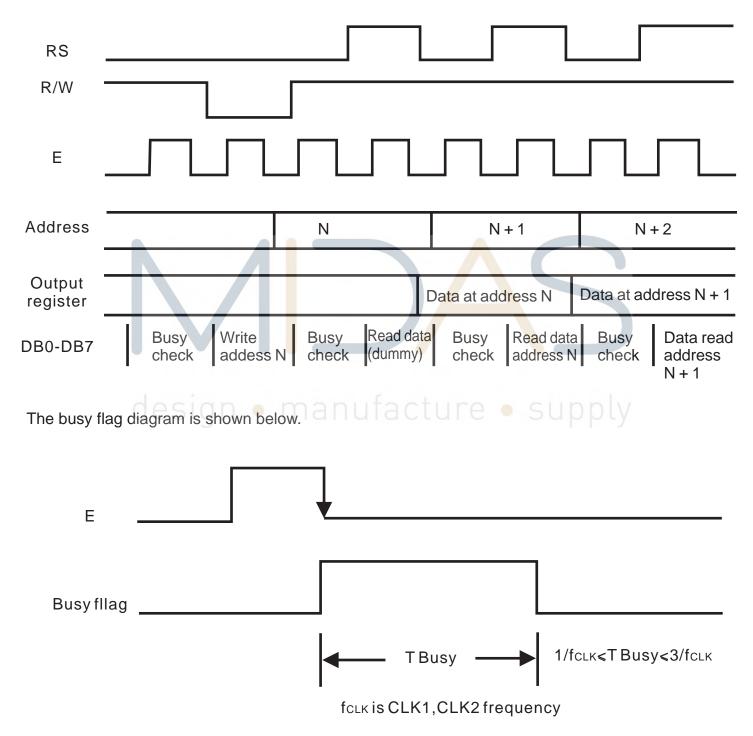
The table below shows the power supply initial conditions.



BOOKBIN	DING AREA			
	PRODUCT	MODE NO.	PAGE	10/16
	SPEC.	MC128064A6W-BNMLW	PAGE	

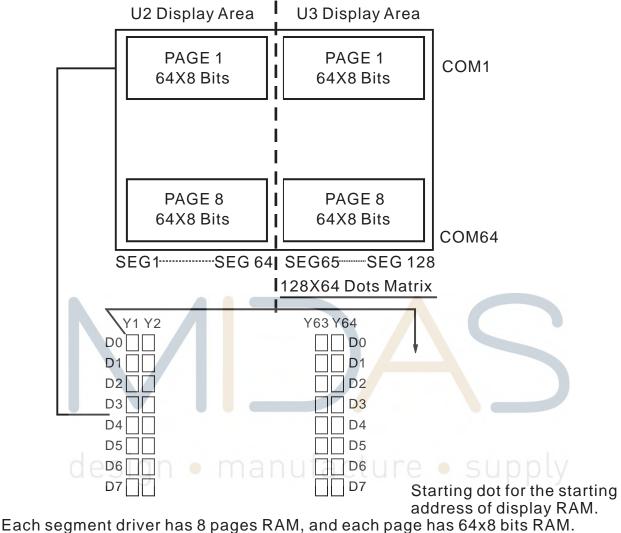
# 12.2 BUSY FLAG

The busy flag (DB7) is used to determine whether Nt7108 is operating or not. When the busy flag is HIGH, internal operation is taking place. When the busy flag is LOW, Nt7108 can accept data or instructions. The busy check diagram is shown below.



BOOKBINDING AREA			
PRODUCT SPEC.	MODE NO. MC128064A6W-BNMLW	PAGE	1 <b>1</b> /1 <b>6</b>

## 12.3 RELATION BETWEEN DISPLAY PATTERN AND DRIVERS



Each segment driver has 8 pages RAM, and each page has 64x8 bits RA D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.

## **12.4 DISPLAY DATA RAM**

The Display Data RAM is used to store the display data for the liquid crystal display. Write data 1 is indicates an ON State of the LCDs dot matrix while the OFF State is written as 0. ADC Signal can control the Display Data RAM and the segment output. Please refer to the table below.

ADC *	Display Data
Н	Y-Address 0:S1 to Y-Address 63:S64

BOOKBIN	DING AREA			
	PRODUCT	MODE NO.	PAGE	19/16
	SPEC.	MC128064A6W-BNMLW	PAGE	

# 13. INSTRUCTION

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	L	L	L	L	Н	I	Н	Н	Н	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF H:ON
Set address (Y address)	L	L	L	Н		Υa	addres	s (0~6	63)		Sets the Y address in the Y address counter.
Set Page (X address)	L	L	н	L	н	Н	Н	Pa	ge (0-	-7)	Sets the X address at the X address register.
Display Start Line (Z address)	L	L	н	н		Disp	lay sta	rt line (	(0~63)		Indicates the display data RAM displayed at the top of the screen.
Status Read	6	5 ipj	B U S Y	n	O N O F F	R E S E T	cţu	Ire	L	suţp	BUSY L:Ready H:In operation ON/OFF L:Display ON H:Display OFF RESET L:Normal H:Reset
Write Display Data	Н	L				Write	e Data	1			Writes data (DB0:7) into display data RAM,After writing instruction,Y address is increased by 1 automatically.
Read Display Data	Н	Н				Rea	id Data	а			Reads data (DB0:7) from display data RAM to the data bus.

PRODUCT MODE NO.	
	2/16
SPEC. MC128064A6W-BNMLW PAGE 1	3/10

## 14. DESCRIPTION OF COMMAND

# **Display On/Off**

 RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0.

Though the data is not on the screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.

# Set Address(Y Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

# Set Page(X Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

# **Display Start Line(Z Address)**

	S R/W		-	-		-			-
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen.

When the display duty cycle is 1/64 or others(1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction , is displayed.

BOOKBINE				
	PRODUCT	MODE NO.	DACE	1 1/16
	SPEC.	MC128064A6W-BNMLW	PAGE	14/10

# Status Read

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	Db1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

## BUSY

When BUSY is 1,the Chip is executing internal operation and no instructions are accepted. When BUSY is 0,the Chip is ready to accept any instructions.

## • ON/OFF

When ON/OFFis 1,the display is off.When ON/OFFis 0,the display is on.

## RESET

When RESET is 1, the system is being initialized. In this condition, no instructions except status read can be accepted. When RESET is 0 initializing has finished and the system is in the usu

When RESET is 0, initializing has finished and the system is in the usual operation condition.

# Write Display Data

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0-D7) into the display data RAM.

After writing instruction, Y address is increased by 1 automatically.

# **Read Display Data**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

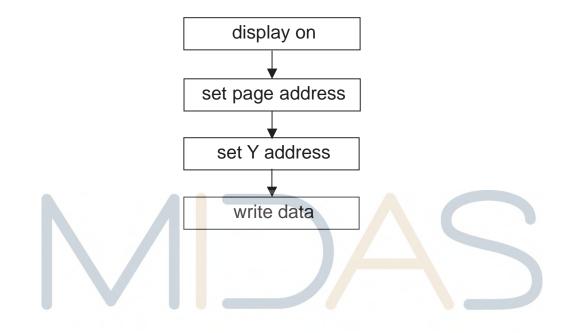
Reads data (D0-D7) from the display data RAM.

After reading instruction, Y address is increased by 1 automatically.

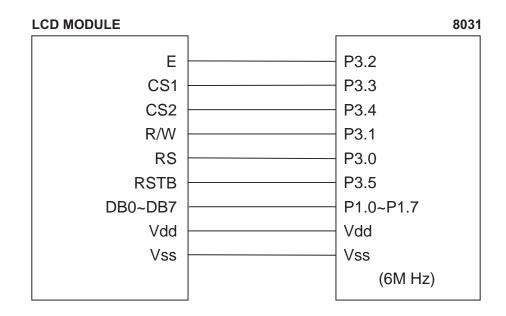
BOOKBINI	DING AREA			
	PRODUCT	MODE NO.	DACE	15/16
	SPEC.	MC128064A6W-BNMLW	PAGE	15/10

# 15. APPLICATION EXAMPLE

# **Application Flowchart**



# Application Circuit - manufacture - supply



BOOKBINE				
	PRODUCT	MODE NO.	DAGE	16/16
	SPEC.	MC128064A6W-BNMLW	- PAGE	

# 16. PACKING DETAIL

WITH LED BKL	WITHOUT LED BKL	NOTE
30 PCS/BOX	30 PCS/BOX	1. The weight is estimated for reference only.
8 BOXES/CARTON	8 BOXES/CARTON	2. Packing detail may be changed without notice.
240 PCS/CARTON	240 PCS/CARTON	
19.00 KGS/CTN(G.W.)	17.00 KGS/CTN(G.W.)	
0.07 M <sup>3</sup> /CARTON	0.07 M <sup>3</sup> /CARTON	

