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		Specification	
Part	M	C128064A6W	/-SPTLY
Number:			
Version:	1		
Date:	05/07/201	1	
		Revision	
No. D	ate	Description	Item Page
		<i>C</i>	

design • manufacture • supply

BOOKBINDING AREA

DOC.

DATASHEET STATEMENT

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 4.1: providing quick reference when you are judging whether or not the product meets your requirements.
 4.2: listing out definitely the tolerance.

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- 5. The sequence of the icons is random and doesn't indicate the importance grade.
- 6. Icons explanation

Midas 2006 version logo.Midas is an integrated manufacturer of flat panel display (FPD). Midas supplies TN, HTN, STN, FSTN monochrome LCD panel; COB, COG, TAB LCD module; and all kinds of LED backlight.



FAST RESPONSE TIME

This icon on the cover indicates the product is with high response speed; Otherwise not.

	C	
	$\mathbf{\Sigma}$	

HIGH CONTRAST

This icon on the cover indicates the product is with high contrast; Otherwise not.



WIDE VIEWING SCOPE

This icon on the cover indicates the product is with wide viewing scope; Otherwise not.



RoHS COMPLIANCE

This icon on the cover indicates the product meets ROHS requirements; Otherwise not.



3TIMEs 100% QC EXAMINATION This icon on the cover indicates the product

has passed Midas thrice 100% QC. Otherwise not.



VIcm = 3.0V

This icon on the cover indicates the product can work at 3.0V exactly; otherwise not.



PROTECTION CIRCUIT

This icon on the cover indicates the product is with protection circuit; Otherwise not.



LONG LIFE VERSION

This icon on the cover indicates the product is long life version (over 9K hours guaranteed); Otherwise not.



Anti UV VERSION

This icon on the cover indicates the product is against UV line. Otherwise not.



OPERATION TEMPERATURE RANGE

This icon on the cover indicates the operating temperature range (X-Y).



TWICE SELECTION OF LED MATERIALS

This icon on the cover indicates the LED had passed Midas twice strict selection which promises the product's identical color and brightness; Otherwise not.



N SERIES TECHNOLOGY (2008 developed) New structure, new craft, new technology and new materials inside both LCD module and LCD panel to improve the "RainBow"

Midas LCD Part Number System

MC	COG	132033	Α	*	6	w	*	*	-	S	Ν	т	L	w	*	*
1	2	3	4	5	6	7	8	9	-	10	11	12	13	14	15	16
1	=	MC: Midas	Сотро	onents												
2	=	Blank: COE	B (chip	on boa	rd) CO	G: chip	on glas	s								
3	=	No of dots		(e.g. 2	40064	= 240 x	x 64 dot	s)	(6	e.g. 216	05 = 2	x 16 5m	m C.H.)		
4	=	Series														
5	=	Series Varia	unt:	A to Z	– see	addend	um									
6	=	3: 3 o'clock		6: 6 o'	clock	Ģ) : 9 o'cl	ock	1	2 : 12 o'	clock					
7	=	S: Normal ((0 to +	50 deg	C) W:	Wide t	emp. (-	20 to +	70 de	gC)X:	Exten	ded ten	np (-30 -	+ 80 De	gC)	
8	=	Character S	et													
		Blank: Star C: Chinese S CB: Chinese H: Hebrew K: Europea L: English/, M: Europea R: Cyrillic W: Europea U: Europea	Simplif e Big 5 un (std) Japane an (En an (En	fied (Gra (Graph) (Englis ese (spec glish/Sc glish/Gr	aphic] ic Disj sh/Ger cial) andina reek)	Display plays or man/Fr wian)	lly) ench/G									
9	=	Bezel Heigh	nt (whe	ere appli	icable .	/availal	ole)									
		Blank 9 2 8 3 7 4 7 5 9 6 7 7 7 8 6 9 6 A 5 B 5 D 6 E 5 F 4 G 3		ble	o Top	Com		5+ 16- non ate non ate non ate non ate ate ate ate ate ate ate ate ate	1	Array Edge I Array Array Array Array Array Array Edge Edge Edge Edge Edge Edge Edge Edge	y y y y y y y y y y y e e e e e		su		ly	
10	=	T: TN S: S	TN B:	STN B	lue G:	STN G	rey F:	FSTN	F2: F	FSTN	V: VA	(Vertica	ally Aliş	gned)		
11	=	P: Positive	N: Ne	gative												
12	=	R: Reflectiv	ve M:	Transm	issive	T: Trar	nsflectiv	ve								
13	=	Backlight:	Blank	: Reflect	tive L	: LED										
14	=	Backlight C	Colour:	Y: Yel	llow-G	reen W	White	e B: Bl	ie R:	Red A	: Ambe	er 0: Oi	range G	: Green	RGB: 1	R.G.B.
15	=	Driver Chip	:	Blank	: Stan	dard l	[: I ² C	S: SPI	Г: Тоз	shiba T	6963C	A: Av	ant SAI	P1024B	R: R	aio RA6963
16	=	Voltage Va	riant: e	e.g. 3 = 3	3v											

BOOKBINDING AREA							
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1. GENERAL SPECIFICATIONS

ITEM	NOMINAL DIMENSIONS / AVAILABLE OPTIONS
DISPLAY FORMAT	128 X 64 DOT MATRIX
LCD PANEL OPTIONS	STN (Yellow-green color)
POLARIZER OPTIONS	Positive, Transflective
BACKLIGHT OPTIONS	Array type LED backlight (Yellow-green color)
VIEWING ANGLE OPTIONS	6:00 (Bottom)
TEMPERATURE RANGE OPTIONS	Wide temperature range (-20 °C ~ 70 °C)
CONTROLLER IC	NT7107C+NT7108C
NEGATIVE IC	Built in
DISPLAY DUTY	1/64
DRIVING BIAS	1/9

2. MECHANICAL SPECIFICATIONS

OVERALL SIZE	LED backlight	versio	on: 93.0 x 70.0 x	max 13.0	mm			
VIEWING AREA	72.0W x <mark>4</mark> 0.0H	mm	HOLE-HOLE	88.0W x 64.0H	mm			
DOT SIZE	0.48W x <mark>0</mark> .48H	mm	DOT PITCH	0.04W x 0.04H	mm			
WEIGHT (EL BKL)	6 0. 0	g	WEIGHT (LED BKL)	83.0	g			
3. ABSOLUTE MAXIMUM RATINGS								

3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	MIN	MAX	UNIT
POWER SUPPLY (LOGIC)	Vdd	25°C	-0.3	7.0	V
POWER SUPPLY (LCD)	V0	25°C	Vdd -19.0	Vdd +0.3	V
INPUT VOLTAGE	Vin	25°C	-0.3	Vdd +0.3	V
OPERATING TEMPERATURE	Vopr		-20	70	°C
STORAGE TEMPERATURE	Vstg		-30	80	C°

4. ELECTRONICAL CHARACTERISTIC*

ITEM	SYMBOL	CONDITION	ST	ANDAI	RD	UNIT
	STWBUL	CONDITION	MIN	ΤΥΡ	MAX	UNIT
Input voltage	Vdd	+5V	2.7	5.0	5.5	V
Supply current	ldd	Vdd=5V		2.1		mA
		-20 [°] C	8.40		9.00	
Recommended LCD driving		0°C	8.20		8.80	
voltage for normal temp. Version module	Vdd - V0	25 [°] C	8.10	8.40	8.70	V
		50°C	8.00		8.60	
		70 [°] C	7.85		8.45	
LED forward voltage	Vf	25°C	3.8		4.4	V
LED forward current	lf	25°C		330		mA
LED reverse Current	lr	25°C			330	μA
LED Peak wave length	λρ	25°C lf = 330mA	568		575	nm
LED illuminance (Without LCD)	Lv	25°C lf = 330mA	180		220	cd/m ²
LED life time		25°C If = 330mA	100K**			Hours

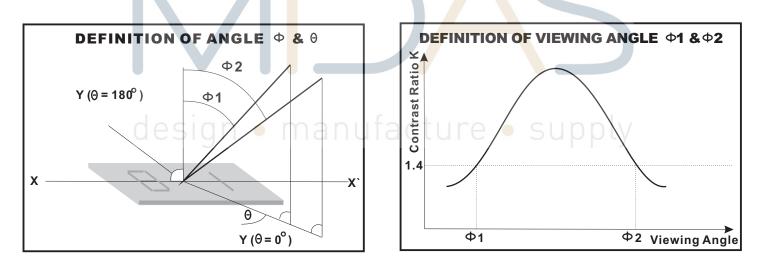
* The above data are for reference only.

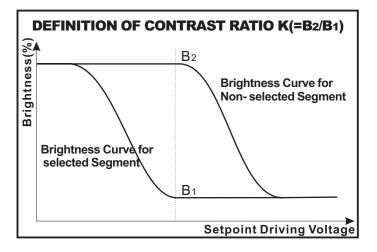
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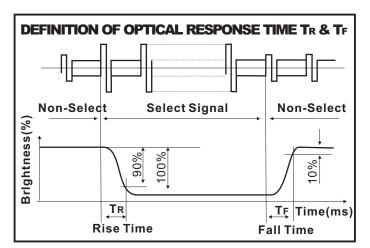
5. OPTICAL CHARACTERISTIC

FOR TN TYPE LCD MODULE (TA=25 °C, Vdd=5.0V \pm 0.25V)									
ITEM	SYMBOL	CONDITION	MIN	ТҮР	MAX	UNIT			
	Φ2–Φ 1	K=4	30			deq			
VIEWING ANGLE	Θ	N=4	25			ueg			
CONTRAST RATIO	К			2					
RESPONSE TIME(RISE)	TR			120	150	ms			
RESPONSE TIME(FALL)	TF			120	150	ms			

FOR STN TYPE LCD MODULE (TA=25 °C, Vdd=5.0V ±0.25V)								
ITEM	SYMBOL	CONDITION	MIN	ТҮР	MAX	UNIT		
	Φ 2 –Φ 1	K-A	40			deg		
VIEWING ANGLE	Θ	K=4	60]		ueg		
CONTRAST RATIO	К			6				
RESPONSE TIME(RISE)	TR			150	250	ms		
RESPONSE TIME(FALL)	TF			150	250	ms		







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6. DC CHARACTERISTIC

(Unless otherwise stated, VDD= $+5V \pm 10\%$, VSS=0V, Ta=25 °C)

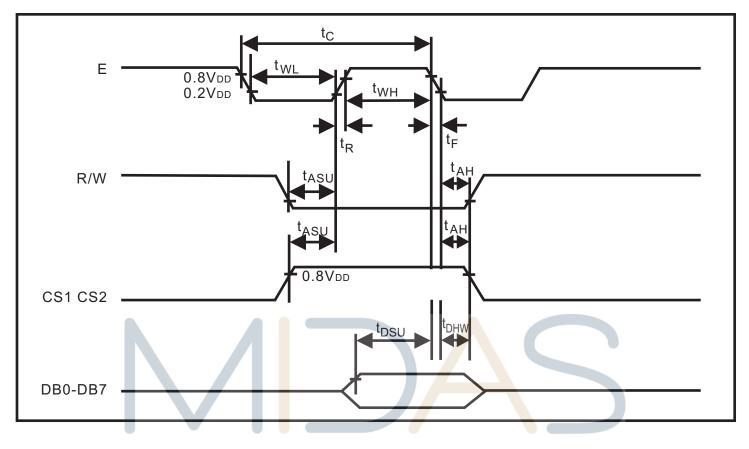
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
High Level Input Voltage	VIH1		0.7VDD		VDD	V
nigh Level input voltage	VIH2		0.7VDD		VDD	V
Low Level Input Voltage	VIL1		0		0.3VDD	V
Low Level input voltage	VIL2		0		0.8	V
High Level Output Voltage	VOH	IOH = - 200µА	2.4			V
Low Level Output Voltage	VOL	IOL = 1.6 mA			0.4	V
Input Leakage Current	ILKG	VIN = VDD to VSS	-1.0		1.0	μ Α
Three-State (OFF) Input Current	ITSL	VIN = VDD to VSS	-5.0		5.0	μ Α
	IDD1	During Display			100	μ Α
Operating Current	IDD2	During Access, Access Cycle = 1MHz			500	μ

7. AC CHARACTERISTIC

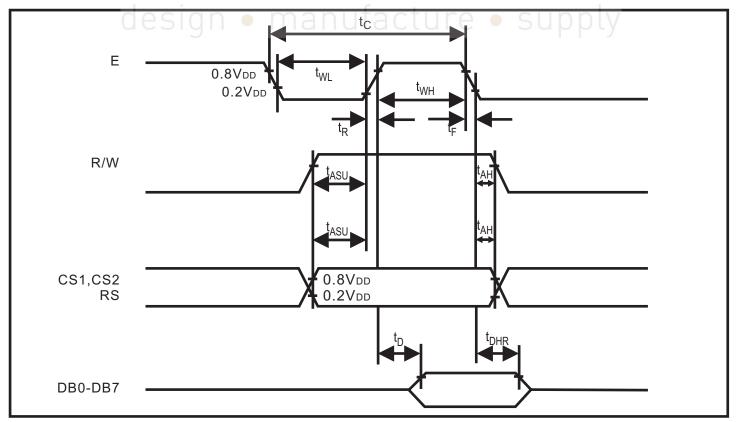
Characteristic	Symbol	an Minac	Тур	SUMax	Unit
E Cycle	tc	1000	_		ns
E High Level Width	t wн	450	—	—	ns
E Low Level Width	t w∟	450	—	—	ns
E Rise Time	t _R		_	25	ns
E Fall Time	tf	_	—	25	ns
Address Setup Time	t asu	140	—	—	ns
Address Hold Time	t ah	10	—	—	ns
Data Setup Time	t dsu	200	—	—	ns
Data Delay Time	tD	_	—	320	ns
Data Hold Time (Write)	t dhw	10		_	ns
Data Hold Time (Read)	t _{DHR}	20			ns

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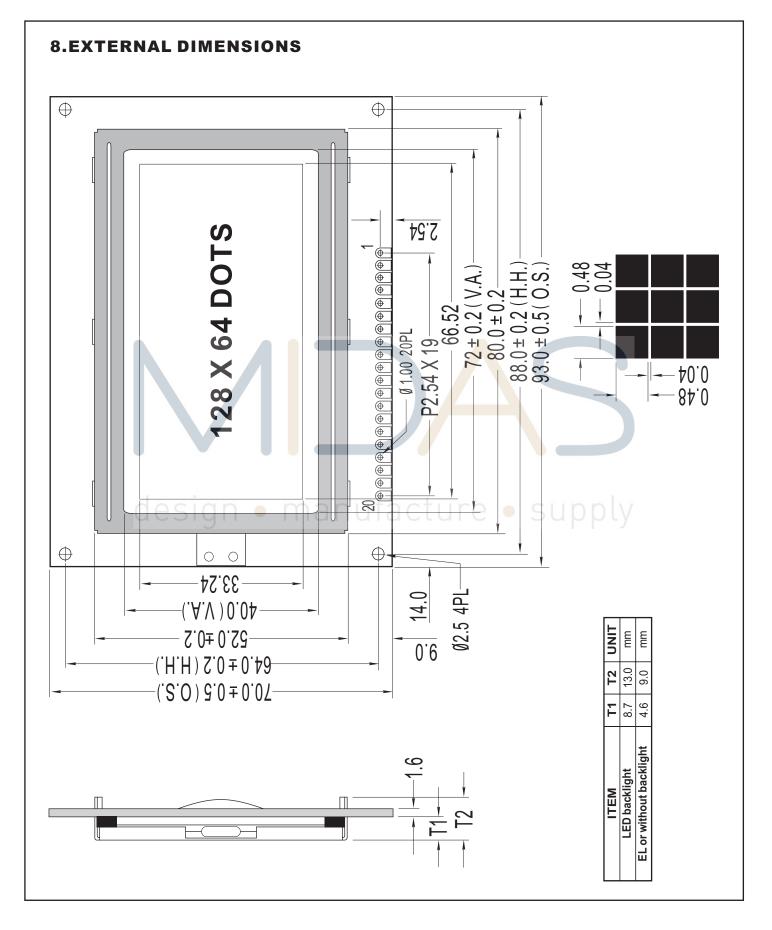
7.1 WRITE MODE TIMING DIAGRAM



7.2 READ MODE TIMING DIAGRAM



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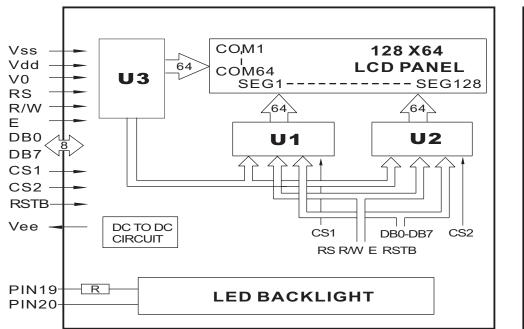


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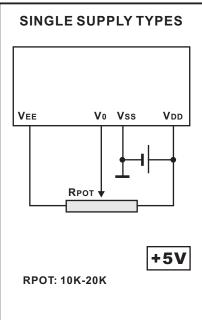
9. PIN ASSIGNMENT

PIN NO.	SYMBOL	FUN	ICTION	REMARK
1	Vss		0V	
2	Vdd	Power Supply	+5V	
3	V0		Contrast Adjust	
4	RS	Rigister	Select signal	
5	R/W	Read / Write		
6	E	Chip Enable signal		
7	DB0	Data Bit 0		
8	DB1	Data Bit 1		
9	DB2	Data Bit 2		
10	DB3	Data Bit 3		
11	DB4	Data Bit 4		
12	DB5	Dat	Data Bit 5	
13	DB6	Dat	a Bit 6	
14	DB7	Dat	a Bit 7	
15	CS1	When CS1=H,	CS2=L <mark>, s</mark> ele <mark>ct</mark> U1	
16	CS2	When CS1=L,	CS2=H, select U2	
17	RSTB	Reset signal		
18	Vee	Negative voltage output		
19	LED+	Anode of LED Unit		5.0V
20	LED-	Cathode	of LED Unit	0V

10. BLOCK DIAGRAM



11. POWER SUPPLY



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12. FUNCTIONAL DESCRIPTION

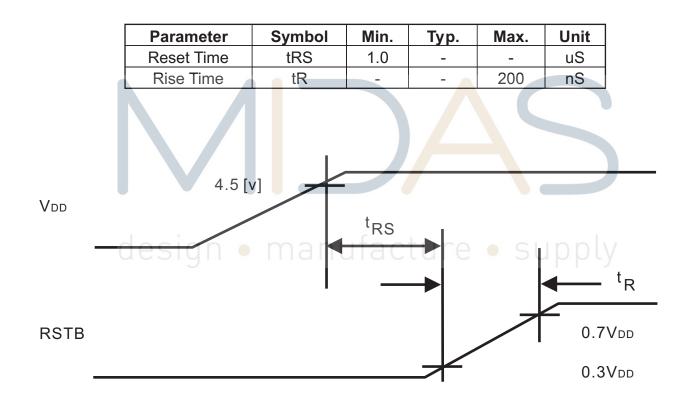
12.1 RESET

The system can be initialized by setting the RSTB to LOW when turning the power ON or by instruction from the MPU. When the RSTB is set to LOW, the following condition occurs:

- 1. The Display is turned OFF.
- 2. The Display Start Line register is set to 0 (Z-Address 0).

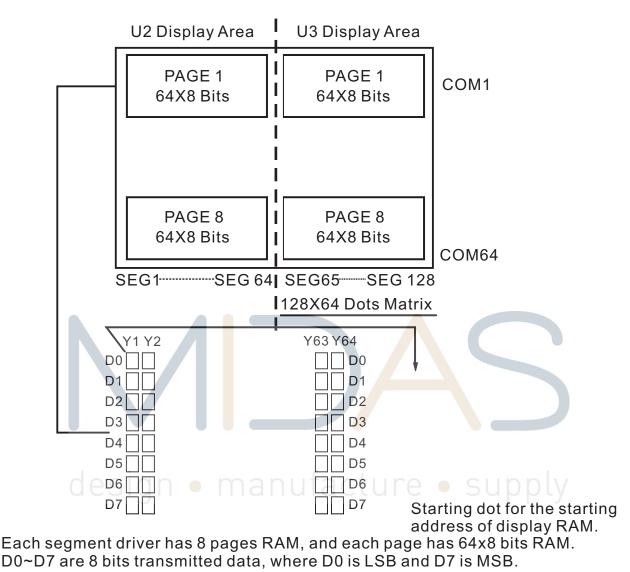
No instructions except the status read can be executed when the RSTB is LOW. This means that in order to execute other instructions, the RSTB must be cleared by setting DB4 to 0 and the DB7 set to 0 by status read instruction.

The table below shows the power supply initial conditions.



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12.3 RELATION BETWEEN DISPLAY PATTERN AND DRIVERS



12.4 DISPLAY DATA RAM

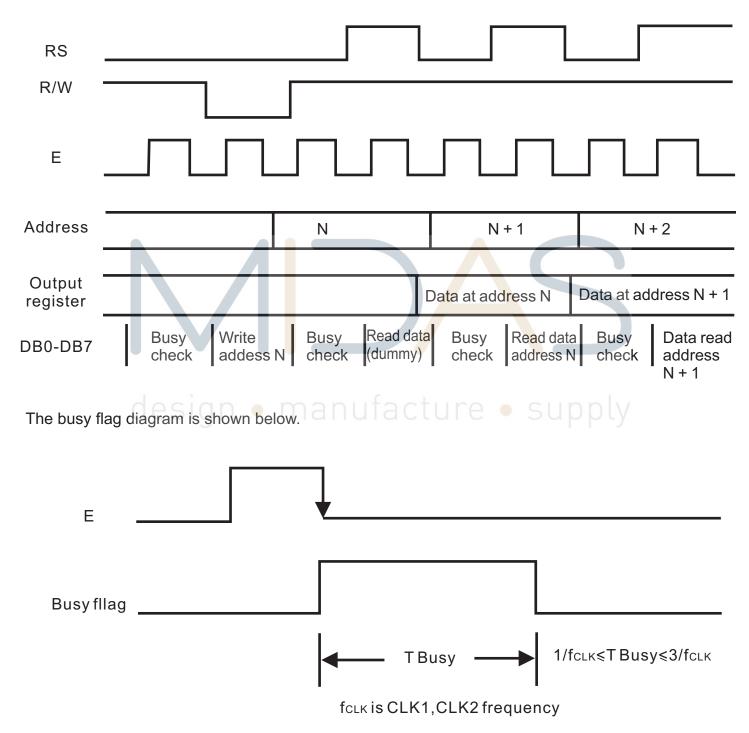
The Display Data RAM is used to store the display data for the liquid crystal display. Write data 1 is indicates an ON State of the LCDs dot matrix while the OFF State is written as 0. ADC Signal can control the Display Data RAM and the segment output. Please refer to the table below.

ADC *	Display Data
Н	Y-Address 0:S1 to Y-Address 63:S64

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12.2 BUSY FLAG

The busy flag (DB7) is used to determine whether Nt7108 is operating or not. When the busy flag is HIGH, internal operation is taking place. When the busy flag is LOW, Nt7108 can accept data or instructions. The busy check diagram is shown below.



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13. INSTRUCTION

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	L	L	L	L	Н	Н	Н	Н	Н	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF H:ON
Set address (Y address)	L	L	L	Н		Υa	addres	s (0~6	63)		Sets the Y address in the Y address counter.
Set Page (X address)	L	L	Н	L	Н	Н	Н	Pa	ge (0-	~7)	Sets the X address at the X address register.
Display Start Line (Z address)	L	L	н	н		Disp		Indicates the display data RAM displayed at the top of the screen.			
Status Read	163	5 ipj	B U S Y	n	O N O F F	R E S E T	cţu	re	L	sup	BUSY L:Ready H:In operation ON/OFF L:Display ON H:Display OFF RESET L:Normal H:Reset
Write Display Data	Н	L					Writes data (DB0:7) into display data RAM,After writing instruction,Y address is increased by 1 automatically.				
Read Display Data	Н	Н		Read Data							Reads data (DB0:7) from display data RAM to the data bus.

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14. DESCRIPTION OF COMMAND

Display On/Off

 RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0.

Though the data is not on the screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.

Set Address(Y Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

Set Page(X Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

Display Start Line(Z Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen.

When the display duty cycle is 1/64 or others(1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction , is displayed.

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Status Read

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	Db1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

BUSY

When BUSY is 1,the Chip is executing internal operation and no instructions are accepted. When BUSY is 0,the Chip is ready to accept any instructions.

• ON/OFF

When ON/OFFis 1,the display is offWhen ON/OFFis 0,the display is on.

RESET

When RESET is 1,the system is being initialized. In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in the usual operation condition.

Write Display Data

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0-D7) into the display data RAM.

After writing instruction, Y address is increased by 1 automatically.

Read Display Data

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

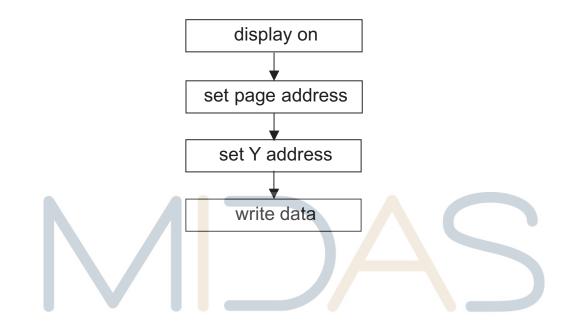
Reads data (D0-D7) from the display data RAM.

After reading instruction, Y address is increased by 1 automatically.

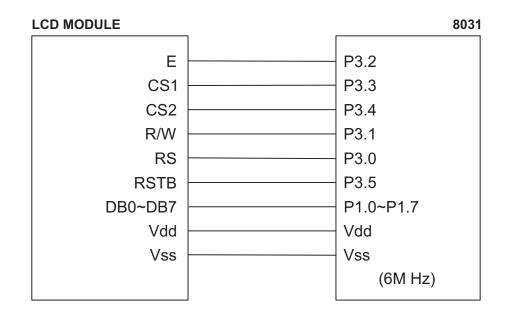
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15. APPLICATION EXAMPLE

Application Flowchart



Application Circuit - manufacture - supply



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16. PACKING DETAIL

WITH LED BKL	WITHOUT LED BKL	NOTE
30 PCS/BOX	30 PCS/BOX	1. The weight is estimated for reference only.
8 BOXES/CARTON	8 BOXES/CARTON	2. Packing detail may be changed without notice.
240 PCS/CARTON	240 PCS/CARTON	
19.00 KGS/CTN(G.W.)	17.00 KGS/CTN(G.W.)	
0.07 M ³ /CARTON	0.07 M ³ /CARTON	

