HALOGEN

FREE



Vishay General Semiconductor

High Current Density Surface Mount Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.27 \text{ V}$ at $I_F = 5 \text{ A}$

TMBS® eSMP® Series K SlimDPAK

PRIMARY CHARACTERISTICS				
I _{F(AV)}	35 A			
V_{RRM}	45 V			
I _{FSM}	260 A			
V _F at I _F = 35 A (T _A = 125 °C)	0.46 V			
T _J max.	150 °C			
Package	SlimDPAK			
Diode variation	Single die			

FEATURES

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

MECHANICAL DATA

Case: SlimDPAK

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V35PW45	UNIT	
Device marking code		V35PW45		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum average forward rectified current (Fig. 1)	I _{F(AV)} ⁽¹⁾	35	А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	260	А	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Note

(1) With infinite heat sink



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum Instantaneous forward voltage	$I_F = 5.0 \text{ A}$	T _A = 25 °C	V _F ⁽¹⁾	0.40	-	V
	I _F = 17.5 A			0.47	-	
	I _F = 35 A			0.54	0.62	
	I _F = 5.0 A	T _A = 125 °C		0.27	-	
	I _F = 17.5 A			0.37	-	
	I _F = 35 A			0.46	0.54	
Reverse current	V _R = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	2.5	- mA
	v _R = 45 v	T _A = 125 °C		27	65	
Typical junction capacitance	4.0 V, 1 MHz		CJ	4230	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)			
PARAMETER	ETER SYMBOL V35PW45		
Tuning thermal registeres	R _{θJA} (1)(2)	55	°C/W
Typical thermal resistance	R _{0JM} (3)	1.5	C/VV

Notes

- $^{(1)}$ The heat generated must be less than thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$
- $^{(2)}$ Free air, mounted on recommended copper pad area; thermal resistance $R_{\theta JA}$ junction to ambient
- $^{(3)}$ Mounted on infinite heat sink; thermal resistance $R_{\theta JM}$ junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V35PW45-M3/I	0.20	I	4500	13" diameter plastic tape and reel	
V35PW45HM3/I (1)	0.20	I	4500	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

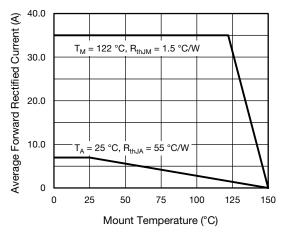


Fig. 1 - Maximum Forward Current Derating Curve

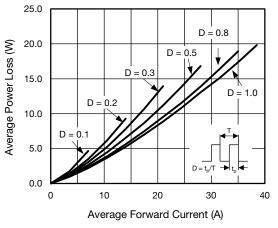


Fig. 2 - Forward Power Loss Characteristics

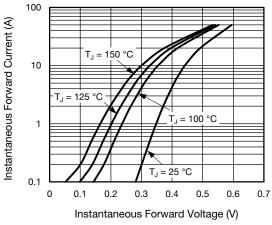


Fig. 3 - Typical Instantaneous Forward Characteristics

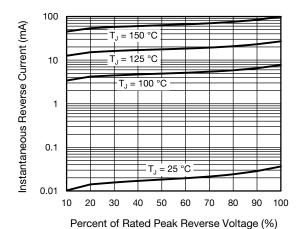


Fig. 4 - Typical Reverse Leakage Characteristics

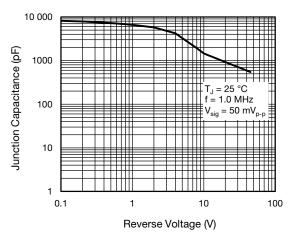


Fig. 5 - Typical Junction Capacitance

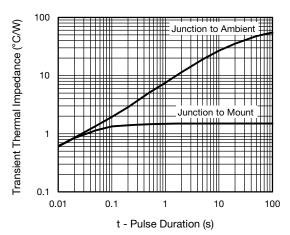


Fig. 6 - Typical Transient Thermal Impedance



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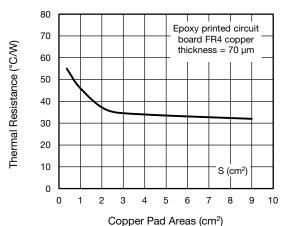
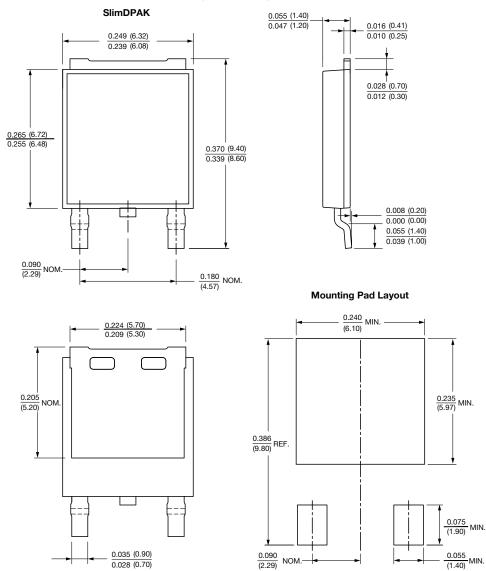


Fig. 7 - Typical Resistance Junction to Ambient vs. Copper Pad Areas

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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