

DESIGN/PROCESS CHANGE NOTIFICATION

This is to inform you that a change is being made to the products listed below.

Unless otherwise indicated in the details of this notification, the identified change will have no impact on product quality, reliability, electrical, visual or mechanical performance and affected products will remain fully compliant to all published specifications. Products incorporating this change may be shipped interchangeably with existing unchanged products.

This change is planned to take effect in 90 calendar days from the date of this notification. Please work with your local Fairchild Sales Representative to manage your inventory of unchanged product if your evaluation of this change will require more than 90 calendar days.

Please contact your local Customer Quality Engineer within 30 days of receipt of this notification if you require any additional data or samples. Alternatively, you may send an email request for data, samples or other information to PCNSupport@fairchildsemi.com.

Implementation of change:

Expected First Shipment Date for Changed Product : Nov. 19, 2012

Expected First Date Code of Changed Product :1230

Description of Change (From) :

Datasheet electrical values of $I_{DET}=550\mu A$ of 0.37 V (minimum) and 0.43 V (maximum).

Current Sense Section						
t_{PD}	Delay to Output			150	200	ns
V_{LIMIT}	The Limit Voltage on CSPWM Pin for Over Power Compensation	$I_{DET} < 75\mu A, T_A=25^\circ C$	0.81	0.84	0.87	V
		$I_{DET}=185\mu A, T_A=25^\circ C$	0.69	0.72	0.75	
		$I_{DET}=350\mu A, T_A=25^\circ C$	0.55	0.58	0.61	
		$I_{DET}=550\mu A, T_A=25^\circ C$	0.37	0.40	0.43	

Description of Change (To) :

Datasheet electrical values of $I_{DET}=550\mu A$ of 0.34 V (minimum) and 0.46 V (maximum).

Current Sense Section						
t_{RD}	Delay to Output			150	200	ns
V_{LIMIT}	Limit Voltage on CSPWM Pin for Over-Power Compensation	$I_{DET} < 75\mu A, T_A=25^\circ C$	0.81	0.84	0.87	V
		$I_{DET}=185\mu A, T_A=25^\circ C$	0.69	0.72	0.75	
		$I_{DET}=350\mu A, T_A=25^\circ C$	0.55	0.58	0.61	
		$I_{DET}=550\mu A, T_A=25^\circ C$	0.34	0.40	0.46	

Reason for Change:

To better align listed datasheet limits with the actual product performance based on design and manufacturing tolerances.

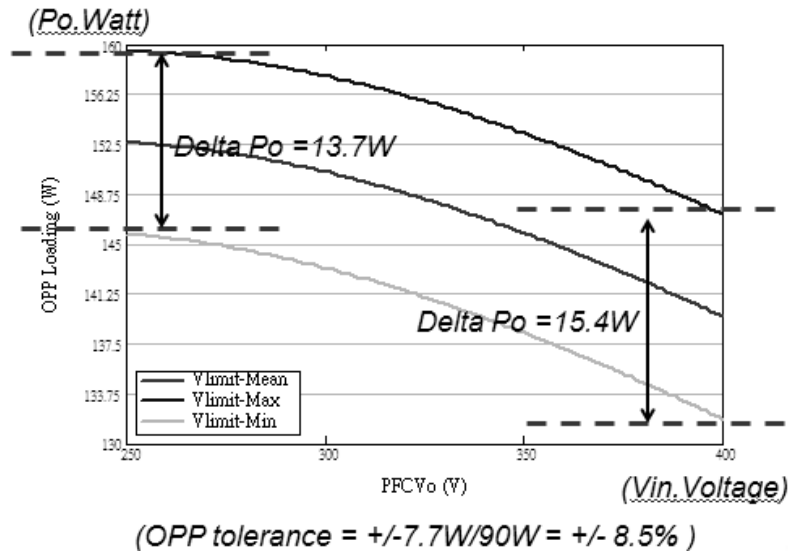
Affected Product(s):

FAN6921A MLMY	FAN6921A MRMY	FAN6921 MLMY
FAN6921 MRMY		

Based on power boards with 90W adapters:

R_{sense} : 0.160 Ω , L_m : 480 μ H, N_p =32, N_s =6, N_{aux} =6, R_{det} =180k, T_{off_delay} =400ns

Before change: V_{limit} tolerance = ± 30 mV, I_{det} =260 μ A~417 μ A from V_{in} =250V~400V



Based on power boards with 90W adapters:

R_{sense} : 0.160 Ω , L_m : 480 μ H, N_p =32, N_s =6, N_{aux} =6, R_{det} =135k, T_{off_delay} =400ns

After change: V_{limit} tolerance = ± 60 mV, I_{det} =347 μ A~556 μ A from V_{in} =250V~400V

