#### **PRODUCT / PROCESS CHANGE NOTIFICATION**

1. PCN basic data		
1.1 Company	STMicroelectronics International N.V	
1.2 PCN No.	IPD/15/9447	
1.3 Title of PCN	Darlington Arrays ULN2003D1013TR and ULQ2003D1013TR SO16 Qualification in AMKOR Manila (Philippine) subcontractor.	
1.4 Product Category	Linear Voltage regulators	
1.5 Issue date	2015-10-14	

2. PCN Team		
2.1 Contact supplier		
2.1.1 Name	SETTLES JEFF	
2.1.2 Phone	+44 1628896222	
2.1.3 Email	jeff.settles@st.com	
2.2 Change responsibility		
2.2.1 Product Manager	Lorenzo NASO	
2.1.2 Marketing Manager	Antonio RIVIERA	
2.1.3 Quality Manager	Paolo MORETTI	

3. Change			
3.1 Category	3.2 Type of change	3.3 Manufacturing Location	
Transfer of a full process or process brick (process step, control plan, recipes) from one site to another site	Product : even if test or process transfer is qualified	AMKOR Manila (Philippine)	

4. Description of change			
	Old	New	
4.1 Description	Shenzhen (China), Bouskoura (Morocco) and ASE (Shanghai)	AMKOR Manila (Philippine)	
4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?	No impact in terms of electrical performances, Quality and Relaibility		

5. Reason / motivation for change		
5.1 Motivation	Progressing on the activities related to quality improvement and along the plan of rationalizing the manufacturing processes, ST is glad to announce the qualification of SO16 in the AMKOR Manila subcontractor (Philippine).	
5.2 Customer Benefit	CAPACITY INCREASE	

6. Marking of parts / traceability of change		
6.1 Description	The traceability of the parts assembled in the new subcontractor will be ensured by different internal codification and QA number.	

7. Timing / schedule		
7.1 Date of qualification results	2015-10-07	
7.2 Intended start of delivery	2016-01-07	
7.3 Qualification sample available?	Upon Request	

8. Qualification / Validation			
8.1 Description	REL 6088-171-W-2015-ULQ2003D1013TR- L203- SO16 Amkor.pdf		
8.2 Qualification report and qualification results	Available (see attachment)	Issue Date	2015-10-14

#### 9. Attachments (additional documentations)

9447PpPrdtLst.pdf REL 6088-171-W-2015-ULQ2003D1013TR- L203- SO16 Amkor.pdf

10. Affected parts			
10. 1 Current		10.2 New (if applicable)	
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No	
	ULN2003D1013TR		
	ULQ2003D1013TR		

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Voltage Regulator & Vref

Quality and Reliability

# **Reliability Report**

**Qualification of a New Subcontractor for SO16 Package** 

Package: SO16 - Amkor T.V: ULQ2003D1013TR

O an anal lu			4	
General Information		Locations		
Product Line	L203	Wafer fab	Ang Mo kio	
Product Description	Multidarlington Array			
P/N	ULQ2003D1013TR			
Product Group	IPD	Assembly plant	AMKOR	
Product division	IND.& POWER CONV Voltage Regulator & Vre			
Packages	S016	Reliability Lab	Catania Reliability LAB	
Silicon Process technology	Bipolar	Reliability assessment	Pass	

#### **DOCUMENT INFORMATION**

Version	Date	Pages	Prepared by	Approved by	Comment
1.0	August 2015	6	Angelo Basile	Giovanni Presti	Final Report

Note: This report is a summary of the reliability trials performed in good faith by STMicroelectronics in order to evaluate the potential reliability risks during the product life using a set of defined test methods.

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## **<u>1</u>** APPLICABLE AND REFERENCE DOCUMENTS

Document reference	Short description
JESD47	Stress-Test-Driven Qualification of Integrated Circuits

### 2 GLOSSARY

DUT	Device Under Test
SS	Sample Size
STD	Standard

## **<u>3 RELIABILITY EVALUATION OVERVIEW</u>**

### 3.1 Objectives

SO16 Qualification in AMKOR subcontractor T.V.:Darlington Arrays ULQ2003D1013TR

### 3.2 Conclusion

Qualification Plan requirements have been fulfilled without exception. It is stressed that reliability tests have shown that the devices behave correctly against environmental tests (no failure). Moreover, the stability of electrical parameters during the accelerated tests demonstrates the ruggedness of the products and safe operation, which is consequently expected during their lifetime.



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## 4 DEVICE CHARACTERISTICS

### 4.1 Device description

The ULQ2001A, ULQ2002A, ULQ2003 and ULQ2004A are high voltage, high current Darlington arrays each containing seven open collector Darlington pairs with common emitters. Each channel is rated at 500 mA and can withstand peak currents of 600 mA. Suppression diodes are included for inductive load driving and the inputs are pinned opposite the outputs to simplify board layout

### 4.2 Construction note

P/N	ULQ2003D1013TR			
Wafer/Die fab. information				
Wafer fab manufacturing location	Ang Mo Kio SINGAPORE			
Technology	BiP > 6um			
Die finishing back side	CHROMIUM/NICKEL/GOLD			
Die size	2280, 1200 micron			
Passivation type	SiN (nitride)			
Wafer Testing (EWS) information				
Electrical testing manufacturing				
location	Ang Mo Kio EWS			
Tester	ASL1000			
Test program	CL203CB6_0300.zip			
Assembly information				
Assembly site	AMKOR ATP1			
Package description	SO 16			
Molding compound	Ероху			
Die attach material	Glue			
Wires bonding materials/diameters	ameters Cu - 1.0mil			
Final testing information				
Testing location	AMKOR ATP3			
Tester	ASL 1000			
Test program	L203_STS_FA 02.prg /l203 STS QAprg_			



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## 5 TESTS RESULTS SUMMARY

### 5.1 Test vehicle

Lot #	Trace Code	Package	Line	Comment
1 Lot				STD
2 lot				STD
3 lot	MBQ7*L203DA6	SO16L	L20303	STD
4 lot				Corner lot HH
5 lot			·	Corner lot LL

### 5.2 Test plan and results summary

F/N. 0EQ2003D1013TK				Stens	Steps Failure			s	Note		
Test	РС	Std ref.	Conditions	SS	h=hours cy=cycles	1 Lot	2 Lot	3 Lot	Lot HH	Lot LL	Note
Die Or	iente	d Tests									
					168h	0/77	0/77	0/77			
HTOL	Ν	JESD22	Ta =125°C		500h	0/77	0/77	0/77			
A-100	VDIAS+50V		1000h	0/77	0/77	0/77					
			168h	0/45	0/45	0/45	0/45	0/45			
HTSL	Ν	JESD22	Ta = 150°C		500h	0/45	0/45	0/45	0/45	0/45	
A-103			1000h	0/45	0/45	0/45	0/45	0/45			
					168h	0/45	0/45	0/45			
HTSL N JESD22	Ta = 175°C		500h	0/45	0/45	0/45			Engineering		
		A-103			1000h	0/45	0/45	0/45			evaluation
Packag	ge Or	iented Test	<u>Ş</u>	-							-
PC	Y	JESD22 A-113	Drying 24 H @ 125°C Store 168 H @ Ta=85°C Rh=85% Oven Reflow @ Tpeak=260°C 3 times		Final	Pass	Pass	Pass	Pass	Pass	
AC	Y	JESD22 A-102	Pa=2Atm / Ta=121°C		168h	0/77	0/77	0/77			
THB Y JESD22 A-101	Ta = 85°C, Rh=85% Vbias +35V		168h	0/77	0/77	0/77					
			500h	0/77	0/77	0/77					
			1000h	0/77	0/77	0/77					
					100cy	0/77	0/77	0/77	0/77	0/77	
тс	Y	JESD22 A-104	Ta = -65°C to 150°C		300cy	0/77	0/77	0/77	0/77	0/77	]
A-104			500cy	0/77	0/77	0/77	0/77	0/77			



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## 6 ANNEXES

### 6.1 Tests Description

Test name	Description	Purpose		
Die Oriented				
<b>HTOL</b> High Temperature Operative Life	The device is stressed in static or dynamic configuration, approaching the operative max. absolute ratings in terms of junction temperature and bias condition.	To determine the effects of bias conditions and temperature on solid state devices over time. It simulates the devices' operating condition in an accelerated way. The typical failure modes are related to, silicon degradation, wire-bonds degradation, oxide faults.		
HTSL High Temperature Storage Life	The device is stored in unbiased condition at the max. temperature allowed by the package materials, sometimes higher than the max. operative temperature.	To investigate the failure mechanisms activated by high temperature, typically wire-bonds solder joint ageing, data retention faults, metal stress- voiding.		
Package Oriented				
<b>PC</b> Preconditioning	The device is submitted to a typical temperature profile used for surface mounting devices, after a controlled moisture absorption.	As stand-alone test: to investigate the moisture sensitivity level. As preconditioning before other reliability tests: to verify that the surface mounting stress does not impact on the subsequent reliability performance. The typical failure modes are "pop corn" effect and delamination.		
AC Auto Clave (Pressure Pot)	The device is stored in saturated steam, at fixed and controlled conditions of pressure and temperature.	To investigate corrosion phenomena affecting die or package materials, related to chemical contamination and package hermeticity.		
<b>TC</b> Temperature Cycling	The device is submitted to cycled temperature excursions, between a hot and a cold chamber in air atmosphere.	To investigate failure modes related to the thermo-mechanical stress induced by the different thermal expansion of the materials interacting in the die-package system. Typical failure modes are linked to metal displacement, dielectric cracking, molding compound delamination, wire-bonds failure, die-attach layer degradation.		
<b>THB</b> Temperature Humidity Bias	The device is biased in static configuration minimizing its internal power dissipation, and stored at controlled conditions of ambient temperature and relative humidity.	To evaluate the package moisture resistance with electrical field applied, both electrolytic and galvanic corrosion are put in evidence.		



### **Public Products List**

PCN Title : Darlington Arrays ULN2003D1013TR and ULQ2003D1013TR

SO16 Qualification in AMKOR Manila (Philippine) subcontractor.

PCN Reference : IPD/15/9447

PCN Created on : 07-Oct-2015

Subject : Public Products List

Dear Customer,

Please find below the Standard Public Products List impacted by the change.

	ULN2003D1013TR	ULQ2003D1013TR	
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