

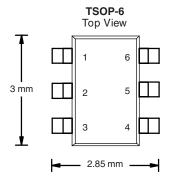
N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
30	0.028 at V _{GS} = 10 V	6.7		
	0.038 at V _{GS} = 4.5 V	5.7		

FEATURES

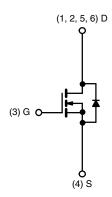
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3424DV-T1-E3 (Lead (Pb)-free)

Si3424DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_A = 25$ °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Dunin Courset /T 150 °C)	T _A = 25 °C	- I _D	6.7	5.0	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		5.4	4.0	
Pulsed Drain Current		I _{DM}	30		Α
Continuous Source Current (Diode Conduction) ^a		I _S	1.7	1.0	
	T _A = 25 °C	- P _D	2.0	1.14	W
Maximum Power Dissipation ^a	T _A = 70 °C		1.3	0.73	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian una lumation ta Anabianta	t ≤ 5 s	R _{thJA}	40	62.5	°C/W
Maximum Junction-to-Ambient ^a	Steady State		90	110	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	25	30	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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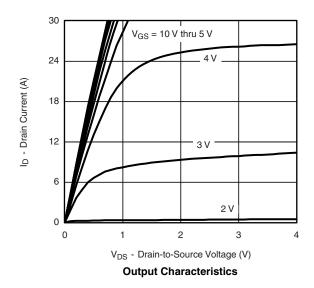
SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valta da Duaira Comunant	lass	V _{DS} = 30 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
	D	V _{GS} = 10 V, I _D = 6.7 A		0.023	0.028	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 5.7 \text{ A}$		0.032	0.038		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 6.7 \text{ A}$		14		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 1.7 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			11.5	18		
Gate-Source Charge	Q _{gs}	Q_{gs} $V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6.7 \text{ A}$		1.6		nC	
Gate-Drain Charge	Q _{gd}			3.2		1	
Turn-On Delay Time	t _{d(on)}			7	15		
Rise Time	t _r	t_r $V_{DD} = 15 \text{ V}, R_L = 15 \Omega$		10	20		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω		20	40	ns	
Fall Time	t _f			11	20		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.7 A, dI/dt = 100 A/μs		40	80		

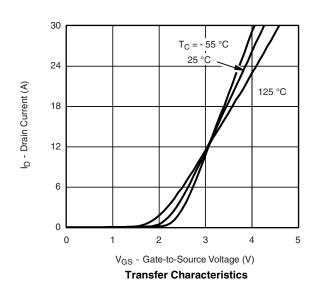
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



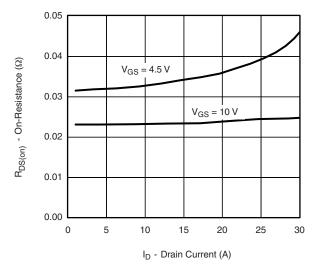




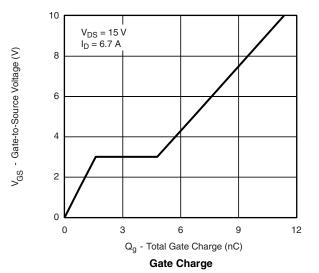


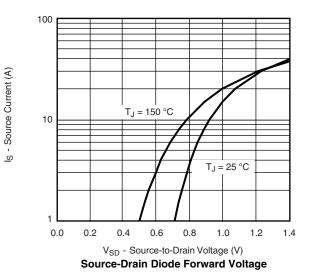


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



On-Resistance vs. Drain Current





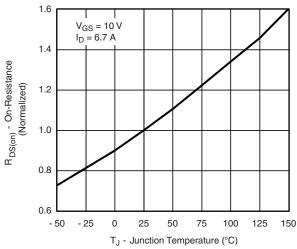
600 C_{iss}

400 C_{oss}

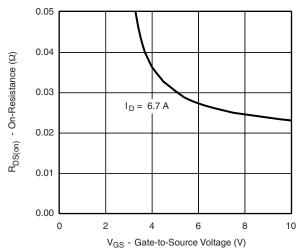
C_{rss}

0 5 10 15 20 25 30





On-Resistance vs. Junction Temperature

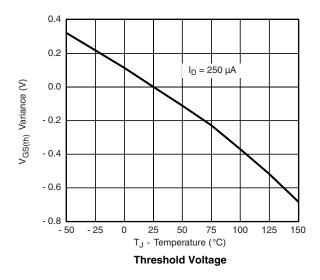


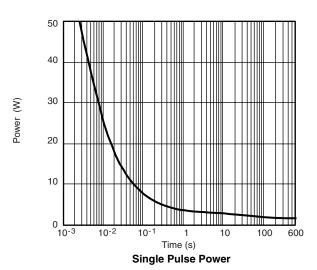
On-Resistance vs. Gate-to-Source Voltage

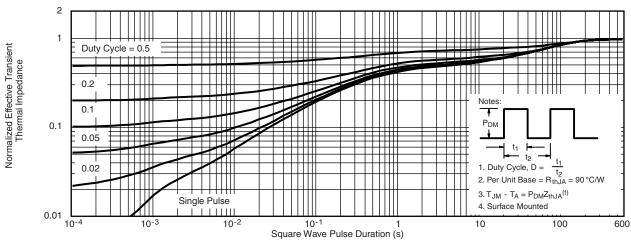
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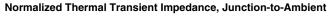
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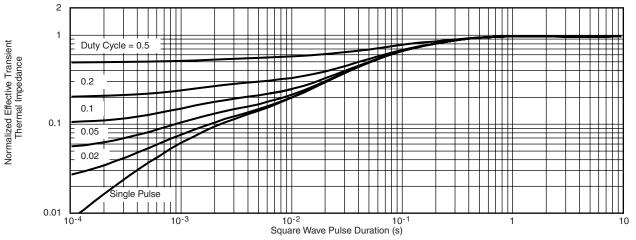
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted











Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71317.



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