



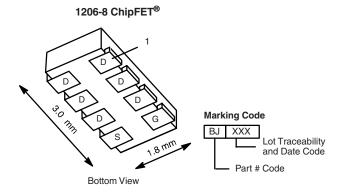
P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 30	0.045 at V _{GS} = - 10 V	- 5.9		
	0.080 at V _{GS} = - 4.5 V	- 4.4		

FEATURES

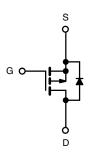
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFETs





Ordering Information: Si5435BDC-T1-E3 (Lead (Pb)-free)

Si5435BDC-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise n	oted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Drain Current /T 150 °C\3	T _A = 25 °C	- I _D	- 5.9	- 4.3		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		- 4.3	- 3.1	^	
Pulsed Drain Current		I _{DM}	- 30		Α	
Continuous Source Current ^a		I _S	- 2.1	- 1.1		
	T _A = 25 °C	P _D	2.5	1.3	W	
Maximum Power Dissipation ^a	T _A = 85 °C		1.3	0.7		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	
Soldering Recommendations (Peak Temperature) ^{b, c}			260		*C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian Installanta Ambienta	t ≤ 5 s	R _{thJA}	40	50	
Maximum Junction-to-Ambient ^a	Steady State		80	95	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		15	20	

Notes:

- a. Surface Mounted on 1" x 1" FR4 board.
- b. See Reliability Manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

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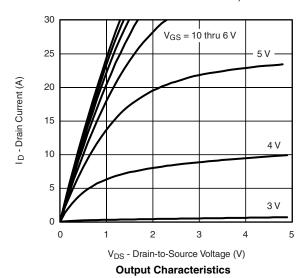
Parameter	Symbol	Symbol Test Conditions		Тур.	Max.	Unit	
Static			•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1		
		V_{DS} = - 30 V, V_{GS} = 0 V, T_J = 85 °C			- 5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 30			Α	
Drain-Source On-State Resistance ^a	В	$V_{GS} = -10 \text{ V}, I_D = -4.3 \text{ A}$		0.035	0.045	0	
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.3 A		0.065	0.080	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 4.3 A		14		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.1 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^b			•				
Total Gate Charge	Q_g			16	24		
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -4.3 \text{ A}$		2.7		nC	
Gate-Drain Charge	Q_{gd}			4.1			
Gate Resistance	R_{g}	f = 1 MHz		8.5		Ω	
Turn-On Delay Time	t _{d(on)}			8	15		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		12	20		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1 A, V_{GEN} = - 10 V, R_g = 6 Ω		32	50	ns	
Fall Time	t _f			20	30		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.1 A, dl/dt = 100 A/μs		25	50		

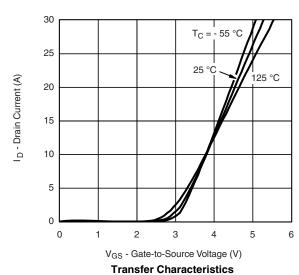
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



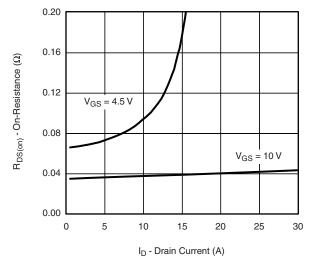




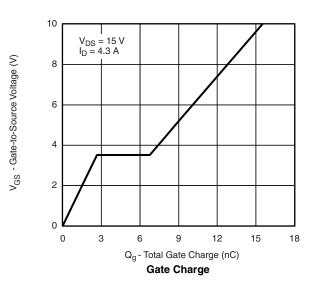


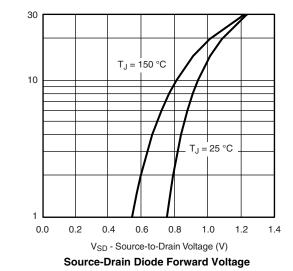


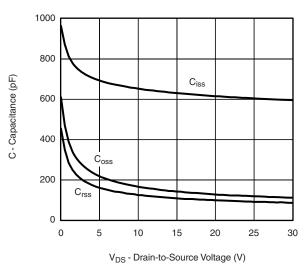
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



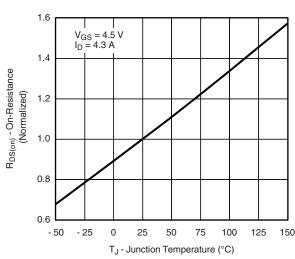
On-Resistance vs. Drain Current



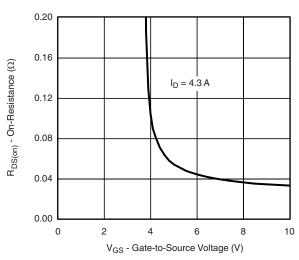




Capacitance



On-Resistance vs. Junction Temperature



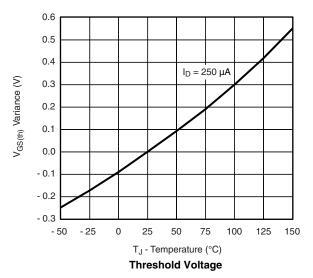
On-Resistance vs. Gate-to-Source Voltage

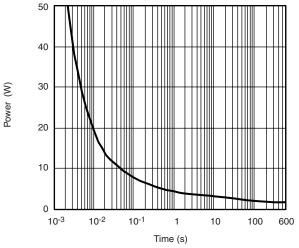
Is - Source Current (A)

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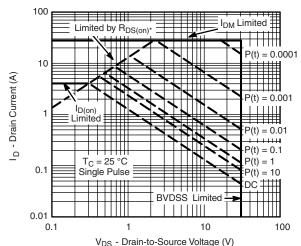
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



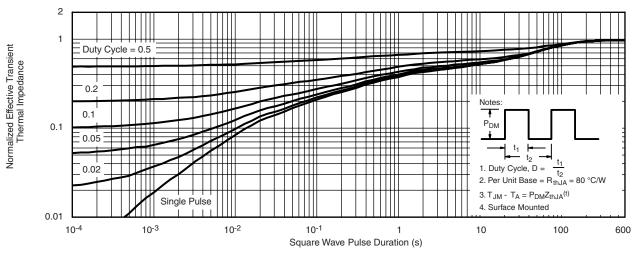


Single Pulse Power



* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

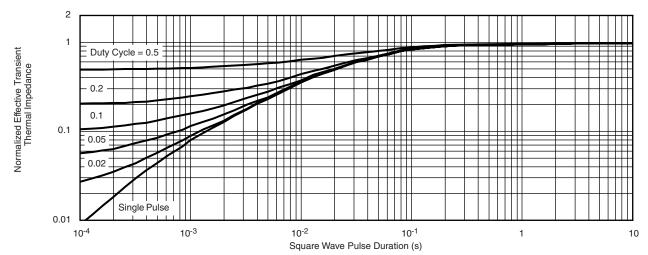
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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Revision: 02-Oct-12 Document Number: 91000