RoHS COMPLIANT

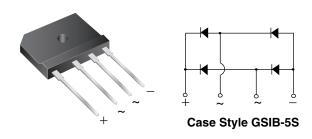
HALOGEN

**FREE** 



### Vishay General Semiconductor

# Low V<sub>F</sub> Single-Phase Single In-Line Bridge Rectifiers



PRIMARY CHARACTERISTICS				
Package	GSIB-5S			
I <sub>F(AV)</sub>	25 A			
$V_{RRM}$	600 V			
I <sub>FSM</sub>	550 A			
I <sub>R</sub>	10 μA			
$V_F$ at $I_F = 12.5$ A, $T_A = 125$ °C	0.76 V			
T <sub>J</sub> max.	150 °C			
Diode variations	In-Line			

#### **FEATURES**

- UL recognition file number E54214, Vol. 1
- Thin single in-line package
- · Oxide planar chip junction
- Low forward voltage drop
- · High surge current capability
- High case dielectric strength of 2500 V<sub>RMS</sub>
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### TYPICAL APPLICATIONS

General purpose use in AC/DC bridge full wave rectification for switching power supply, home appliances and white-goods applications specially for telecom power supply, high efficiency desktop PC and server SMPS.

#### **MECHANICAL DATA**

Case: GSIB-5S

Epoxy meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked on body

**Mounting Torque:** 10 cm-kg (8.8 in-lbs) maximum **Recommended Torque:** 5.7 cm-kg (5 in-lbs)

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER  Maximum repetitive peak reverse voltage		SYMBOL	LVB2560	UNIT	
		V <sub>RRM</sub>	600	V	
Maximum average forward rectified output current at	T <sub>C</sub> = 105 °C	I <sub>O</sub> <sup>(1)</sup>	25	^	
	T <sub>A</sub> = 25 °C	I <sub>O</sub> <sup>(2)</sup>	3.6	A	
Non-repetiitive peak forward surge current 8.3 ms single sine-wave, $T_J = 25\ ^{\circ}\text{C}$		I <sub>FSM</sub>	550	А	
Rating for fusing (t < 8.3 ms)	T <sub>J</sub> = 25 °C	l <sup>2</sup> t	1255	A <sup>2</sup> s	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150	°C	

#### Notes

- (1) Unit case mounted on aluminum plate heatsink
- (2) Units mounted on PCB without heatsink



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	1 10 5 4	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.89	0.92	V
	I <sub>F</sub> = 12.5 A	T <sub>A</sub> = 125 °C		0.76	-	
Reverse current per diode	V <sub>R</sub> = 600 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.2	10	μΑ
		T <sub>A</sub> = 125 °C		140	-	
Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	1.8	-	μs
Typical junction capacitance	4.0 V, 1 MHz	4.0 V, 1 MHz		330	-	pF

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	LVB2560	UNIT	
Maximum thermal resistance	R <sub>0JA</sub> (2)	25	°C/W	
	R <sub>0</sub> JC (1)	1.0	C/VV	

#### Notes

(1) With heatsink

(2) Without heatsink, free air

EMC SURGE IMMUNITY TEST STANDARD (T <sub>A</sub> = 25 °C, unless otherwise noted)						
STANDARD	STANDARD TEST TYPE TEST CONDITIONS		SYMBOL	CLASS	VALUE	
IEC 61000-4-5	Power supply coupling mode, line to line	1.2/50 µs waveform, R = 2 $\Omega$ , T <sub>A</sub> = 25 °C $^{(1)}$	$V_{PEAK}$	-	6 kV maximum	

#### Note

(1) Immunity to IEC 61000-4-5 peak pulse voltage test, 1.2/50 μs, 2 Ω, 5 times each of positive and negative polarity test

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	BASE QUANTITY	DELIVERY MODE			
LVB2560-M3/45	7.1	45	20	Tube		

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

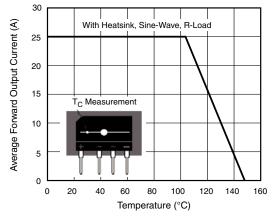


Fig. 1 - Derating Curve Output Rectified Current

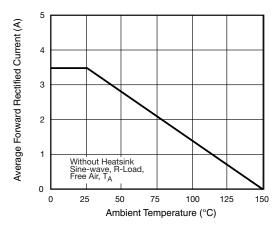


Fig. 2 - Forward Current Derating Curve



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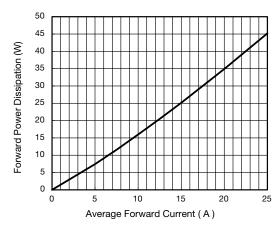


Fig. 3 - Forward Power Dissipation

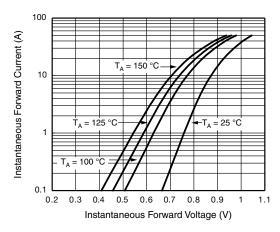


Fig. 4 - Typical Forward Characteristics Per Diode

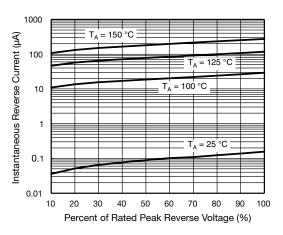


Fig. 5 - Typical Reverse Characteristics Per Diode

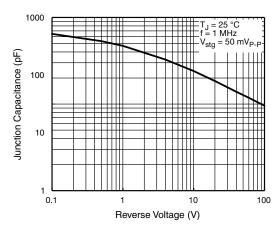
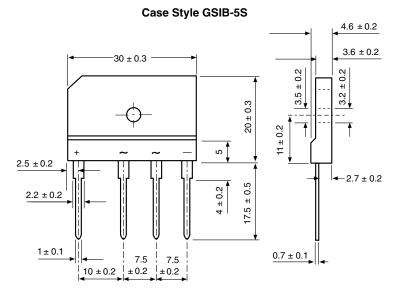


Fig. 6 - Typical Junction Capacitance Per Diode

### PACKAGE OUTLINE DIMENSIONS in millimeters





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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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