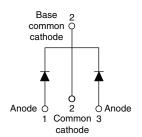


## VS-20CTQ...PbF Series, VS-20CTQ...-N3 Series

Vishay Semiconductors

## Schottky Rectifier, 2 x 10 A





13 mJ

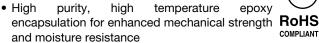
**TO-220AB** 

 $E_{AS}$ 

PRODUCT SUMMARY					
Package	TO-220AB				
I <sub>F(AV)</sub>	2 x 10 A				
$V_{R}$	35 V, 40 V, 45 V				
V <sub>F</sub> at I <sub>F</sub>	0.57 V				
I <sub>RM</sub> max.	15 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
Diode variation	Common cathode				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High frequency operation





- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION**

The VS-20CTQ... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	20	А			
$V_{RRM}$	Range	35 to 45	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	1060	A			
V <sub>F</sub>	10 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.57	V			
TJ	Range	- 55 to 175	°C			

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS- 20CTQ035PbF	VS- 20CTQ035-N3	VS- 20CTQ040PbF	VS- 20CTQ040-N3	VS- 20CTQ045PbF	VS- 20CTQ045-N3	UNITS
Maximum DC reverse voltage	$V_{R}$							
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	35	40	40	45	45	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	(AV) 50 % duty cycle at T <sub>C</sub> = 145 °C, rectangular waveform		20	
Maximum peak one cycle non-repetitive surge current per leg	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1060	Α
urge current per leg I <sub>FSM</sub> See fig. 7		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	265	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 2.0  \text{A},  L = 6.5  \text{mH}$		13	mJ
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		2.0	А



# VS-20CTQ...PbF Series, VS-20CTQ...-N3 Series

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	10 A	- T <sub>J</sub> = 25 °C	0.64	V	
		20 A		0.76		
		10 A	T <sub>J</sub> = 125 °C	0.57		
		20 A	1J = 125 C	0.68		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	2	mA	
See fig. 2		T <sub>J</sub> = 125 °C	VR = nateu VR	15		
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nΗ	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

#### Note

 $<sup>^{(1)}~</sup>$  Pulse width < 300  $\mu s,~duty~cycle < 2~\%$ 

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	1	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg	unction to case per leg		DC operation See fig. 4	3.25	
Maximum thermal resistance, junction to case per package	•		DC operation	1.63	°C/W
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque minimum maximum				6 (5)	kgf ⋅ cm
				12 (10)	(lbf $\cdot$ in)
Marking device			Case style TO-220AB	20CTQ035	
				20CTQ040	
				20CT	Q045

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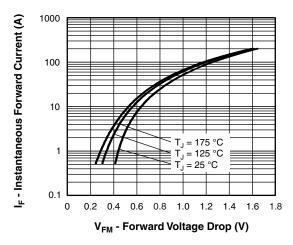


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

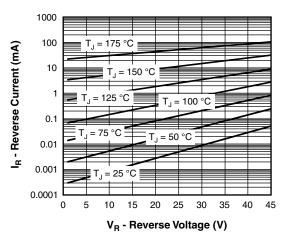


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

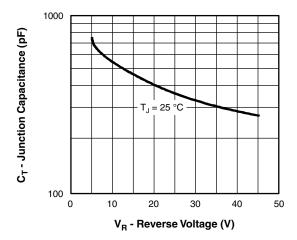


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

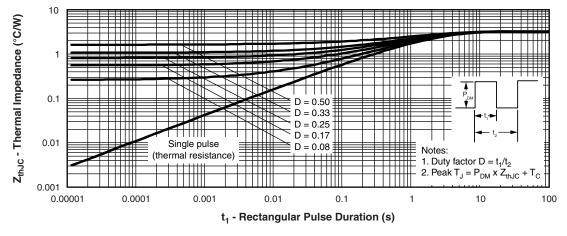


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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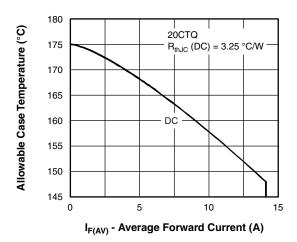


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

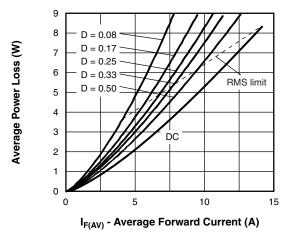


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

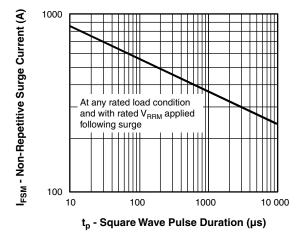


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

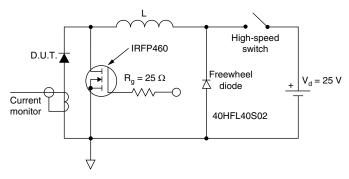
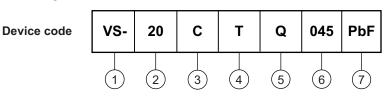


Fig. 8 - Unclamped Inductive Test Circuit

## VS-20CTQ...PbF Series, VS-20CTQ...-N3 Series

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**



1 - Vishay Semiconductors product

2 - Current rating (20 = 20 A)

3 - Circuit configuration

C = Common cathode

4 - Package

T = TO-220

5 - Schottky "Q" series

035 = 35 V 040 = 40 V

6 - Voltage rating

045 = 45 V

7 - Environmental digit

and PoHS compliant

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-20CTQ035PbF	50	1000	Antistatic plastic tube			
VS-20CTQ035-N3	50	1000	Antistatic plastic tube			
VS-20CTQ040PbF	50	1000	Antistatic plastic tube			
VS-20CTQ040-N3	50	1000	Antistatic plastic tube			
VS-20CTQ045PbF	50	1000	Antistatic plastic tube			
VS-20CTQ045-N3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95222</u>				
Dort marking information	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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