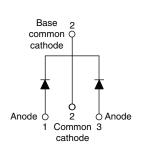


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Schottky Rectifier, 2 x 30 A

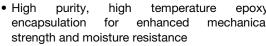


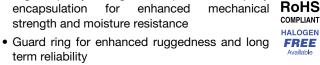


PRODUCT SUMMARY					
Package	TO-220AB				
I _{F(AV)}	2 x 30 A				
V_{R}	30 V				
V _F at I _F	0.44 V				
I _{RM} max.	350 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	13 mJ				

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UI			
I _{F(AV)}	Rectangular waveform (per device)	60	Α		
V _{RRM}		30	V		
I _{FRM}	T _C = 120 °C (per leg)	60	Α		
I _{FSM}	t _p = 5 μs sine	$t_p = 5 \ \mu s \ sine$ 1500			
V _F	30 A _{pk} , T _J = 125 °C	0.44	V		
T _J	Range	- 65 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-62CTQ030PbF	VS-62CTQ030-N3	UNITS	
Maximum DC reverse voltage	V _R	30	30	V	
Maximum working peak reverse voltage	V _{RWM}	30	30	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	oer leg		50 % duty cycle at T _C = 120 °C, rectangular waveform		30	
forward current per	device	I _{F(AV)}			60	
Peak repetitive forward current per leg		I _{FRM}	Rated V_R , square wave, 20 kHz, $T_C = 127$ °C		60	Α
Maximum peak one cycle non-repetitive surge current per leg		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1500	
			10 ms sine or 6 ms rect. pulse	V _{RRM} applied	300	
Non-repetitive avalanche energy per leg		E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3 \text{A}, L = 2.9 \text{mH}$		13	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		3	Α	



VS-62CTQ030PbF, VS-62CTQ030-N3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	30 A	T _J = 25 °C	0.46	0.5	V
		60 A		0.56	0.6	
		30 A	T 105 %O	0.39	0.44	
		60 A	T _J = 125 °C	0.54	0.59	
Mariana		T _J = 25 °C	Rated DC voltage	0.4	2.5	mA
Maximum instantaneous reverse current	I _{RM}	T _J = 125 °C	rated DC voltage		350	IIIA
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		30	000	pF
Typical series inductance	L _S	Measured from top of terminal to mounting plane		8	.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10	000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYME	BOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature ra	nge T _J			- 65 to 150	°C	
Maximum storage temperature rar	nge T _{Ste}	g		- 65 to 175		
Maximum thermal resistance, junction to case per leg	R _{thJ}	IC	DC operation	1.2	°C/W	
Typical thermal resistance, case to heatsink	R _{thC}	cs	Mounting surface, smooth and greased	0.50	- 'C/W	
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
	inimum		Non-lubricated threads	6 (5)	kgf · cm	
Mounting torque ma	aximum		Non-lubricated tirreads	12 (10)	$(lbf \cdot in)$	
Marking device			Case style TO-220AB	62CT	Q030	

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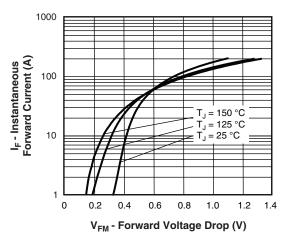


Fig. 1 - Maximum Forward Voltage Drop Characteristics

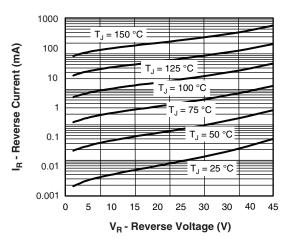


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

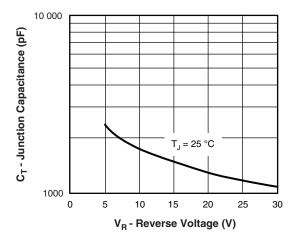


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

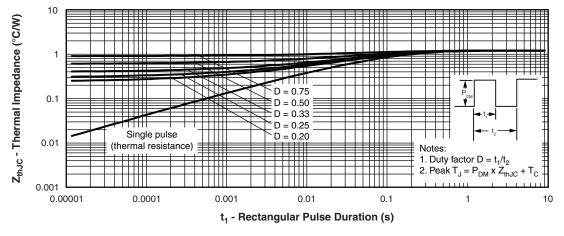


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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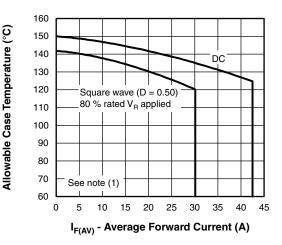


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

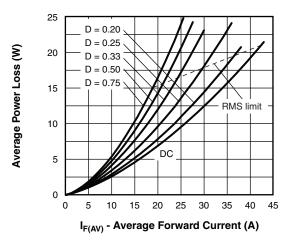


Fig. 6 - Forward Power Loss Characteristics

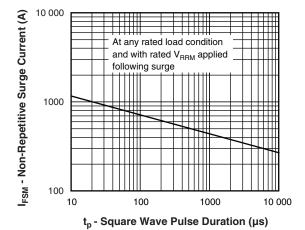


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

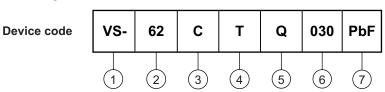
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{thJC}}; \\ \text{Pd} = \text{Forward power loss} = I_{\text{F(AV)}} \times \text{V}_{\text{FM}} \text{ at } (I_{\text{F(AV)}}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{\text{REV}} = \text{Inverse power loss} = \text{V}_{\text{R1}} \times \text{I}_{\text{R}} \text{ (1 - D)}; I_{\text{R}} \text{ at } \text{V}_{\text{R1}} = 80 \text{ \% rated V}_{\text{R}} \\ \end{array}$

VS-62CTQ030PbF, VS-62CTQ030-N3

Vishay Semiconductors

ORDERING INFORMATION TABLE



1 - Vishay Semiconductors product

2 - Current rating (60 = 60 A)

3 - Circuit configuration

C = Common cathode

4 - Package

T = TO-220

5 - Schottky "Q" series

Voltage rating (030 = 30 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-62CTQ030PbF	50	1000	Antistatic plastic tube			
VS-62CTQ030-N3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Death are adding a information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			
SPICE model		www.vishay.com/doc?95185			



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