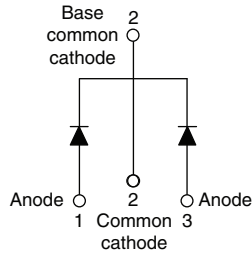




## Schottky Rectifier, 2 x 15 A



TO-220AB



### FEATURES

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



PRODUCT SUMMARY	
Package	TO-220AB
I <sub>F(AV)</sub>	2 x 15 A
V <sub>R</sub>	25 V, 30 V
V <sub>F</sub> at I <sub>F</sub>	0.40 V
I <sub>RM</sub> max.	97 mA at 125 °C
T <sub>J</sub> max.	150 °C
Diode variation	Common cathode
E <sub>AS</sub>	13 mJ

### DESCRIPTION

The VS-32CTQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
I <sub>F(AV)</sub>	Rectangular waveform	30	A
V <sub>RRM</sub>		25/30	V
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	900	A
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.40	V
T <sub>J</sub>	Range	- 55 to 150	°C

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-32CTQ025PbF	VS-32CTQ025-N3	VS-32CTQ030PbF	VS-32CTQ030-N3	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	25	25	30	30	V
Maximum working peak reverse voltage	V <sub>RWM</sub>					

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 115 °C, rectangular waveform		30	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I <sub>FSM</sub>	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	900	
		10 ms sine or 6 ms rect. pulse		250	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.20 A, L = 11.10 mH		13	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 μs Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		3	A



<b>ELECTRICAL SPECIFICATIONS</b>					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	$V_{FM}^{(1)}$	15 A	$T_J = 25\text{ }^\circ\text{C}$	0.49	V
		30 A		0.58	
		15 A	$T_J = 125\text{ }^\circ\text{C}$	0.40	
		30 A		0.53	
Maximum reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{Rated } V_R$	1.75	mA
		$T_J = 125\text{ }^\circ\text{C}$		97	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.233	V
Forward slope resistance	$r_f$			9.09	m $\Omega$
Maximum junction capacitance per leg	$C_T$	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 $^\circ\text{C}$		1300	pF
Typical series inductance per leg	$L_S$	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated $V_R$		10 000	V/ $\mu\text{s}$

**Note**

<sup>(1)</sup> Pulse width < 300  $\mu\text{s}$ , duty cycle < 2 %

<b>THERMAL - MECHANICAL SPECIFICATIONS</b>					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$			- 55 to 150	$^\circ\text{C}$
Maximum thermal resistance, junction to case per leg	$R_{thJC}$	DC operation See fig. 4		3.25	$^\circ\text{C/W}$
Typical thermal resistance, case to heatsink	$R_{thCS}$	Mounting surface, smooth and greased		0.50	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm (lbf · in)
	maximum			12 (10)	
Marking device		Case style TO-220AB		32CTQ025	
				32CTQ030	

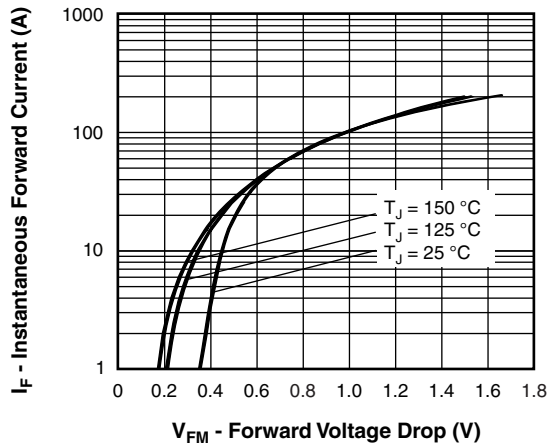


Fig. 1 - Maximum Forward Voltage Drop Characteristics

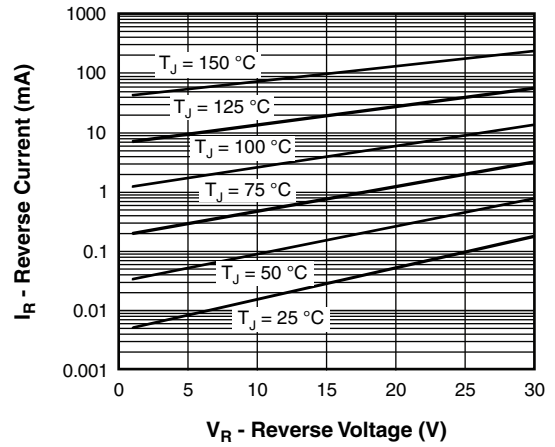


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

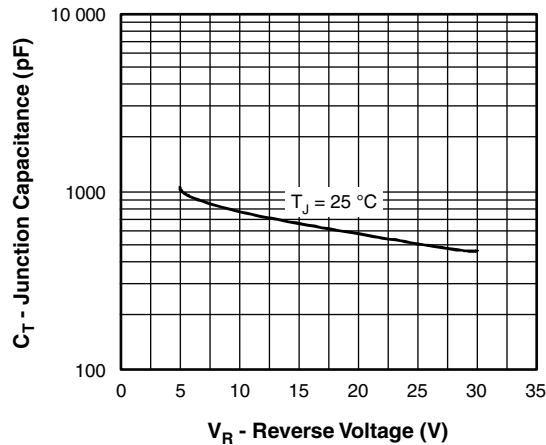


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

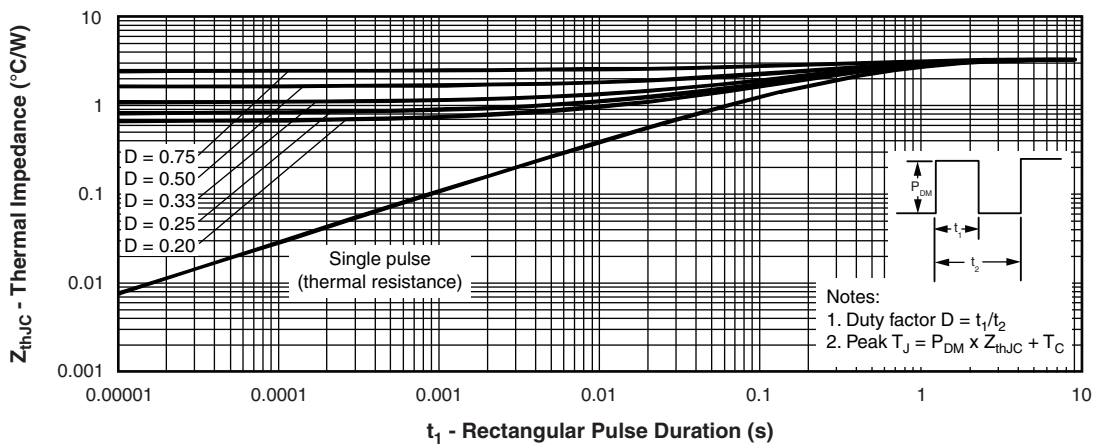


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

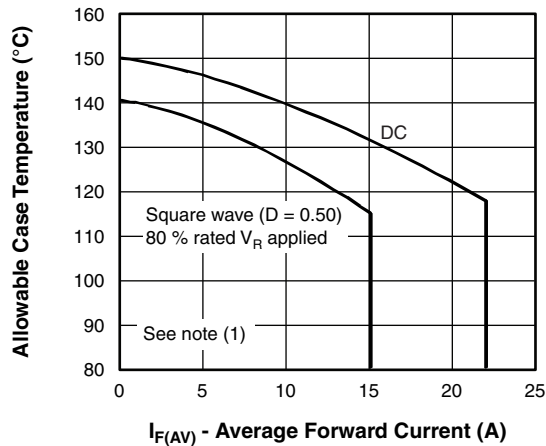


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

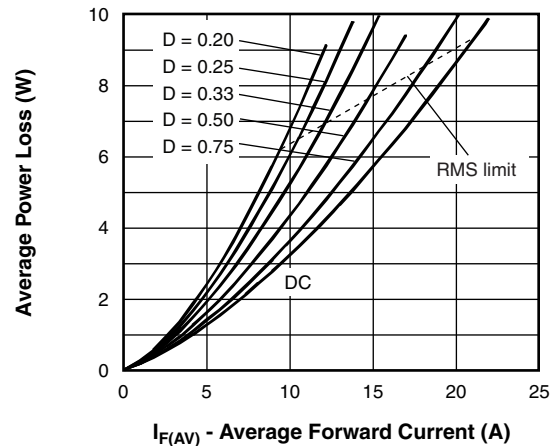


Fig. 6 - Forward Power Loss Characteristics

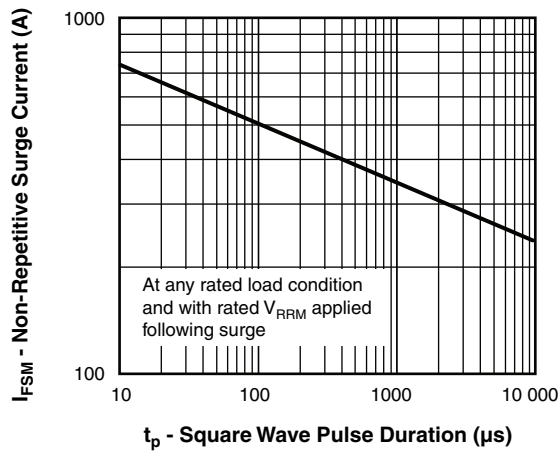


Fig. 7 - Maximum Non-Repetitive Surge Current

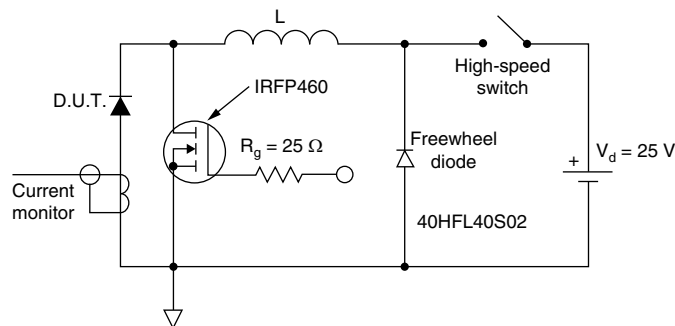


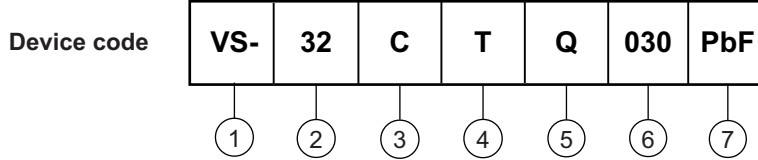
Fig. 8 - Unclamped Inductive Test Circuit

**Note**

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{dREV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80\%$  rated  $V_R$



ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - Current rating (30 A)
- 3** - Circuit configuration:  
C = Common cathode
- 4** - Package:  
T = TO-220
- 5** - Schottky "Q" series
- 6** - Voltage ratings
 

025 = 25 V
030 = 30 V
- 7** - Environmental digit
  - PbF = Lead (Pb)-free and RoHS compliant
  - -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-32CTQ025PbF	50	1000	Antistatic plastic tube
VS-32CTQ025-N3	50	1000	Antistatic plastic tube
VS-32CTQ030PbF	50	1000	Antistatic plastic tube
VS-32CTQ030-N3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS		
Dimensions		<a href="http://www.vishay.com/doc?95222">www.vishay.com/doc?95222</a>
Part marking information	TO-220AB PbF	<a href="http://www.vishay.com/doc?95225">www.vishay.com/doc?95225</a>
	TO-220AB -N3	<a href="http://www.vishay.com/doc?95028">www.vishay.com/doc?95028</a>



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