

Vishay Siliconix

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)		
- 30	0.035 at V _{GS} = - 10 V	- 6.2		
	0.053 at V _{GS} = - 4.5 V	- 5.0		

FEATURES

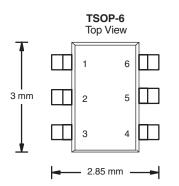
- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

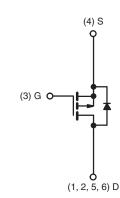
APPLICATIONS

Load Switch



COMPLIANT HALOGEN FREE Available





Ordering Information: Si3483DV-T1-E3 (Lead (Pb)-free) Si3483DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	Γ _A = 25 °C, unle	ss otherwise r	noted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		M	
Gate-Source Voltage		V _{GS}	± 20		V	
Continuous Drain Ourrent (T. 150 %0)	T _A = 25 °C	- I _D	- 6.2	- 4.7		
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T _A = 70 °C		- 4.9	- 3.7		
Pulsed Drain Current		I _{DM}	- 25		А	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1.7 - 0.95			
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	2.0	1.14	W	
	T _A = 70 °C		1.3	0.73		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	$t \le 5 s$	- R _{thJA} R _{thJF}	45	62.5	
Maximum Junction-to-Ambient ^a	Steady State		90	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		25	30	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1.0		- 3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μΑ	
		V_{DS} = - 30 V, V_{GS} = 0 V, T_J = 85 °C			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \leq$ - 5 V, V_{GS} = - 10 V	- 25			А	
Drain-Source On-State Resistance ^a	Р	$V_{GS} = -10$ V, $I_{D} = -6.2$ A		0.028	0.035	0	
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 5.0 A	$V_{GS} = -4.5 \text{ V}, \text{ I}_{\text{D}} = -5.0 \text{ A}$ 0.04		0.053	Ω	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -6.2 \text{ A}$		14		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S}$ = - 1.7 A, $V_{\rm GS}$ = 0 V		- 0.8	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			23	35		
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 10 V, I_D = - 6.2 A		3.6		nC	
Gate-Drain Charge	Q _{gd}			6		1	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		10	15		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ - 1 A, V_{GEN} = - 10 V, R_{g} = 6 Ω		71	110	ns	
Fall Time	t _f			45	70		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		45	70		

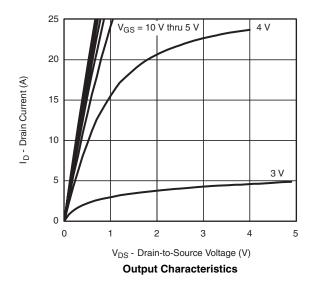
Notes:

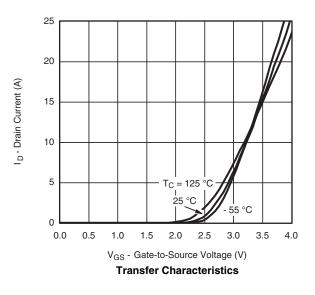
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



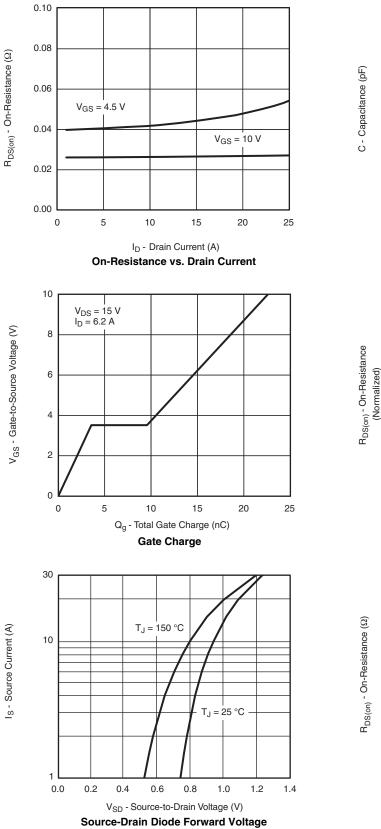


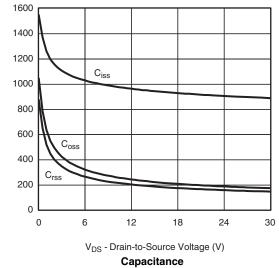


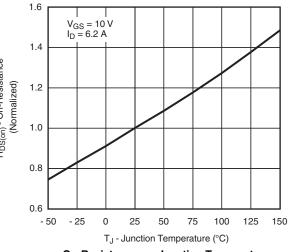
Si3483DV

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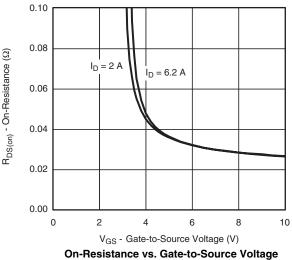
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On-Resistance vs. Junction Temperature

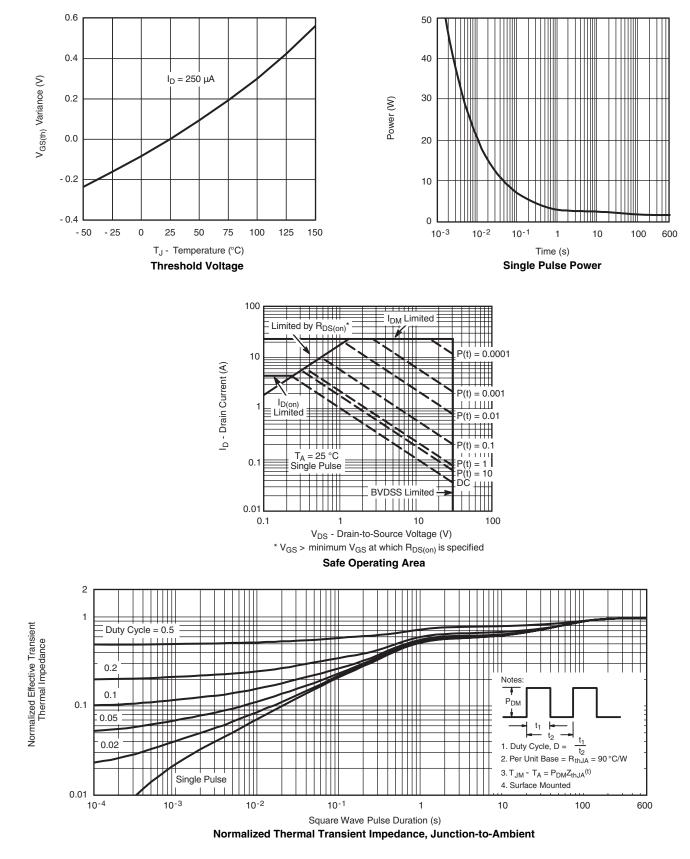


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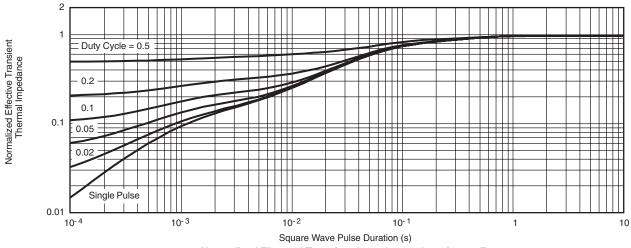




Si3483DV

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72078.



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