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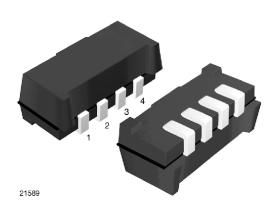
Vishay Semiconductors

RoHS

HALOGEN FREE

GREEN

IR Receiver Modules for Remote Control Systems



MECHANICAL DATA

Pinning:

1, 4 = GND, $2 = V_S$, 3 = OUT

ORDERING CODE

Taping:

TSOP77...WTT - top view taped TSOP77...WTR - side view taped

FEATURES

- Continuous data transmission possible
- Very low supply current
- · Photo detector and preamplifier in one package
- · Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- Improved immunity against ambient light
- Capable of side or top view
- Insensitive to supply voltage ripple and noise
- Low profile 2.35 mm
- Narrow optical filter to reduce interference from plasma TV emissions
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



The TSOP77...W series are miniaturized SMD IR receiver modules for infrared remote control systems. Two PIN diodes and a preamplifier are assembled on a leadframe, the epoxy package contains an IR filter.

The demodulated output signal can be directly connected to a microprocessor for decoding. The TSOP772...W and TSOP774..W are optimized to suppress almost all spurious pulses from energy saving lamps like CFLs. The AGC4 used in the TSOP774..W may suppress some data signals. The TSOP772..W is a legacy product for all common IR remote control data formats. Between these two receiver types, the TSOP774..W is preferred. Customers should initially try the TSOP774..W in their design.

These components have not been qualified according to automotive specifications.

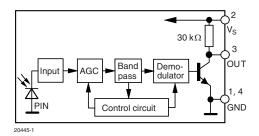
PARTS T	ABLE		
AGC		LEGACY, FOR LONG BURST REMOTE CONTROLS (AGC2)	RECOMMENDED FOR LONG BURST CODES (AGC4) (1)
	30 kHz	TSOP77230W	TSOP77430W
Carrier frequency	33 kHz	TSOP77233W	TSOP77433W
	36 kHz	TSOP77236W	TSOP77436W (2)(3)(4)
	38 kHz	TSOP77238W	TSOP77438W (5)(6)(9)
	40 kHz	TSOP77240W	TSOP77440W
	56 kHz	TSOP77256W	TSOP77456W (7)(8)
Package		Heimdal	I no lens
Pinning		1, 4 = GND, 2	= V _S , 3 = OUT
Dimensions	(mm)	6.8 W x 3.0	H x 2.35 D
Mounting		SN	MD
Application		Remote	control
Best remote	e control code	(2) RC-5 (3) RC-6 (4) Panasonic (5) NEC (6) S	Sharp (7) r-step (8) Thomson RCA (9) r-map

Note

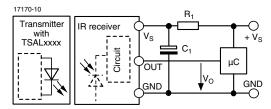
⁽¹⁾ We advise try AGC4 first if the burst length is unknown



BLOCK DIAGRAM



APPLICATION CIRCUIT



The external components R_1 and C_1 are optional to improve the robustness against electrical overstress (typical values are $R_1 = 100 \ \Omega, \ C_1 = 0.1 \ \mu F$).

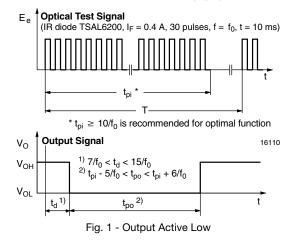
ABSOLUTE MAXIMUM R	ATINGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V _S	-0.3 to +6	V
Supply current		I _S	5	mA
Output voltage		V _O	-0.3 to 5.5	V
Voltage at output to supply		V _S - V _O	-0.3 to (V _S + 0.3)	V
Output current		I _O	5	mA
Junction temperature		T _i	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW
Soldering temperature	t ≤ 10 s, 1 mm from case	T _{sd}	260	°C

Note

• Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND O	PTICAL CHARACTERISTICS (T _{amb} = 25	°C, unles	s otherw	ise speci	fied)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5	-	5.5	V
Supply current	$V_{S} = 5 \text{ V}, E_{v} = 0$	I _{SD}	0.55	0.7	0.9	mA
Supply current	$E_v = 40 \text{ klx, sunlight}$	I _{SH}	-	0.8	-	mA
Transmission distance	$E_v = 0$, IR diode TSAL6200, $I_F = 400$ mA, test signal see fig. 1	d	-	35	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2$, test signal see fig. 1	V _{OSL}	-	-	100	mV
Minimum irradiance	Pulse width tolerance: t_{pi} - 5/ f_o < t_{po} < t_{pi} + 6/ f_o , test signal see fig. 1	E _{e min.}	-	0.4	0.8	mW/m ²
Maximum irradiance	t_{pi} - $5/f_0 < t_{po} < t_{pi} + 6/f_0$, test signal see fig. 1	E _{e max.}	50	-	-	W/m ²
Directivity	Angle of half transmission distance	Φ1/2	-	± 75	-	deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



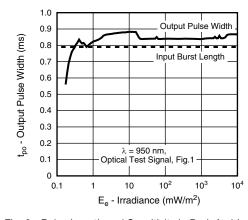


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient



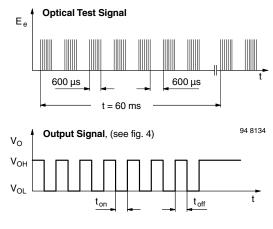


Fig. 3 - Output Function

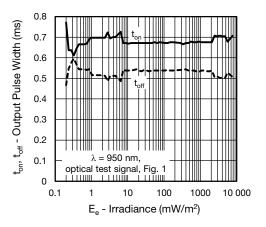


Fig. 4 - Output Pulse Diagram

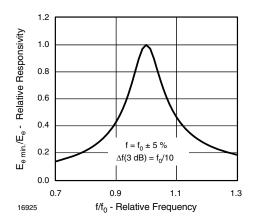


Fig. 5 - Frequency Dependence of Responsivity

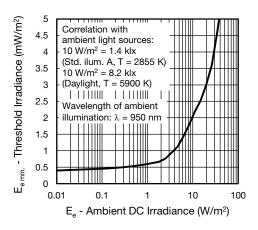
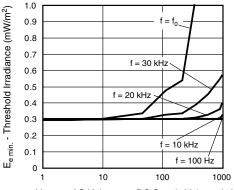


Fig. 6 - Sensitivity in Bright Ambient



 $_{\rm 21394}$ $_{\rm \Delta} \rm Vs_{RMS}$ - AC Voltage on DC Supply Voltage (mV)

Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

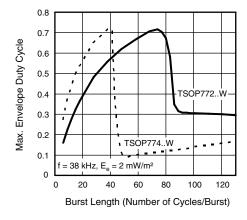


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

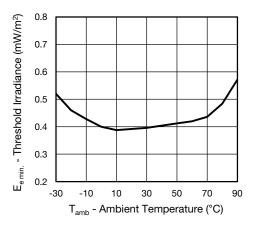


Fig. 9 - Sensitivity vs. Ambient Temperature

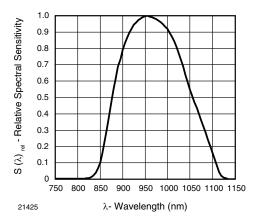


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

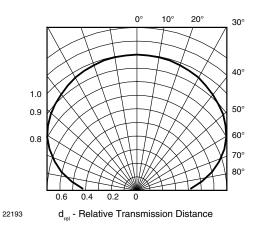


Fig. 11 - Horizontal Directivity

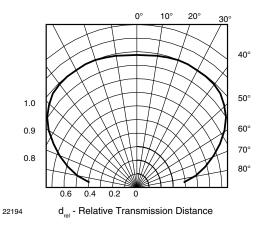


Fig. 12 - Vertical Directivity

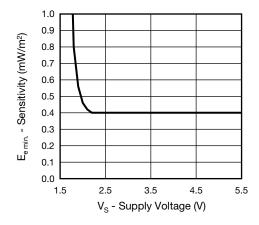


Fig. 13 - Sensitivity vs. Supply Voltage



SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output.

Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see figure 14 or figure 15)

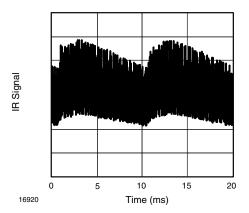


Fig. 14 - IR Disturbance from Fluorescent Lamp with Low Modulation

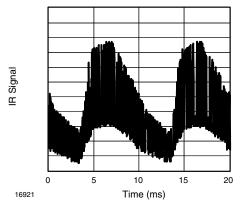


Fig. 15 - IR Disturbance from Fluorescent Lamp with High Modulation

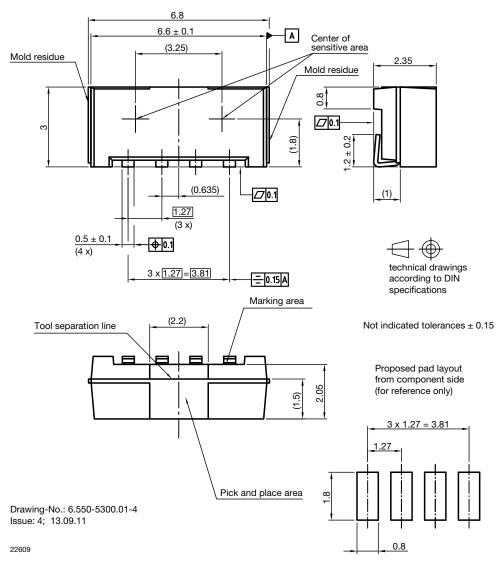
	TSOP772W	TSOP774W
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 12 cycles	10 to 35 cycles ≥ 12 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	800	1300
NEC code	Yes	Preferred
RC5/RC6 code	Yes	Preferred
Thomson 56 kHz code	Yes	Preferred
Sharp code	Yes	Preferred
Suppression of interference from fluorescent lamps	Most common disturbance patterns are suppressed	Even extreme disturbance patterns are suppressed

Notes

- For data formats with short bursts please see the datasheet of TSOP773..W and TSOP775..W
- Best choice of AGC for some popular IR-codes:
 - TSOP77436W: RC-5, RC-6, Panasonic
 - TSOP77438W: NEC, Sharp, r-map
 - TSOP77456W: r-step, Thomson RCA
- For SIRCS 15 and 20 bit, Sony 12 bit IR-codes, please see the datasheet for TSOP75S40FW



PACKAGE DIMENSIONS in millimeters



ASSEMBLY INSTRUCTIONS

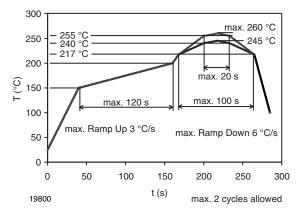
Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

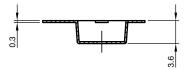
Manual Soldering

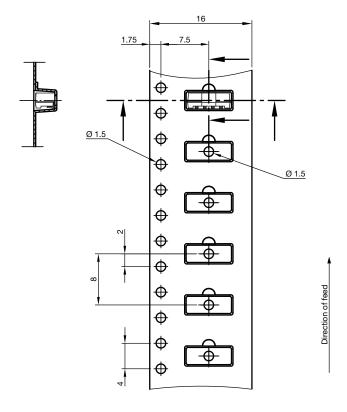
- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



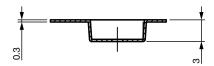
TAPING VERSION TSOP..TR DIMENSIONS in millimeters

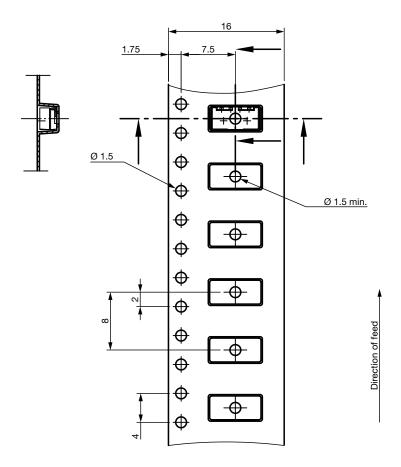




Drawing-No.: 9.700-5342.01-4 Issue: 2; 12.06.13 technical drawings according to DIN specifications

TAPING VERSION TSOP..TT DIMENSIONS in millimeters



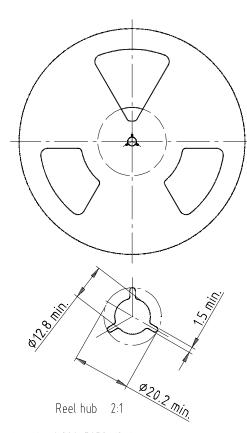


Drawing-No.: 9.700-5341.01-4

Issue: 3; 06.10.15

technical drawings according to DIN specifications

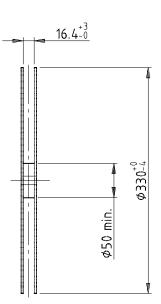
REEL DIMENSIONS in millimeters



Drawing-No.: 9.800-5052.V2-4

Issue: 1; 07.05.02

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Form of the leave open of the wheel is supplier specific.

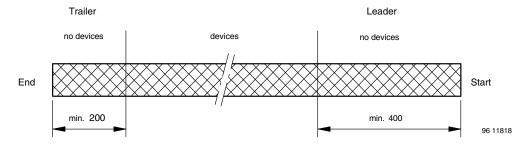
Dimension acc. to IEC EN 60 286-3

Tape width 16



technical drawings according to DIN specifications

LEADER AND TRAILER DIMENSIONS in millimeters



COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 ± 10 mm/min. 165° to 180° peel angle

LABEL

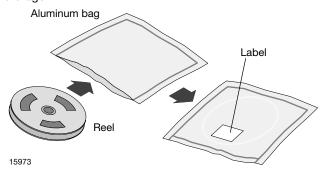
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	PCK	-	
Mixed code indicator	MIXED CODE	-	
Origin	xxxxxxx+	Company logo	
Long bar code top	Туре	Length	
Item-number	N	8	
Plant-code	N	2	
Sequence-number	X	3	
Quantity	N	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	X	3	
Data-code	N	3	
Batch-number	X	10	
Filter	-	1	
Total length	-	17	

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKINGFINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

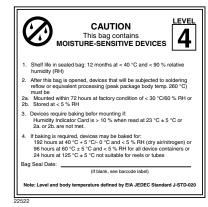
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC $^{\circledR}$ standard J-STD-020 level 4 label is included on all dry bags.



EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags



TSOP772..W, TSOP774..W

Vishay Semiconductors

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

BAR CODE PRODUCT LABEL (example)



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Legal Disclaimer Notice

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Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000