Ultralow V_F Ultrafast Rectifier, 5 A FRED Pt[®]



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TO-252AA (D-PAK)

PRODUCT SUMMARY								
Package	TO-252AA (D-PAK)							
I _{F(AV)}	5 A							
V _R	600 V							
V _F at I _F	0.85 V							
t _{rr} (typ.)	59 ns							
T _J max.	175 °C							
Diode variation	Single die							

FEATURES

- Ultrafast recovery time, extremely low V_F and soft recovery
- 175 °C maximum operating junction temperature
- For PFC DCM operation
- Low leakage current
- HALOGEN Meets MSL level 1, per J-STD-020, LF maximum FREE peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS inverters or as freewheeling diodes. Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL TEST CONDITIONS									
Peak repetitive reverse voltage	V _{RRM}		600	V						
Average rectified forward current	I _{F(AV)}	T _C = 159 °C	5							
Non-repetitive peak surge current	I _{FSM}	$T_J = 25 \ ^{\circ}C$	80	А						
Peak repetitive forward current	I _{FM}	$T_{C} = 159 \text{ °C}, f = 20 \text{ kHz}, d = 50 \%$	10							
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-				
Forward voltage	VF	I _F = 5 A	-	0.97	1.20	V			
	۷F	I _F = 5 A, T _J = 150 °C	-	0.85	1.0				
	I _R	$V_{R} = V_{R}$ rated	-	-	5				
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$		125	μA				
Junction capacitance	CT	V _R = 600 V	-	3.5	-	pF			
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH			

Revision: 07-Jul-15

Document Number: 93250

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RoHS

COMPLIANT





DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 $^{\circ}$ C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time		$I_F = 1 \text{ A}, dI_F/dt = 10$	00 A/µs, V _R = 30 V	-	59	70			
	+	$I_F = 1 \text{ A}, dI_F/dt = 50$	-	75	-				
	t _{rr}	T _J = 25 °C		-	145	-	ns A		
		T _J = 125 °C	l _F = 5 A dl _F /dt = 200 A/µs V _R = 390 V	-	203	-			
Poak rocovary ourrant	I _{RRM}	T _J = 25 °C		-	12.6	-			
Peak recovery current		T _J = 125 °C		-	14.7	-			
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	931	-	50		
		T _J = 125 °C		-	1480	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C				
Thermal resistance, junction to case per leg	R _{thJC}		-	-	3	°C/W				
Approximate weight				0.3		g				
Approximate weight				0.01		oz.				
Marking device		Case style TO-252AA (D-PAK)	5EWL06FN							

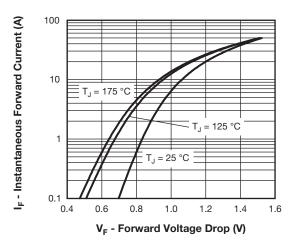


Fig. 1 - Typical Forward Voltage Drop Characteristics

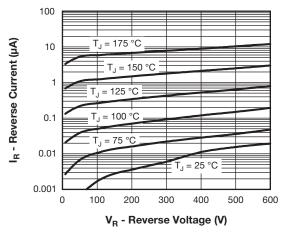


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



VS-5EWL06FN-M3

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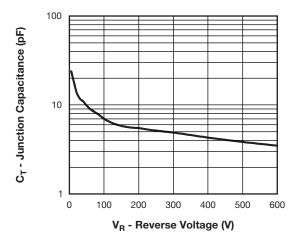


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

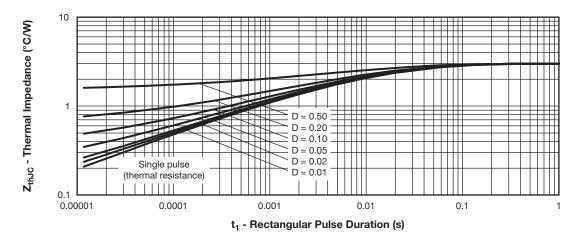
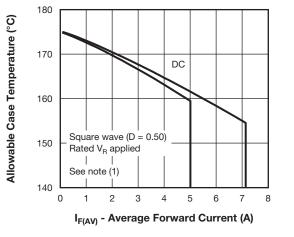
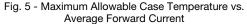


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





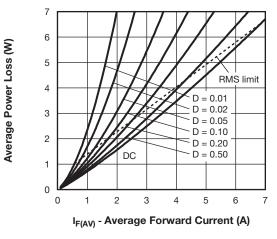


Fig. 6 - Forward Power Loss Characteristics

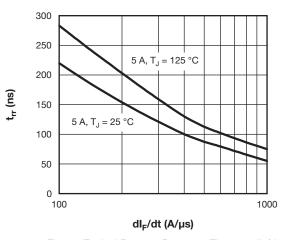
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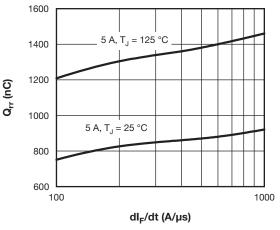
Fig. 7 - Typical Reverse Recovery Time vs. $dI_{\mbox{\scriptsize F}}/dt$

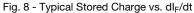
Note

SHA

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$





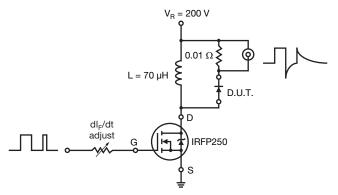
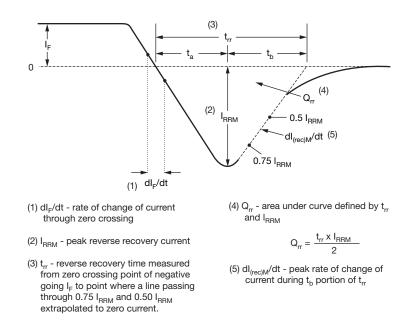
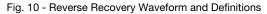


Fig. 9 - Reverse Recovery Parameter Test Circuit





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ORDERING INFORMATION TABLE

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Device code	VS-	5	Е	w	L	06	FN	TRL	-M3
	V3-	5		~~		00			
		2	3	4	5	6	7	8	9
	1 -	- Visł	nay Sem	niconduc	ctors pro	oduct			
	2 -	Cur	rent rati	ng (5 =	5 A)				
	3 -	Circ	uit conf	iguratior	ו:				
		E =	single c	liode					
	4 -	Pac	kage id	entifier:					
		W =	D-PAK	,					
	5 -	L =	low V _F ,	fast rec	overy				
	6 -	· Volt	age rati	ng (06 =	= 600 V)				
	7 -	FN	= TO-25	52AA					
	8 -	• N	one = tu	lbe					
		• TI	R = tape	e and ree	el				
		• TI	RL = tap	e and re	eel (left	orienteo	d)		
		• TI	RR = tap	be and r	eel (righ	nt orient	ed)		
	9 -	Env	ironmer	ntal digit	:				
		-M3	= halog	gen-free	, RoHS-	complia	ant and	termina	tions le

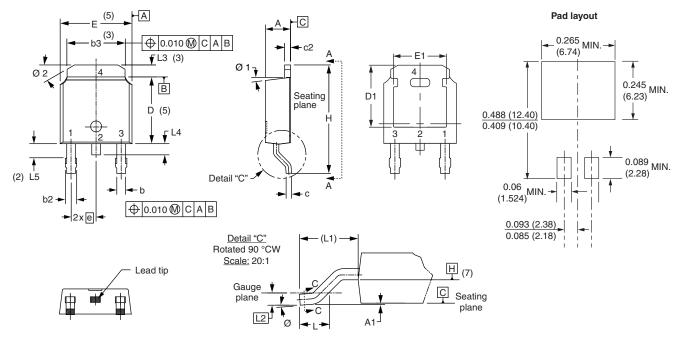
ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-5EWL06FN-M3	75	3000	Antistatic plastic tube						
VS-5EWL06FNTR-M3	2000	2000	13" diameter reel						
VS-5EWL06FNTRL-M3	3000	3000	13" diameter reel						
VS-5EWL06FNTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS								
Dimensions	www.vishay.com/doc?95016							
Part marking information	www.vishay.com/doc?95176							
Packaging information	www.vishay.com/doc?95033							
SPICE model	www.vishay.com/doc?95217							



D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS		INCHES		NOTES		MILLIN	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51 BSC		0.020 BSC		
с	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

⁽⁴⁾ Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC outline TO-252AA

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