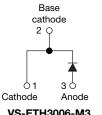
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Vishay Semiconductors

Hyperfast Rectifier, 30 A FRED Pt[®]



2L TO-220AC



2L TO-220 FULL-PAK



VS-ETH3006-M3

VS-ETH3006FP-M3

PRODUCT SUMMARY					
Package	2L TO-220AC, 2L TO-220FP				
I _{F(AV)}	30 A				
V _R	600 V				
V _F at I _F	1.4 V				
t _{rr} (typ.)	27 ns				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES

- · Hyperfast soft recovery time
- · Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{BMS})
- True 2 pin package
- · Designed and qualified according to JEDEC[®]-JESD 47
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage	V _{RRM}		600	V		
Average rectified forward current in DC	levus.	T _C = 131 °C	- 30	A		
FULL-PAK	IF(AV)	T _C = 51 °C	30			
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	180			
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MAX.					
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-		
Forward voltage	V _F	I _F = 30 A	-	2.0	2.65	2.65 V	
		I _F = 30 A, T _J = 150 °C	-	1.4	1.8		
Reverse leakage current I _R -		$V_{R} = V_{R}$ rated	-	0.02	30		
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	50	300	μA	
Junction capacitance	CT	V _R = 600 V	-	20	-	pF	
Series inductance	LS	Measured lead to lead 5 mm from package body	-	8	-	nH	

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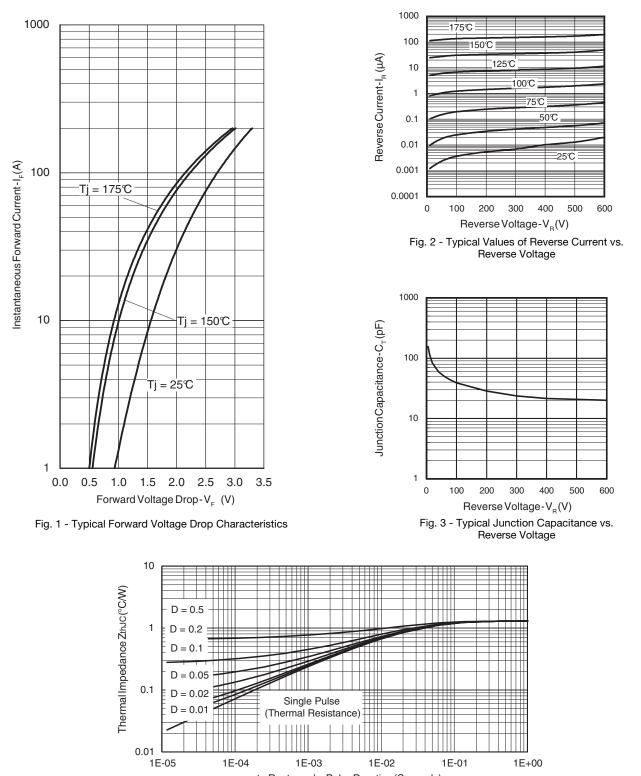
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DYNAMIC RECOVERY CHARACTERISTICS (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t =$	1 A, dI _F /dt = 50 A/µs, V _R = 30 V		26	35	
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 30 A dI _F /dt = 200 A/μs V _R = 200 V	-	26	-	ns
		T _J = 125 °C		-	70	-	
Pook receivery ourrent		T _J = 25 °C		-	3.5	-	A
Peak recovery current	I _{RRM}	T _J = 125 °C		-	7.6	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	50	-	nC
		T _J = 125 °C		-	280	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance,	D		-	0.84	1.3		
junction to case FULL-PAK	R _{thJC}		-	3.2	3.8		
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-		
Mainht			-	2	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)	
Marking davias		Case style 2L TO-220AC	ETH3006				
Marking device		Case style 2L TO-220 FULL-PAK		ETH3	006FP		

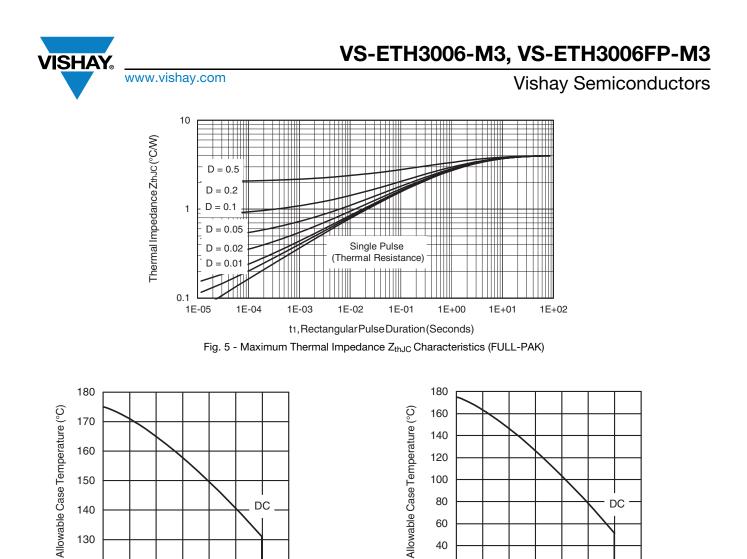


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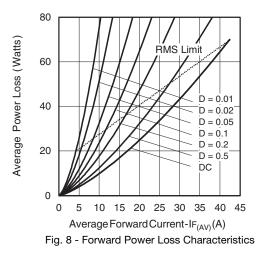
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 Average Forward Current - $IF_{(AV)}(A)$ Fig. 7 - Maximum Allowable Case Temperature vs.

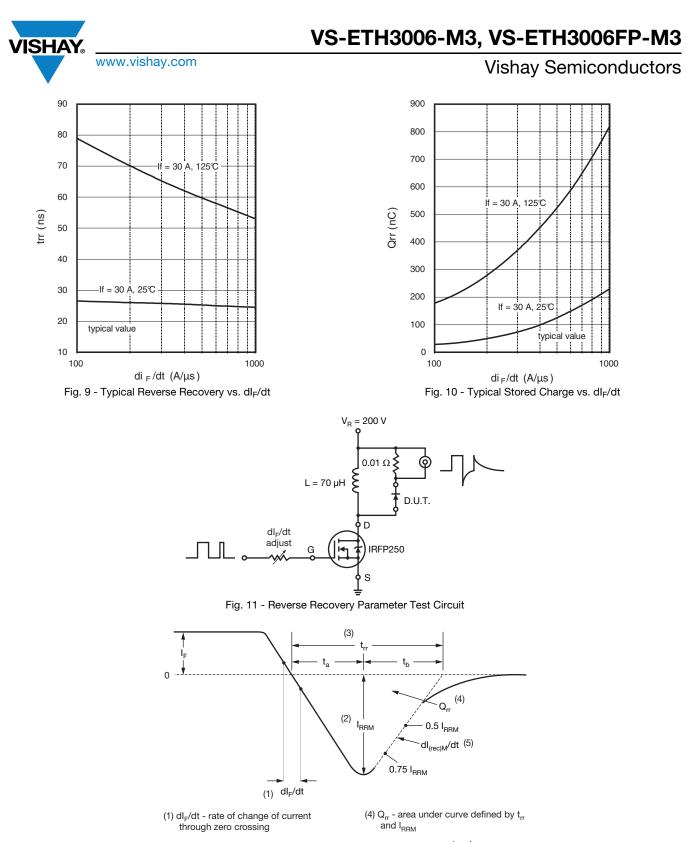
Average Forward Current (FULL-PAK)



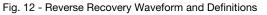
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Average Forward Current - $IF_{(AV)}(A)$

Fig. 6 - Maximum Allowable Case Temperature vs. Average Forward Current



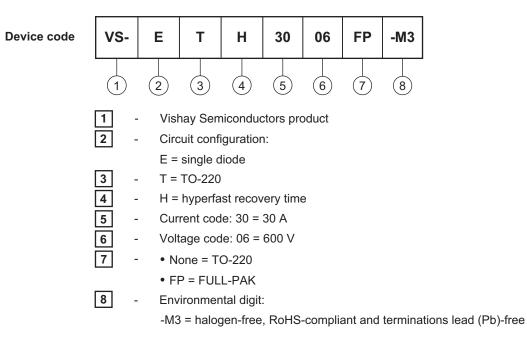
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (5) dI_{(rec)M}/dt peak rate of change of current during $t_{\rm b}$ portion of $t_{\rm rr}$





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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPT						
VS-ETH3006-M3	50	1000	Antistatic plastic tube			
VS-ETH3006FP-M3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	2L TO-220AC	www.vishay.com/doc?95259			
Dimensions	2L TO-220 FULL-PAK	www.vishay.com/doc?95260			
Part marking information	2L TO-220AC	www.vishay.com/doc?95391			
	2L TO-220 FULL-PAK	www.vishay.com/doc?95392			

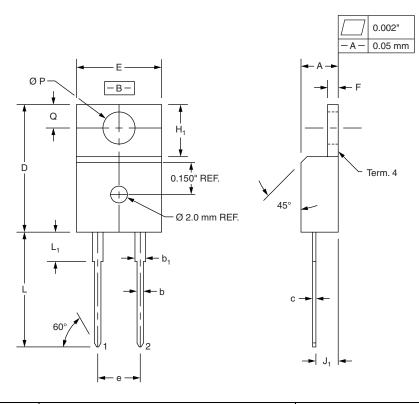




Din TO 220

True 2 Pin TO-220

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCH	ES
STMBOL	MIN.	MAX.	MIN.	MAX.
A	4.32	4.57	0.170	0.180
b	0.71	0.91	0.028	0.036
b ₁	1.15	1.39	0.045	0.055
с	0.36	0.53	0.014	0.021
D	14.99	15.49	0.590	0.610
E	10.04	10.41	0.395	0.410
e	5.08	BSC	0.200 E	BSC
F	1.22	1.37	0.048	0.054
H ₁	5.97	6.47	0.235	0.255
J ₁	2.54	2.79	0.100	0.110
L	13.47	13.97	0.530	0.550
L ₁ ⁽¹⁾	3.31	3.81	0.130	0.150
Ø P	3.79	3.88	0.149	0.153
Q	2.60	2.84	0.102	0.112

Notes

 $^{\left(1\right)}$ Lead dimension and finish uncontrolled in L_{1}

• These dimensions are within allowable dimensions of JEDEC TO-220AB rev. J outline dated 3-24-87

Controling dimension: Inch

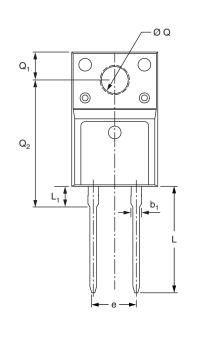


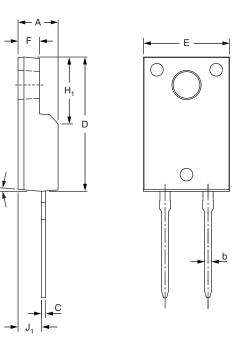


True 2 Pin TO-220 FULL-PAK

θ

DIMENSIONS in millimeters and inches





SYMBOL	MILLIN	METERS	INC	HES
STMDOL	MIN.	MAX.	MIN.	MAX.
A	4.53	4.93	0.178	0.194
b	0.71	0.91	0.028	0.036
b ₁	1.15	1.39	0.045	0.055
С	0.36	0.53	0.014	0.021
D	15.67	16.07	0.617	0.633
E	9.96	10.36	0.392	0.408
е	5.08	typical	0.200	typical
F	2.34	2.74	0.092	0.107
H ₁	6.50	6.90	0.256	0.272
J ₁	2.56	2.96	0.101	0.117
L	12.78	13.18	0.503	0.519
L ₁	2.23	2.63	0.088	0.104
ØQ	2.98	3.38	0.117	0.133
Q ₁	3.10	3.50	0.122	0.138
Q ₂	14.80	15.20	0.583	0.598
θ	0°	5°	0°	5°

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