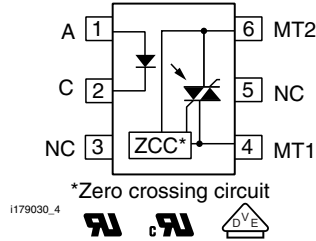
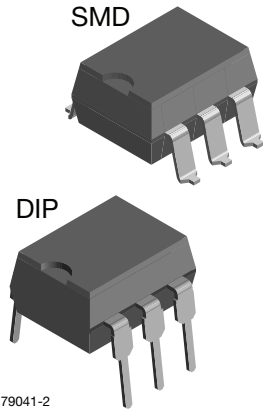


## Phototriac, Zero Crossing, 1.5 kV/μs dV/dt, 600 V



### FEATURES

- 1500 V/μs dV/dt minimum
- 600 V blocking voltage
- 100 mA on-state current
- Zero crossing detector
- Low input trigger current
- 6 pin DIP package
- Material categorization: For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT

### APPLICATIONS

- Household appliances
- Triac drive/AC motor drives
- Solenoid/valve controls
- Office automation equipment/machine
- Temperature (HVAC)/lighting controls
- Switching power supply

### AGENCY APPROVALS

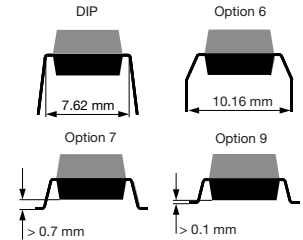
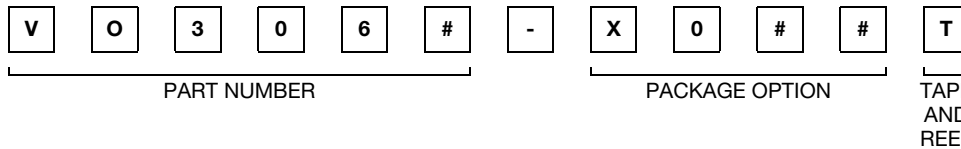
- UL1577, file no. E52744 system code H
- cUL - file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5) available with option 1

### DESCRIPTION

The VO3062 and VO3063 triac driver family consists of a GaAs infrared LED optically coupled to a monolithic photosensitive zero crossing triac detector chip.

The 600 V blocking voltage permits control of off-line voltages up to 240 V<sub>AC</sub>, with a safety factor of more than two, and is sufficient for as much as 380 V.

### ORDERING INFORMATION



AGENCY CERTIFIED/PACKAGE	TRIGGER, CURRENT I <sub>FT</sub> (mA)	
	5	10
<b>UL, cUL</b>		
DIP-6	VO3063	VO3062
DIP-6, 400 mil, option 6	VO3063-X006	VO3062-X006
SMD-6, option 7	VO3063-X007T	VO3062-X007T
SMD-6, option 9	VO3063-X009T	-
<b>VDE, UL, cUL</b>		
DIP-6, 400 mil, option 6	VO3063-X016	VO3062-X016
SMD-6, option 7	VO3063-X017T	VO3062-X017T

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
<b>INPUT</b>					
Reverse voltage			$V_R$	6	V
Forward current - continuous			$I_F$	60	mA
Power dissipation			$P_{diss}$	100	mW
<b>OUTPUT</b>					
Off state output terminal voltage		VO3062, VO3063	$V_{DRM}$	600	V
Peak non-repetitive surge current	PW = 100 $\mu$ s, 120 pps		$I_{TSM}$	1	A
Power dissipation			$P_{diss}$	200	mW
On-state RMS current			$I_{T(RMS)}$	100	mA
<b>COUPLER</b>					
Isolation test voltage	t = 1 s		$V_{ISO}$	5300	$V_{RMS}$
Total power dissipation			$P_{tot}$	300	mW
Operating temperature range			$T_{amb}$	- 55 to + 100	$^{\circ}\text{C}$
Storage temperature range			$T_{stg}$	- 55 to + 150	$^{\circ}\text{C}$
Soldering temperature <sup>(1)</sup>	maximum $\leq$ 10 s		$T_{slid}$	260	$^{\circ}\text{C}$

**Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP) "Assembly Instructions" ([www.vishay.com/doc?80054](http://www.vishay.com/doc?80054)).

<b>THERMAL CHARACTERISTICS</b>				
PARAMETER	SYMBOL	VALUE	UNIT	
Maximum LED junction temperature	$T_{jmax}$	125	$^{\circ}\text{C}$	<p>The diagram shows a thermal network for the device. It includes nodes for ambient temperature (<math>T_A</math>), case temperature (<math>T_C</math>), junction detector temperature (<math>T_{JD}</math>), junction emitter temperature (<math>T_{JE}</math>), and board temperature (<math>T_B</math>). Thermal resistances are indicated by resistors: <math>\theta_{CA}</math> (case to ambient), <math>\theta_{CB}</math> (case to board), <math>\theta_{DB}</math> (junction detector to board), <math>\theta_{DC}</math> (junction detector to case), <math>\theta_{EB}</math> (junction emitter to board), <math>\theta_{EC}</math> (junction emitter to case), <math>\theta_{DE}</math> (junction detector to emitter), and <math>\theta_{BE}</math> (board to emitter). A dashed box labeled 'Package' encloses the junction detector and emitter nodes.</p>
Maximum output die junction temperature	$T_{jmax}$	125	$^{\circ}\text{C}$	
Thermal resistance, junction emitter to board	$\theta_{JEB}$	150	$^{\circ}\text{C}/\text{W}$	
Thermal resistance, junction emitter to case	$\theta_{JEC}$	139	$^{\circ}\text{C}/\text{W}$	
Thermal resistance, junction detector to board	$\theta_{JDB}$	78	$^{\circ}\text{C}/\text{W}$	
Thermal resistance, junction detector to case	$\theta_{JDC}$	103	$^{\circ}\text{C}/\text{W}$	
Thermal resistance, junction emitter to junction detector	$\theta_{JED}$	496	$^{\circ}\text{C}/\text{W}$	
Thermal resistance, case to ambient	$\theta_{CA}$	3563	$^{\circ}\text{C}/\text{W}$	

**Note**

- The thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note.



<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>INPUT</b>							
Reverse current	$V_R = 6\text{ V}$		$I_R$			10	$\mu\text{A}$
Forward voltage	$I_F = 30\text{ mA}$		$V_F$		1.2	1.5	V
<b>OUTPUT</b>							
Leakage with LED off, either direction	$V_{DRM} = 600\text{ V}$		$I_{DRM}$		10	500	nA
Critical rate of rise off-state voltage	$V_D = 400\text{ V}$		dV/dt	1500	2000		V/ $\mu\text{s}$
<b>COUPLER</b>							
LED trigger current, current required to latch output		VO3063	$I_{FT}$			5	mA
		VO3062	$I_{FT}$			10	mA
Peak on-state voltage, either direction	$I_{TM} = 100\text{ mA Peak}$ , $I_F = \text{Rated } I_{FT}$		$V_{TM}$		1.7	3	V
Holding current, either direction			$I_H$		200		$\mu\text{A}$
Inhibit voltage (MT1-MT2 voltage above which device will not trigger)			$V_{INH}$		12	22	V
Leakage in inhibited state	$I_F = 10\text{ mA maximum}$ , at rated $V_{DRM}$ , off state		$V_{DRM2}$		250	1000	$\mu\text{A}$

**Note**

- Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

<b>SAFETY AND INSULATION RATINGS</b>						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification (according to IEC 68 part 1)			55/100/21			
Pollution degree (DIN VDE 0109)			2			
Comparative tracking index	CTI	175				
Peak transient overvoltage	$V_{IOTM}$	8000			$V_{peak}$	
Peak working insulation voltage	$V_{IORM}$	890			$V_{peak}$	
Isolation resistance at $T_{amb} = 100\text{ }^{\circ}\text{C}$ , $V_{DC} = 500\text{ V}$	$R_{IO}$	$10^{11}$			$\Omega$	
Isolation resistance at $T_{amb} = 25\text{ }^{\circ}\text{C}$ , $V_{DC} = 500\text{ V}$	$R_{IO}$	$10^{12}$			$\Omega$	
Partial discharge test voltage (method a, $V_{pd} = V_{IORM} \times 1.875$ )	$V_{pd}$	1325			$V_{peak}$	
Safety rating - power	$P_{SO}$			400	mW	
Safety rating - input current	$I_{SI}$			150	mA	
Safety rating - temperature	$T_{SI}$			165	$^{\circ}\text{C}$	
Clearance distance (Standard DIP-6)		7			mm	
Creepage distance (Standard DIP-6)		7			mm	
Clearance distance (400 mil DIP-6)		8			mm	
Creepage distance (400 mil DIP-6)		8			mm	

**Note**

- According to DIN EN60747-5-5 (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

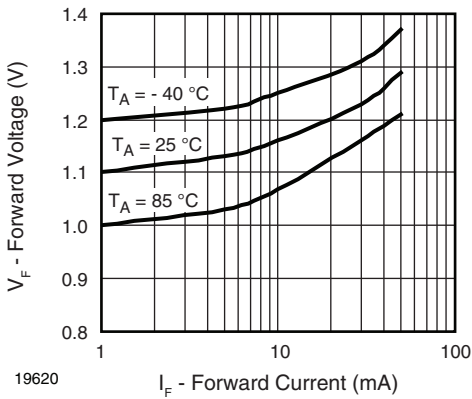


Fig. 1 - Forward Voltage vs. Forward Current

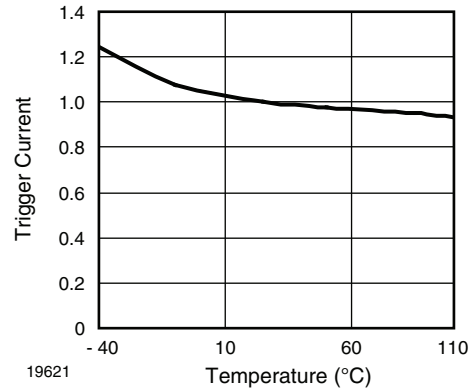


Fig. 4 - Normalized Trigger Current vs. Temperature

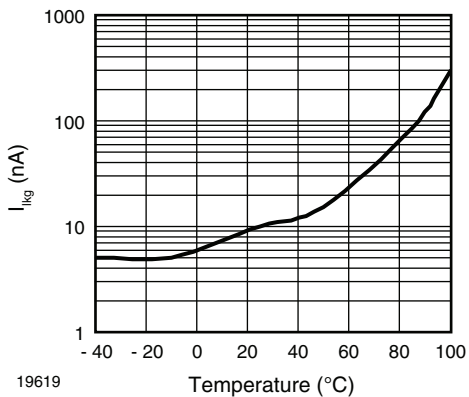


Fig. 2 - Off-State Leakage Current vs. Temperature

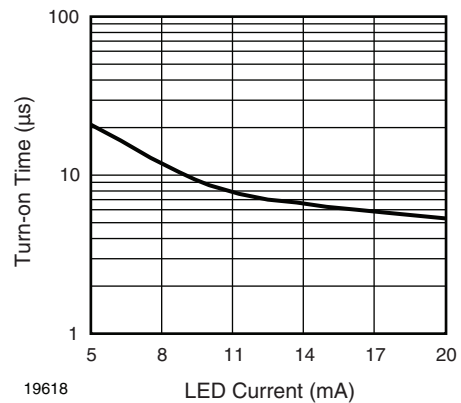


Fig. 5 - Turn-on Time vs. LED Current

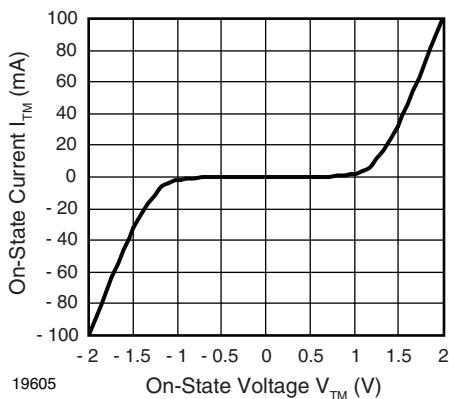


Fig. 3 - On-State Current vs.  $V_{TM}$

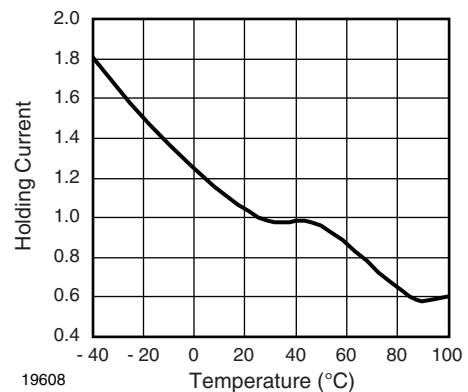


Fig. 6 - Normalized Holding Current vs. Temperature

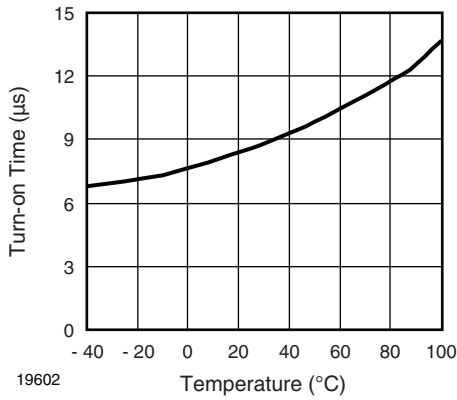


Fig. 7 - Turn-on Time vs. Temperature

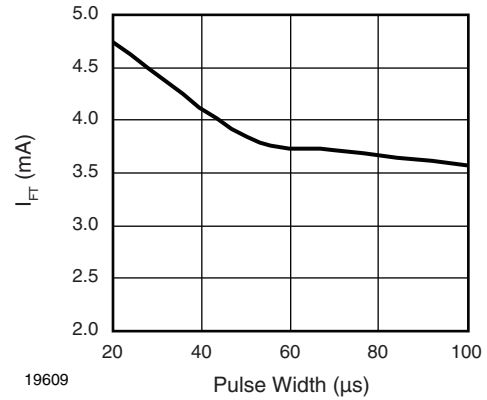
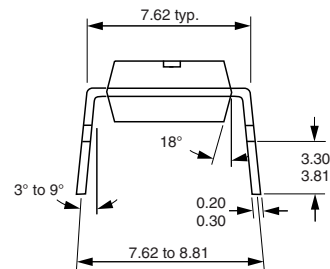
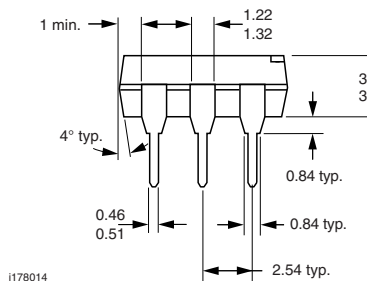
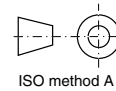
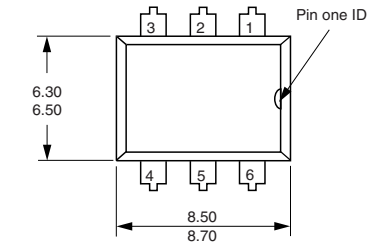
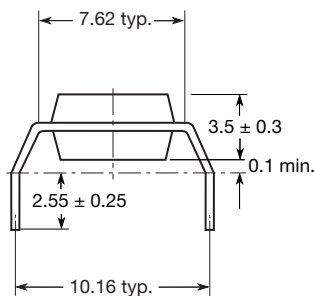


Fig. 8 - Trigger Current vs. Pulse Width

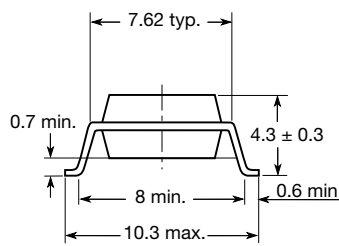
**PACKAGE DIMENSIONS** in millimeters



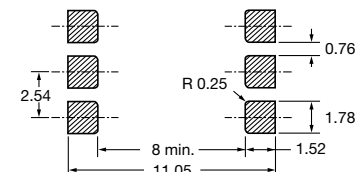
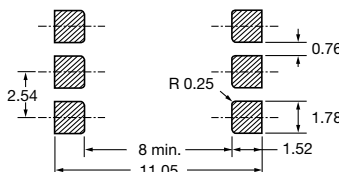
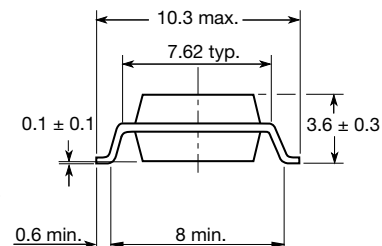
**Option 6**



**Option 7**



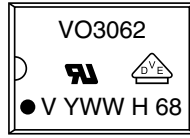
**Option 9**



20802-24



**PACKAGE MARKING** (example)



**Notes**

- The VDE logo is only marked on option1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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