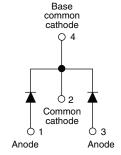


Schottky Rectifier, 2 x 3.5 A



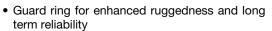


D-PAK	(TO-252AA)
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PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I _{F(AV)}	2 x 3.5 A					
V_{R}	30 V					
V _F at I _F	See Electrical table					
I _{RM}	50 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	8 mJ					

FEATURES

• Low forward voltage drop





- Halogen-free according to IEC 61249-2-21
- definition
- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTION

The VS-6CWQ03FN-M3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES UNITS									
I _{F(AV)}	Rectangular waveform	7	А						
V _{RRM}		30	V						
I _{FSM}	t _p = 5 μs sine	535	Α						
V _F	3 Apk, T _J = 125 °C (per leg)	0.35	V						
TJ	Range	- 40 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-6CWQ03FN-M3	UNITS					
Maximum DC reverse voltage	V_{R}	30	V					
Maximum working peak reverse voltage	V_{RWM}	30	V					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average forward current	o per log		50 % distribution of T = 404 °C master and accordance		3.5				
See fig. 5	per device	I _{F(AV)}	50 % duty cycle at T _C = 134 °C, rectangular waveform		7	Α			
Maximum peak one cycle		_	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	535	A			
non-repetitive surge current See fig. 7	per leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	90				
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4 mH		8	mJ			
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А			

Schottky Rectifier, 2 x 3.5 A



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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS			
		3 A	T _{.1} = 25 °C	0.45				
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	6 A	11=23 0	0.52	V			
See fig. 1	V FM (*)	3 A	T _{.1} = 125 °C	0.35	V			
		6 A	1J = 125 C	0.46				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	$T_J = 25 ^{\circ}\text{C}$ $V_B = \text{Rated } V_B$		2	mA			
See fig. 2	I 'RM`'	T _J = 125 °C	VR = Nated VR	50	111/4			
Threshold voltage	V _{F(TO)}	$T_1 = T_1 \text{ maximum}$		0.22	V			
Forward slope resistance	r _t	ij = ij maximum		32.86	mΩ			
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} , (test signal ran	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C					
Typical series inductance per leg	L _S	Measured lead to lead 5 m	5.0	nΗ				
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs			

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C				
Maximum thermal resistance,	per leg	D	DC operation	4.7	°C/W				
junction to case	per device	R_{thJC}	See fig. 4	2.35	C/ VV				
Approximate weight				0.3	g				
Approximate weight				0.01	OZ.				
Marking device			Case style D-PAK (similar to TO-252AA)	6CWC	03FN				

Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink



Schottky Rectifier, 2 x 3.5 A

Vishay Semiconductors

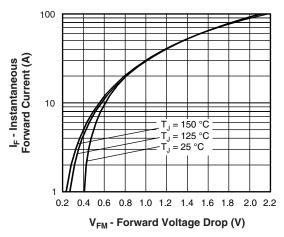


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

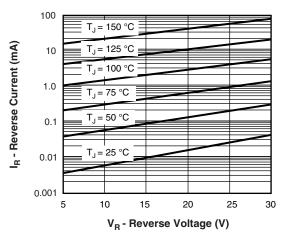


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

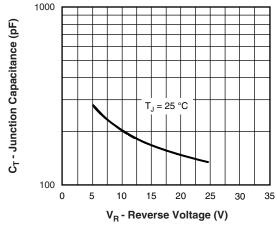


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

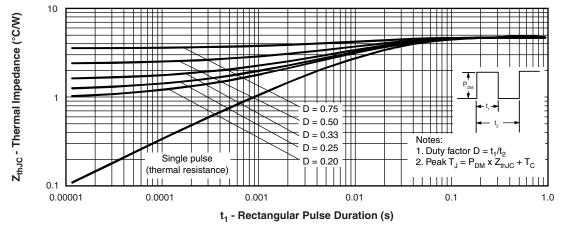


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

Schottky Rectifier, 2 x 3.5 A



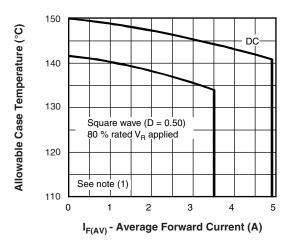


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

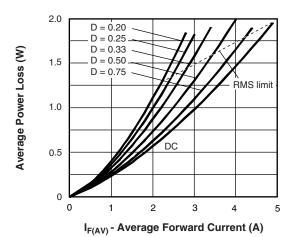


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

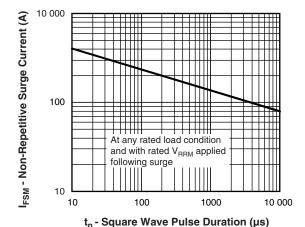


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

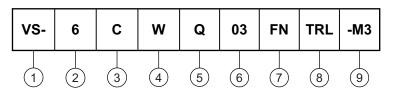
(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

Schottky Rectifier, 2 x 3.5 A

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (7 A)

Center tap configuration

4 - Package identifier:

W = D-PAK

5 - Schottky "Q" series

6 - Voltage rating (03 = 30 V)

7 - FN = TO-252AA

8 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

9 - Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-6CWQ03FN-M3	75	3000	Antistatic plastic tube						
VS-6CWQ03FNTR-M3	2000	2000	13" diameter reel						
VS-6CWQ03FNTRL-M3	3000	3000	13" diameter reel						
VS-6CWQ03FNTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95016</u>					
Part marking information	www.vishay.com/doc?95176				
Packaging information	www.vishay.com/doc?95033				
SPICE model	www.vishay.com/doc?95437				



INCHES

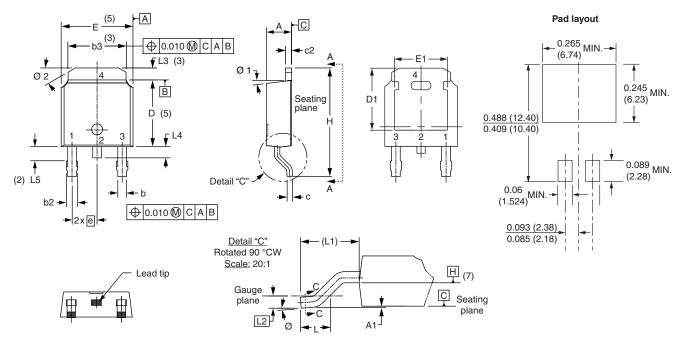
MIN.

MAX.

NOTES

D-PAK (TO-252AA)

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	STIVIBOL	MIN.	MAX.	
Α	2.18	2.39	0.086	0.094		е	2.29	BSC	
A1	-	0.13	-	0.005		Н	9.40	10.41	
b	0.64	0.89	0.025	0.035		L	1.40	1.78	
b2	0.76	1.14	0.030	0.045		L1	2.74	BSC	
b3	4.95	5.46	0.195	0.215	3	L2	0.51	BSC	
С	0.46	0.61	0.018	0.024		L3	0.89	1.27	
c2	0.46	0.89	0.018	0.035		L4	-	1.02	
D	5.97	6.22	0.235	0.245	5	L5	1.14	1.52	
D1	5.21	-	0.205	-	3	Ø	0°	10°	
Е	6.35	6.73	0.250	0.265	5	Ø1	0°	15°	
E1	4.32	-	0.170	-	3	Ø2	25°	35°	

е	e 2.29 BSC 0.090 BSC		BSC		
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	BSC	0.108	REF.	
L2	0.51	BSC	0.020	BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	·
Ø2	25°	35°	25°	35°	·
	H L1 L2 L3 L4 L5 Ø	H 9.40 L 1.40 L1 2.74 L2 0.51 L3 0.89 L4 - L5 1.14 Ø 0° Ø1 0°	H 9.40 10.41 L 1.40 1.78 L1 2.74 BSC L2 0.51 BSC L3 0.89 1.27 L4 - 1.02 L5 1.14 1.52 Ø 0° 10° Ø1 0° 15°	H 9.40 10.41 0.370 L 1.40 1.78 0.055 L1 2.74 BSC 0.108 L2 0.51 BSC 0.020 L3 0.89 1.27 0.035 L4 - 1.02 - L5 1.14 1.52 0.045 Ø 0° 10° 0° Ø1 0° 15° 0°	H 9.40 10.41 0.370 0.410 L 1.40 1.78 0.055 0.070 L1 2.74 BSC 0.108 REF. L2 0.51 BSC 0.020 BSC L3 0.89 1.27 0.035 0.050 L4 - 1.02 - 0.040 L5 1.14 1.52 0.045 0.060 Ø 0° 10° 0° 10° Ø1 0° 15° 0° 15°

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



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Vishay

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