

Midas Components Limited
Electra House
32 Southtown Road
Great Yarmouth
Norfolk
NR31 0DU
England

Telephone Fax Email Website +44 (0)1493 602602 +44 (0)1493 665111 sales@midasdisplays.com www.midasdisplays.com

	Specification							
Part	MCOT256064BA-GM							
Number:	IVICO I 230004DA-GIVI							
Version:								
Date:								



Midas Displays OLED Part Number System

MCO	2	21605	A 4	E	V	-	-	VV O	1	10
1	2	3	4	5	6		/	8	9	10
1 =	MCO:		Midas D	isplays OLE	D					

B: COB (Chip on Board) **T**: TAB (Taped Automated Bonding)

3 = No of dots: (e.g. $240064 = 240 \times 64 \text{ dots}$) (e.g. $21605 = 2 \times 16 \text{ 5mm C.H.}$)

4 = **Series** A to Z

Blank:

2

5 = **Series Variant**: A to Z and 1 to 9 – see addendum

6 = Operating Temp Range: A: -30+85° C V: -40+80° C Y: -40 +70° C Z: -30+70° C

X: -40 +85° C

7 = Character Set: Blank: Not Applicable

E: Multi European Font Set (English/Japanese – Western European (K) – Cyrillic (R))

8 = Colour: Y: Yellow W: White B: Blue R: Red G: Green RGB: Full Colour

9 = Interface: P: Parallel I: I²C S: SPI M: Multi

10 = **Voltage Variant:** e.g. **3** = 3v

F/Displays/Midas Brand/Midas NEW OLED Part Number System 18 June 2013 2011.doc $\,$

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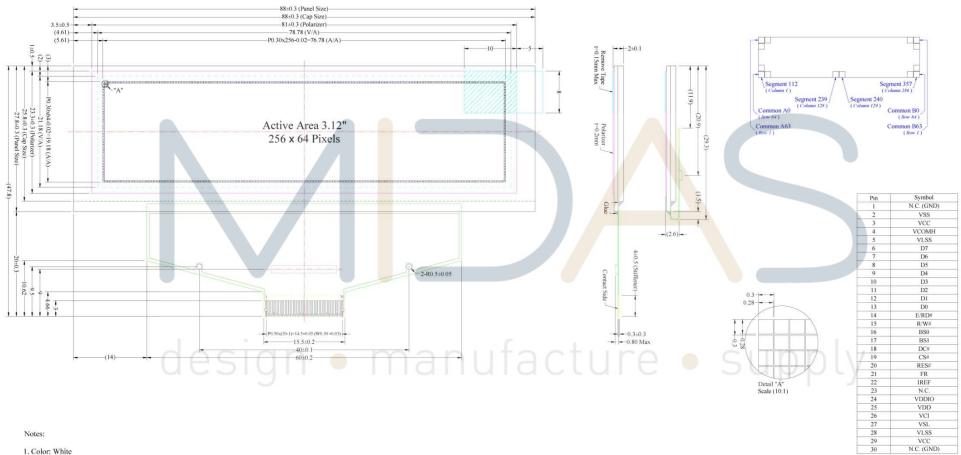
Functions and Features

- 256X64 Graphic
- Built-in controller
- viewing angle Free
- Wide Temperature -40° C $\sim +85^{\circ}$ C (Operating)
- RoHS compliant

Mechanical Specification

Item	Description		
Product No.	MCOT256064BA-GM		
Inch	3.12"		
Color	Green		
Active Area	76.78(W)×19.18(H)	mm	
Panel Size	88.00(W)× <mark>2</mark> 7.80(H)×2.00(D)	mm	
Dot Size	0.28(W)×0 <mark>.2</mark> 8(H)	mm	
Dot Pitch	0.3(W)×0.3(H)	mm	
Display Format	256×64		
Duty Ratio	1/64 Duty	Duty	
Controller	SSD1322 or Equivalent		
Operation Temperature	-40~85 Hallulactule Supply	°C	
Storage Temperature	-40~90		
Response Time	≤10	us	
Assembly	Connector		

Mechanical Drawing



- 2. Driver IC: SSD1322 3. Die Size: 12374um x 1526um
- 4. COF Number: SSD1322U
- 5. Interface:

8-bit 68XX/80XX Parallel, 3-/4-wire SPI

- 6. General Tolerance: ±0.30
- 7. The total thickness (2.10 Max) is without polarizer protective film & remove tape. The actual assembled total thickness with above materials should be 2.35 Max.

Pin Description

Power Supply

Pin Number	Symbol	Туре	Function			
			Power Supply for Operation			
26	VCI		This is a voltage supply pin. It must be connected to external source &			
			always be equal to or higher than VDD & VDDIO.			
			Power Supply for Core Logic Circuit			
25	VDD		This is a voltage supply pin. It can be supplied externally (within the range			
23	VDD		of 2.4~2.6V) or regulated internally from VCI. A capacitor should be			
			connected between this pin & VSS under all circumstances.			
			Power Supply for I/O Pin			
			This pin is a power supply pin of I/O buffer. It should be connected to			
24	VDDIO		VDD or external source. All I/O signal should have VIH reference to			
		Р	VDDIO. When I/O signals pins (BS0~BS1, D0~D7, control signals) pull			
			high, they should be connected to VDDIO.			
			Ground of Logic Circuit			
2	VSS		Thi <mark>s i</mark> s a ground pin. It also acts as a reference for the logic pins. It must			
			be <mark>co</mark> nnected to external ground.			
			Power Supply for OEL Panel			
3,29	VCC		These are the most positive voltage supply pin of the chip. They must be			
			connected to external source.			
	desi	gn •	Ground of Analog Circuit			
5,28	VLSS		These are the analog ground pins. They should be connected to VSS			
			externally.			

Driver

Pin Number	Symbol	Туре	Function
			Current Reference for Brightness Adjustment
22	IREF	I	This pin is segment current reference pin. A resistor should be connected
			between this pin and VSS. Set the current lower than 10uA.
			Voltage Output High Level for COM Signal
4	VCOMH	Р	This pin is the input pin for the voltage output high level for COM signals.
			A tantalum capacitor should be connected between this pin and VSS.
			Voltage Output Low Level for SEG Signal
27	VCI	Р	This is segment voltage reference pin. When external VSL is not used,
27	VSL	Р	this pin should be left open. When external VSL is used, this pin should
			connect with resistor and diode to ground.

Testing Pads

Pin Number	Symbol	Туре	Function
			Current Reference for Brightness Adjustment
21	FR	0	This pin is segment current reference pin. A resistor should be connected
			between this pin and VSS. Set the current lower than 10uA.

Interface

Pin Number	Symbol	Туре	Function							
			Communicating Protocol Sele	ct						
			These pins are MCU interface se	election input. See th	e following table:					
16	BS0			BS1	BS2					
17	BS1		3-wire SPI	1	0					
17	БОТ		4-wire SPI	0	0					
			8-bit 68xx Parallel	1	1					
			8-bit 80xx Parallel	0	1					
			Power Reset for Controller an	d Driver						
20	RES#		This pin is reset signal input. Wh	nen the pin is low, init	ialization of the chip					
			is executed.							
			Chip Select							
19	CS#		This pin is the chip select input.	The chip is enabled f	or MCU					
			communication only when CS# is pulled low.							
			Data/Command Control							
			This pin is Data/Command control pin. When the pin is pulled high, the							
18	D/C#		inp <mark>ut</mark> at D7~D0 is treated as di <mark>spl</mark> ay data. When the pin is pulled low, the							
10	D/C#		input at D7~D0 will be transferred to the command register. For detail							
		1	relationship to MCU interface signals, please refer to the Timing							
			Ch <mark>ar</mark> acteristics Diagram <mark>s.</mark>							
			Read/Write Enable or Read							
			This pin is MCU interface input.	When interfacing to a	a 68XX-series					
	desi		microprocessor, this pin will be used as the Enable (E) signal. Read/write							
14	E/RD#	E/RD#	operation is initiated when this pin is pulled high and the CS# is pulled							
	2,11011		ow. When connecting to an 80XX-microprocessor, this pin receives the							
			Read (RD#) signal. Data read operation is initiated when this pin is pulled							
			low and CS# is pulled low. When serial mode is selected, this pin must be							
			connected to VSS.							
			Read/Write Select or Write							
			This pin is MCU interface input. When interfacing to a 68XX-series							
			microprocessor, this pin will be used as Read/Write (R/W#) selection							
15	R/W#		input. Pull this pin to "High" for read mode and pull it to "Low" for write							
	,		mode. When 80XX interface mode is selected, this pin will be the Write							
			(WR#) input. Data write operation is initiated when this pin is pulled low							
			and the CS# is pulled low. When serial or I2C mode is selected, this pin							
			must be connected to VSS.							

			Host Data Input/output Bus
			These pins are 8-bit bi-directional data bus to be connected to the
6~13	D7~D0	I/O	microprocessor's data bus. When serial mode is selected, D1 will be the
			serial data input SDIN and D0 will be the serial clock input SCLK. Unused
			pins must be connected to VSS except for D2 in serial mode.

Reserve

Pin Number	Symbol	Туре	Function
			Reserved Pin
23	N.C.	-	The N.C. pin between function pins is reserved for compatible and flexible
			design.
	N.C.		Reserved Pin (Supporting Pin)
1,30	(GND)	-	The supporting pins can reduce the influences from stresses on the
	(GIND)		function pins. These pins must be connected to external ground.



Block Diagram



MCU Interface Selection: BS0 and BS1

Pins connected to MCU interface: D7~D0, E/RD#, R/W#, D/C#, CS#, and RES#

C1, C3, C5: 0.1μ F C2, C4: 4.7μ F C6: 10μ F C7: 1μ F

C8: 4.7μ F / 25V Tantalum Capacitor

R1: $680k\Omega\Box$, R1 = (Voltage at IREF – VSS) / IREF

DC Characteristics

Item	Symbol	Condition	Min.	Туре	Max.	Unit
Supply Voltage for Operation	Vcı		2.4	2.8	3.5	Volt
Supply Voltage for Logic	Vdd		2.4	2.5	2.6	Volt
Supply Voltage for I/O Pins	VDDIO		1.65	1.8	VCI	Volt
Supply Voltage for Display	Vcc	Note 3	11.5	12	12.5	Volt
Operating Current for VCI	Icı		-	1.8	2.25	mA
Operating Current for VCC	loo	Note 4	-	26.3	32.9	mA
Operating Current for VCC	Icc	Note 5	-	41.1	51.4	mA
Sleep Mode Current for VCI	Icı,SLEEP		-	1	5	μΑ
Sleep Mode Current for VCC	Icc,SLEEP		-	1	5	μΑ

Note 3: Brightness (Lbr) and Supply Voltage for Display (VCC) are subject to the change of the panel characteristics and the customer's request.

Note 4: VCI = 2.8V, VCC = 12V, 50% Display Area Turn on.

Note 5: VCI = 2.8V, VCC = 12V, 100% Display Area Turn on.

Optical Characteristics

Item	Symbol	Conditions	Min.	Тур	Max.	Unit	
Brightness(Green)	Lbr	-		120	-	cd/m²	
C.I.E. (Green)	(X)	C.I.E	0.27	0.31	0.35		
	(Y)	C.I.E	0.58	0.62	0.66		
Dark Room Contrast	CR	-	-	>10000:1	-		
Viewing anglerange	-	-	-	Free	-	Degree	

^{*} Optical measurement taken at VDD = 2.8V, VCC = 12V.

Absolute Maximum rating

Item	Symbol	Min.	Тур.	Max.	Unit	Notes
Supply Voltage for Operation	VCI	-0.3	-	4	Volt	1,2
Supply Voltage for Logic	VDD	-0.5	-	2.75	Volt	1,2
Supply Voltage for I/O Pins	VDDIO	-0.5	-	VCI	Volt	1,2
Supply Voltage for Display	Vcc	-0.5	-	16	Volt	1,2
Life Time (60 cd/m²)			100,000		Hour	

Note 1: All the above voltages are on the basis of "VSS = 0V".

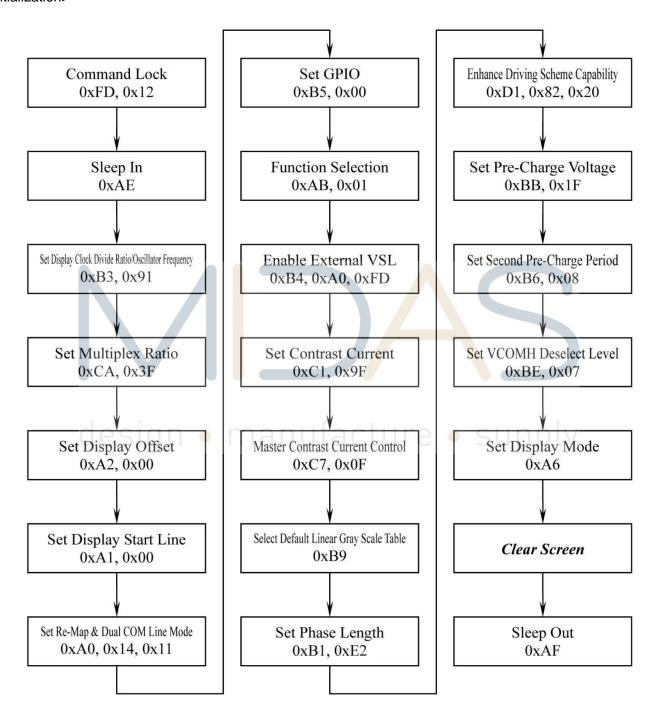
Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section. "Optics". If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

AC Characteristics Please refer "SSD1322 specification. design • manufacture • supply

Actual Application Example

Command usage and explanation of an actual example

<Initialization>



If the noise is accidentally occurred at the displaying window during the operation, please reset the display in order to recover the display function.